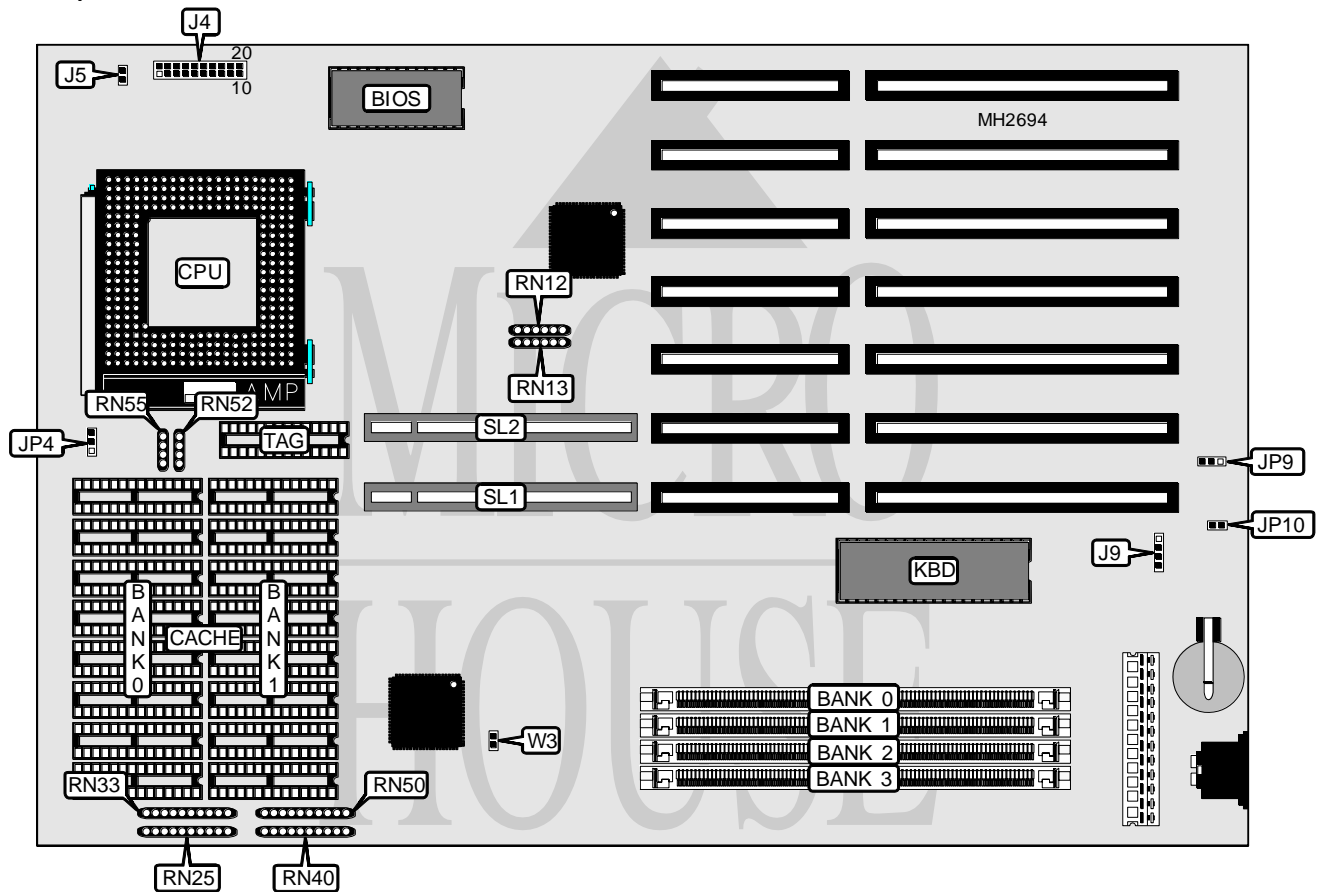


# TMC RESEARCH CORPORATION PAT45PVS (VER.2.0)

<b>Processor</b>	Pentium
<b>Processor Speed</b>	60/66MHz
<b>Chip Set</b>	Unidentified
<b>Max. Onboard DRAM</b>	128MB
<b>Cache</b>	256/512KB
<b>BIOS</b>	Unidentified
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	32-bit VESA local bus slots (2)
<b>NPU Options</b>	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J4 pins 1 - 4	Power LED & keylock	J4 pins 11 - 15
Turbo LED	J4 pins 8 & 18	External battery	J9
Reset switch	J4 pins 9 & 19	32-bit VESA local bus slots	SL1 - SL2
IDE interface LED	J4 pins 10 & 20		

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# TMC REASERCH CORPORATION

## PAT45PVS (VER.2.0)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Battery type select internal	JP9	pins 2 & 3 closed
Battery type select external	JP9	Open
CMOS memory clear	JP9	pins 1 & 2 closed
í Monitor type select color	JP10	Closed
Monitor type select monochrome	JP10	Open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
4MB	(1) 1M x 36	NONE	NONE	NONE
8MB	(1) 1M x 36	NONE	(1) 1M x 36	NONE
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
8MB	(1) 2M x 36	NONE	NONE	NONE
12MB	(1) 1M x 36	NONE	(1) 2M x 36	NONE
16MB	(1) 1M x 36	NONE	(1) 1M x 36	(1) 2M x 36
16MB	(1) 1M x 36	(1) 1M x 36	(1) 2M x 36	NONE
16MB	(1) 2M x 36	NONE	(1) 2M x 36	NONE
16MB	(1) 4M x 36	NONE	NONE	NONE
20MB	(1) 1M x 36	NONE	(1) 2M x 36	(1) 2M x 36
20MB	(1) 1M x 36	NONE	(1) 4M x 36	NONE
24MB	(1) 1M x 36	(1) 1M x 36	(1) 2M x 36	(1) 2M x 36
24MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	NONE
24MB	(1) 2M x 36	NONE	(1) 2M x 36	(1) 2M x 36
24MB	(1) 2M x 36	NONE	(1) 4M x 36	NONE
32MB	(1) 4M x 36	NONE	(1) 4M x 36	NONE
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
32MB	(1) 8M x 36	NONE	NONE	NONE
36MB	(1) 1M x 36	NONE	(1) 8M x 36	NONE
40MB	(1) 1M x 36	NONE	(1) 1M x 36	(1) 8M x 36
40MB	(1) 1M x 36	(1) 1M x 36	(1) 8M x 36	NONE
40MB	(1) 2M x 36	NONE	(1) 8M x 36	NONE
44MB	(1) 1M x 36	NONE	(1) 2M x 36	(1) 8M x 36
48MB	(1) 1M x 36	(1) 1M x 36	(1) 2M x 36	(1) 8M x 36
48MB	(1) 2M x 36	NONE	(1) 2M x 36	(1) 8M x 36
48MB	(1) 4M x 36	NONE	(1) 8M x 36	NONE
52MB	(1) 1M x 36	NONE	(1) 4M x 36	(1) 8M x 36
56MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 8M x 36
56MB	(1) 2M x 36	NONE	(1) 4M x 36	(1) 8M x 36
64MB	(1) 4M x 36	NONE	(1) 4M x 36	(1) 8M x 36
64MB	(1) 4M x 36	(1) 4M x 36	(1) 8M x 36	NONE
64MB	(1) 8M x 36	NONE	(1) 8M x 36	NONE
64MB	(1) 16M x 36	NONE	NONE	NONE
68MB	(1) 1M x 36	NONE	(1) 8M x 36	(1) 8M x 36
72MB	(1) 1M x 36	(1) 1M x 36	(1) 8M x 36	(1) 8M x 36
72MB	(1) 2M x 36	NONE	(1) 8M x 36	(1) 8M x 36
80MB	(1) 4M x 36	NONE	(1) 8M x 36	(1) 8M x 36

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# TMC REASERCH CORPORATION

## PAT45PVS (VER.2.0)

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DRAM CONFIGURATION (CON'T)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
96MB	(1) 4M x 36	(1) 4M x 36	(1) 8M x 36	(1) 8M x 36
96MB	(1) 8M x 36	NONE	(1) 8M x 36	(1) 8M x 36
96MB	(1) 16M x 36	NONE	(1) 8M x 36	NONE
128MB	(1) 16M x 36	NONE	(1) 8M x 36	(1) 8M x 36

DRAM JUMPER CONFIGURATION		
Setting	RN12	RN13
All SIMM types installed	Installed	Not installed
16M x 36 SIMM only intalled	Not installed	Installed

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
256KB	(8) 8K x 8	(8) 8K x 8	(1) 8K x 8
512KB	(8) 32K x 8	(8) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION			
Size	JP4	RN52	RN55
256KB	Open	Installed	Not installed
512KB	pins 2 & 3 closed	Not installed	Installed

VESA BUS SPEED CONFIGURATION				
Speed	RN25	RN33	RN40	RN50
25MHz	Installed	Not installed	Not installed	Not installed
33MHz	Not installed	Installed	Not installed	Not installed
40MHz	Not installed	Not installed	Installed	Not installed
50iMHz	Installed	Not installed	Not installed	Not installed
50MHz	Not installed	Not installed	Not installed	Installed
66iMHz	Not installed	Installed	Not installed	Not installed

VESA WAIT STATE CONFIGURATION	
Wait states	W3
0 wait states	Open
1 wait state	Closed