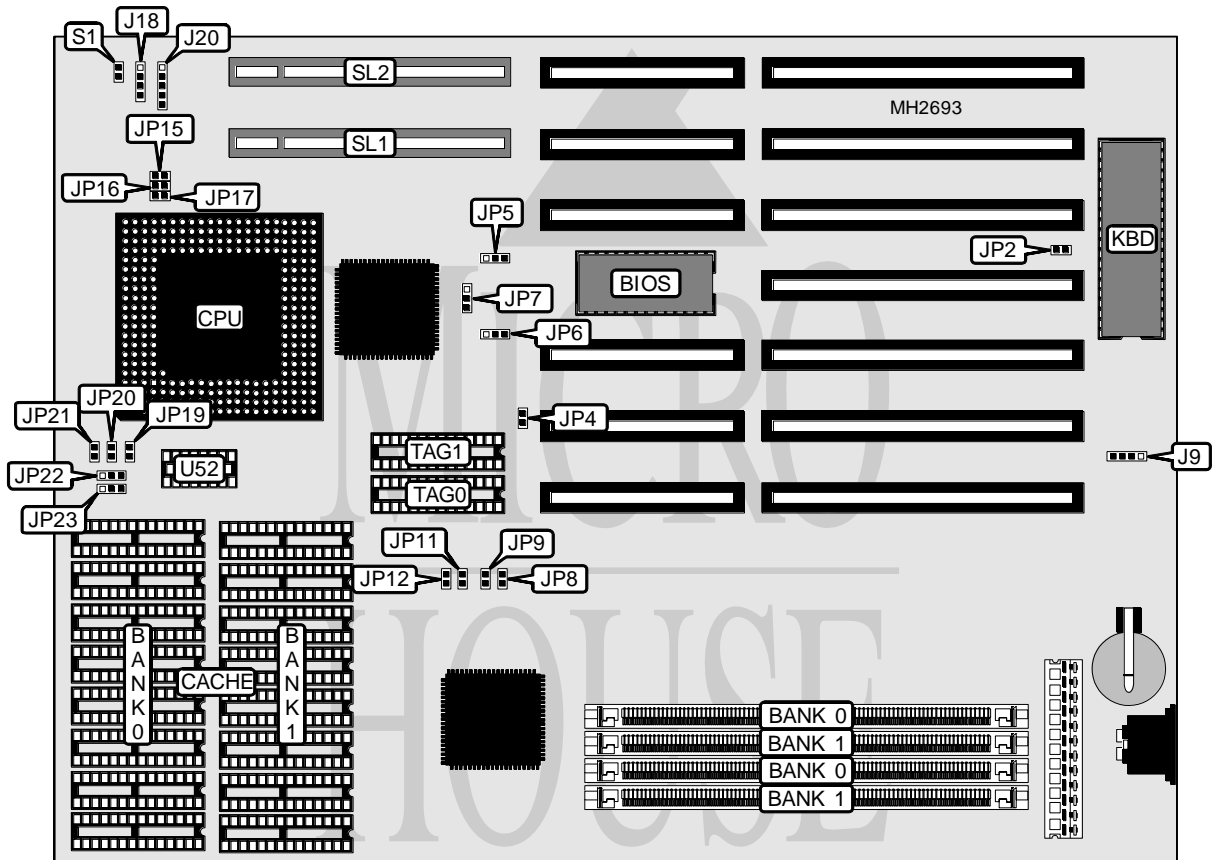


SHUTTLE COMPUTER INTERNATIONAL, INC. HOT-503

Processor	Pentium
Processor Speed	60/66MHz
Chip Set	OPT1
Max. Onboard DRAM	128MB
Cache	64/128/256/512KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J18	Reset switch	S1
Power LED & keylock	J20	32-bit VESA local bus slots	SL1 - SL2

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Battery type select internal	J9	pins 2 & 3 closed
Battery type select external	J9	Open
í Factory configured - do not alter	JP2	N/A
í LDEV# type select end of first T2	JP11	Open
LDEV# type select end of second T2	JP11	Closed
í Local bus enabled	JP12	Open
Local bus disabled	JP12	Closed
í Factory configured - do not alter	JP16	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
2MB	(2) 256K x 36	NONE
4MB	(2) 512K x 26	NONE
4MB	(2) 256K x 36	(2) 256K x 36
6MB	(2) 256K x 36	(2) 512K x 36
8MB	(2) 512K x 36	(2) 512K x 36
8MB	(2) 1M x 36	NONE
12MB	(2) 512K x 36	(2) 1M x 36
16MB	(2) 1M x 36	(2) 1M x 36
16MB	(2) 2M x 36	NONE
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 2M x 36	(2) 2M x 36
32MB	(2) 4M x 36	NONE
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 4M x 36	(2) 4M x 36
64MB	(2) 8M x 36	NONE
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION					
Size	Bank 0	Bank 1	TAG0	TAG1	U52
64KB	(8) 8K x 8	NONE	(1) 8K x 8	NONE	(1) 16K x 1
128KB	(8) 8K x 8	(8) 8K x 8	(1) 8K x 8	NONE	(1) 16K x 1
256KB	(8) 32K x 8	NONE	(1) 8K x 8	NONE	(1) 16K x 1
512KB	(8) 32K x 8	(8) 32K x 8	(1) 8K x 8	(1) 8K x 8	(1) 16K x 1

CACHE JUMPER CONFIGURATION						
Size	JP6	JP19	JP20	JP21	JP22	JP23
64KB	1 & 2	Open	Open	Open	1 & 2	1 & 2
128KB	2 & 3	Closed	Open	Open	2 & 3	2 & 3
256KB	1 & 2	Closed	Closed	Open	1 & 2	1 & 2
512KB	2 & 3	Closed	Closed	Closed	2 & 3	2 & 3

Note: Pins designated should be in closed position.

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VESA WAIT STATE CONFIGURATION	
Wait states	JP15
0 wait states	Closed
1 wait state	Open

BUS SPEED CONFIGURATION	
CPU speed	JP17
<= 33MHz	Open
> 33MHz	Closed

VESA LOCAL BUS CONFIGURATION			
Setting	JP4	JP5	JP7
External	Closed	pins 1 & 2 closed	Open
Internal	Open	pins 2 & 3 closed	pins 2 & 3 closed

LCLK CONFIGURATION		
LCLK	JP8	JP9
LCLK/2	Open	Open
LCLK/3	Open	Closed
LCLK/4	Closed	Open
LCLK/5	Closed	Closed