MODULAR CIRCUIT TECHNOLOGY
J D R - V 4 8 6 - S X 3 3 / D X 3 3 / D X 2 6 6

Processor	80486SX/80486DX/80486DX2
Processor Speed	25/33/66(internal)MHz
Chip Set	UMC
Max. Onboard DRAM	32MB
Cache	128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	None
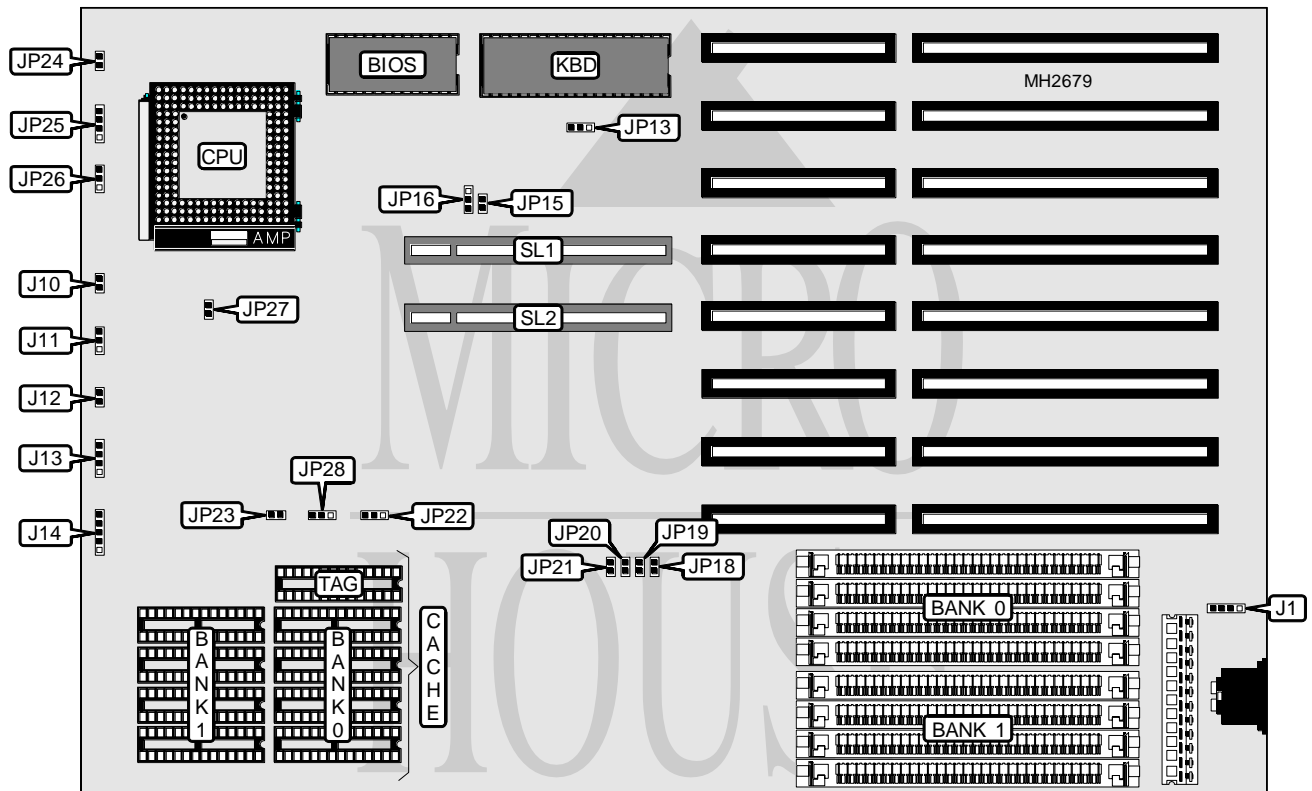
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Speaker	J13
Turbo LED	J10	Power LED & keylock	J14
Turbo switch	J11	32-bit VESA local bus slots	SL1 - SL2
Reset switch	J12		

MODULAR CIRCUIT TECHNOLOGY
J D R - V 4 8 6 - S X 3 3 / D X 3 3 / D X 2 6 6

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP13	pins 2 & 3 closed
Monitor type select monochrome	JP13	pins 1 & 2 closed
í Factory configured - do not alter	JP24	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size	JP22	JP23	JP27	JP28
128KB	pins 2 & 3 closed	Open	Closed	pins 2 & 3 closed
256KB	pins 1 & 2 closed	Closed	Closed	pins 2 & 3 closed

CPU TYPE CONFIGURATION		
Type	JP25	JP26
80486SX	pins 1 & 2, 3 & 4 closed	Open
80486DX	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed
80486DX2	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed

CPU SPEED CONFIGURATION				
Speed	JP18	JP19	JP20	JP21
25MHz	Closed	Closed	Open	Open
33MHz	Open	Closed	Open	Open
66iMHz	Open	Closed	Open	Open

VESA WAIT STATE CONFIGURATION	
Wait states	JP16
0 wait states	pins 1 & 2 closed
1 wait state	pins 2 & 3 closed

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MODULAR CIRCUIT TECHNOLOGY
J D R - V 4 8 6 - S X 3 3 / D X 3 3 / D X 2 6 6

BUS SPEED CONFIGURATION	
CPU speed	JP15
<= 33MHz	Open
> 33MHz	Closed