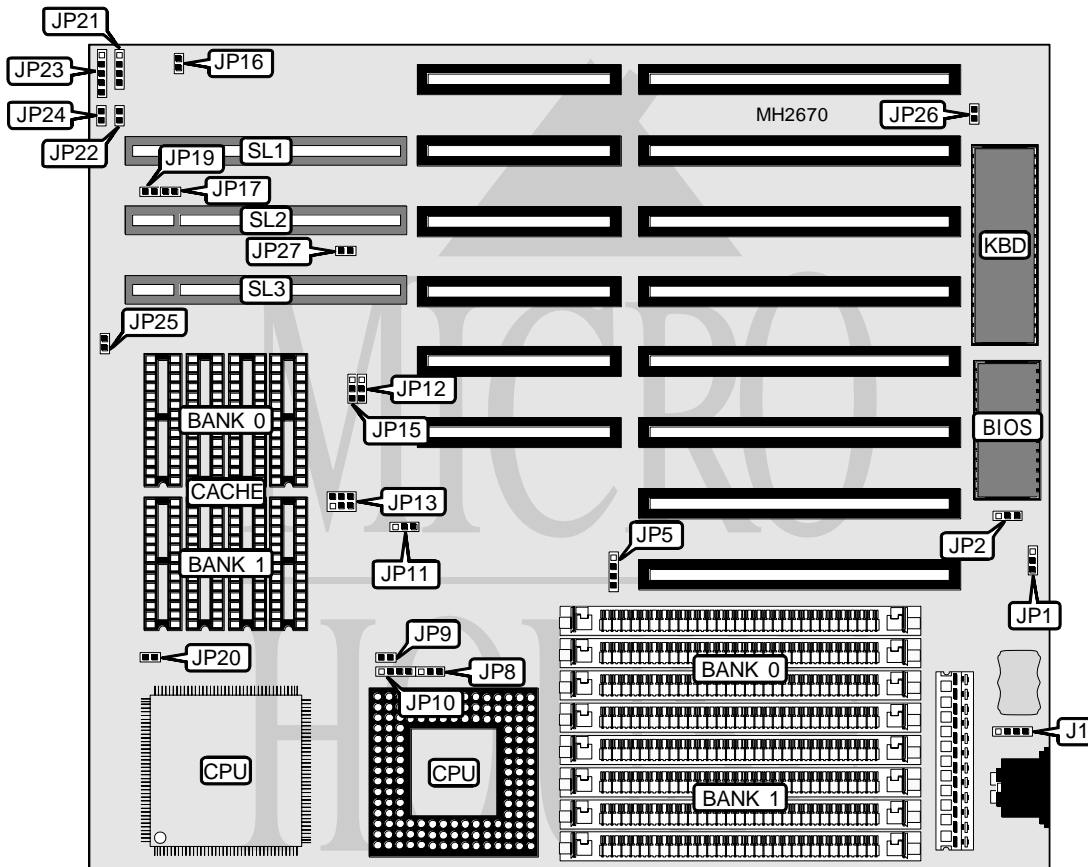


EDOM INTERNATIONAL CORPORATION

486VL3H (MV020)

Processor	CX486M6/80486SX/80487SX/80486DX/80486DX2/Pentium Overdrive
Processor Speed	25/33/40/50(internal)/50/66(internal)MHz
Chip Set	Unidentified
Max. Onboard DRAM	64MB
Cache	64/128/256KB
BIOS	AMI/Award
Dimensions	250mm x 220mm
I/O Options	32-bit VESA local bus slots (3)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Power LED & keylock	JP23
Reset switch	JP16	Turbo LED	JP24
Speaker	JP21	32-bit VESA local bus slots	SL1 - SL3
Turbo switch	JP22		

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486VL3H (MV020)

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 1 & 2 closed
CMOS memory clear	JP1	pins 2 & 3 closed
í Factory configured - do not alter	JP2	pins 1 & 2 closed
í Factory configured - do not alter	JP7	pins 2 & 3 closed
í Factory configured - do not alter	JP9	Open
í Factory configured - do not alter	JP18	pins 1 & 2 closed
í PQFP CPU disabled	JP20	Open
PQFP CPU enabled	JP20	Closed
í Monitor type select monochrome	JP26	Open
Monitor type select color	JP26	Closed
í Factory configured - do not alter	JP27	pins 1 & 2 closed
í Factory configured - do not alter	P1	Open

Note: The loactions of JP7, JP18 & P1 are unidentified.

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
6MB	(4) 256K x 9	(4) 256K x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

Note: The location of the TAG chip is unidentified.

CACHE JUMPER CONFIGURATION				
Size	JP5	JP12	JP15	JP25
64KB	Open	1 & 2	Open	Open
128KB	3 & 4	Open	pins 2 & 3 closed	Closed
256KB	1 & 2, 3 & 4	Open	pins 1 & 2 closed	Closed

Note: Pins designated should be in the closed position.

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CPU TYPE CONFIGURATION		
Type	JP8	JP10
80486SX	Open	pins 2 & 3 closed
80487SX	pins 2 & 3 closed	pins 1 & 2, 3 & 4 closed
80486DX	pins 1 & 2 closed	pins 1 & 2, 3 & 4 closed
80486DX2	pins 1 & 2 closed	pins 1 & 2, 3 & 4 closed

CPU SPEED CONFIGURATION	
Speed	JP13
25MHz	pins 1 & 2, 5 & 6 closed
33MHz	pins 1 & 2, 3 & 4 closed
40MHz	pins 5 & 6 closed
50iMHz	pins 1 & 2, 5 & 6 closed
50MHz	pins 3 & 4 closed
66iMHz	pins 1 & 2, 3 & 4 closed

VESA WAIT STATE CONFIGURATION	
Wait states	JP17
0 wait states	Open
1 wait state	Closed

BUS SPEED CONFIGURATION		
CPU speed	JP11	JP19
<= 33MHz	pins 2 & 3 closed	Open
> 33MHz	pins 1 & 2 closed	Closed