## EDOM INTERNATIONAL CORPORATION 486PIG (MP042)

**Processor** 80486SX/CX486M7/UMC U5S/80486DX/80486DX2/80486DX4/P24D/ Pentium Overdrive

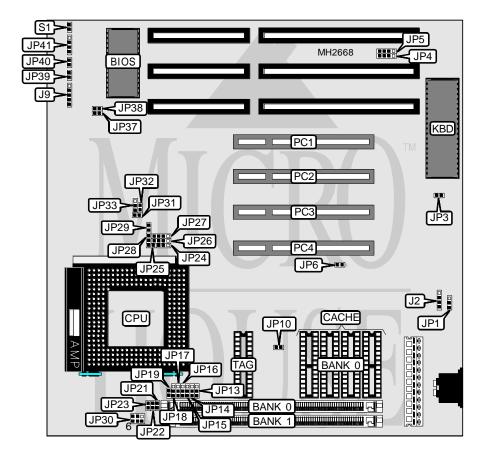
Processor Speed 25/33/40/50(internal)/50/66(internal)/75(internal)/100(internal)MHz

Chip SetOPTIMax. Onboard DRAM128MBCache128/512KBBIOSAward

**Dimensions** 220mm x 190mm

I/O Options 32-bit PCI slots (4), green PC connector

NPU Options None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Turbo switch	JP40
Power LED & keylock	J9	Speaker	JP41
Green PC connector	JP3	32-bit PCI slots	PC1 - PC4
Green PC LED	JP38	Reset switch	S1
Turbo LED	JP39		

Continued on next page. . .

## **EDOM INTERNATIONAL CORPORATION** 486PIG (MP042)

. . . continued from previous page

USER CONFIGUR	ABLE SETTINGS	
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 2 & 3 closed
CMOS memory clear	JP1	pins 1 & 2 closed
í CPU write type select write back	JP17	pins 1 & 2 closed
CPU write type select write through	JP17	pins 2 & 3 closed
í CPU clock speed >33MHz	JP27	pins 2 & 3 closed
CPU clock speed =<33MHz	JP27	pins 3 & 4 closed
í PCI asynchronous with CPU	JP33	pins 1 & 2 closed
PCI synchronous with CPU	JP33	pins 2 & 3 closed
í Factory configured - do not alter	JP37	N/A

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
2MB	(1) 512K x 36	NONE
4MB	(1) 1M x 36	NONE
4MB	(1) 512K x 36	(1) 512K x 36
8MB	(1) 1M x 36	(1) 1M x 36
8MB	(1) 2M x 36	NONE
12MB	(1) 1M x 36	(1) 2M x 36
16MB	(1) 4M x 36	NONE
16MB	(1) 2M x 36	(1) 2M x 36
20MB	(1) 1M x 36	(1) 4M x 36
32MB	(1) 4M x 36	(1) 4M x 36
64MB	(1) 16M x 36	NONE
128MB	(1) 16M x 36	(1) 16M x 36

CACHE CONFIGURATION		
Size	Bank 0	TAG
128KB	(4) 32K x 8	(1) 8K x 8
512KB	(4) 128K x 8	(1) 32K x 8

I	CACHE JUMPER CONFIGURATION			
Size JP13			JP19	JP22
128KB pins 2 & 3 closed		Open	Open	
ſ	512KB	pins 1 & 2 closed	Closed	Closed

Continued on next page. . .

## **EDOM INTERNATIONAL CORPORATION** 486PIG (MP042)

. . . continued from previous page

	С	PU TYPE CONFIGURATIO	N	
Туре	JP14	JP15	JP21	JP23
80486SX	pins 1 & 2 closed	Open	Open	Open
CX486M7	pins 1 & 2 closed	pins 1 & 2 closed	Closed	Closed
UMC U5S	pins 1 & 2 closed	Open	Open	Open
80486DX	pins 1 & 2 closed	Open	Open	Open
80486DX2	pins 1 & 2 closed	Open	Open	Open
80486DX4	pins 1 & 2 closed	Open	Open	Open
P24D	pins 1 & 2 closed	pins 2 & 3 closed	Closed	Open
Pentium Overdrive	pins 2 & 3 closed	Open	Open	Open

	CPU TYPE CONFIGURATION			
Type JP24 JP25		JP26	JP29	
80486SX	Open	Open	pins 2 & 3 closed	Open
CX486M7	Open	Open	pins 1 & 2, 3 & 4 closed	Open
UMC U5S	pins 1 & 2 closed	Open	pins 1 & 2, 3 & 4 closed	Open
80486DX	pins 1 & 2 closed	Open	pins 1 & 2, 3 & 4 closed	Open
80486DX2	pins 1 & 2 closed	Open	pins 1 & 2, 3 & 4 closed	Open
80486DX4	pins 1 & 2 closed	Open	pins 1 & 2, 3 & 4 closed	Open
P24D	pins 1 & 2 closed	Closed	pins 1 & 2, 3 & 4 closed	Open
Pentium Overdrive	pins 1 & 2 closed	Open	pins 1 & 2, 3 & 4 closed	Open

CPU TYPE CONFIGURATION				
Туре	JP6	JP10	JP18	JP28
Cyrix	N/A	N/A	pins 2 & 3 closed	N/A
SL-enhanced	Closed	Closed	pins 1 & 2 closed	Closed
Non SL-enhanced	Open	Open	Open	Open

CPU SPEED CONFIGURATION			
Speed	JP31	JP32	
25MHz	Open	Open	
33MHz	Closed	Closed	
40MHz	Open	Closed	
50iMHz	Open	Open	
50MHz	Closed	Open	
66iMHz	Closed	Closed	
75iMHz	Open	Open	
100iMHz	Closed	Closed	

CPU SPEED CONFIGURATION (80486DX4 ONLY)		
Speed JP16		
2x	pins 1 & 2 closed	
2.5x	pins 2 & 3 closed	
3x	Open	

Continued on next page. . .

## **EDOM INTERNATIONAL CORPORATION** 486PIG (MP042)

. . . continued from previous page

CPU VOLTAGE CONFIGURATION	
Voltage JP30	
3.45v	pins 3 & 5, 4 & 6 closed
5v	pins 1 & 3, 2 & 4 closed

PCI TRIGGER CONFIGURATION		
Setting	JP4	JP5
Edge trigger	pins 1 & 2, 3 & 4 closed	pins 1 & 2, 3 & 4 closed
Level trigger	pins 2 & 3 closed	pins 2 & 3 closed