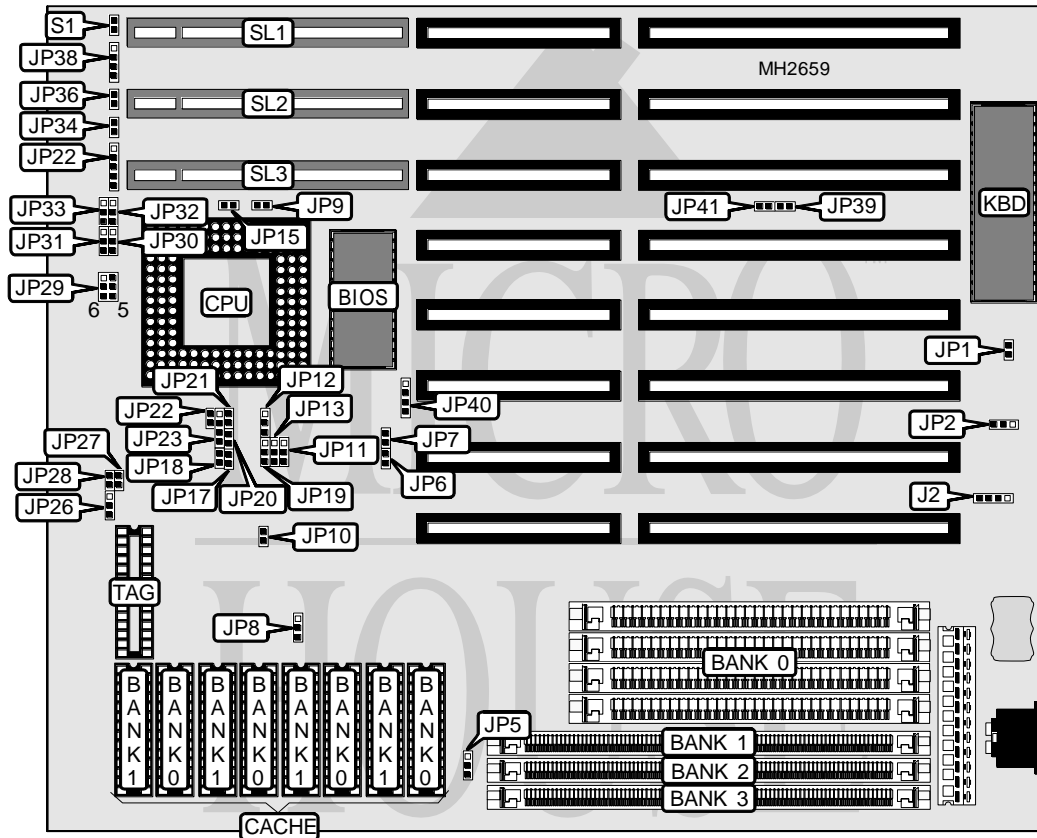


BEK-TRONIC TECHNOLOGY, INC.

BEK-V429

| | |
|--------------------------|--|
| Processor | CX486M6/SL80486SX/80486SX/CX486M7/SL80486DX/80486DX/SL80486DX/80486DX2/80486DX4/P24D/Pentium Overdrive |
| Processor Speed | 25/33/40/50(internal)/50/66(internal)/75(internal)/100(internal)MHz |
| Chip Set | OPTI |
| Max. Onboard DRAM | 128MB |
| Cache | 64/128/256KB |
| BIOS | Award |
| Dimensions | 330mm x 218mm |
| I/O Options | 32-bit VESA local bus slots (3), green PC connector |
| NPU Options | None |



| CONNECTIONS | | | |
|---------------------|----------|------------------------------|-----------|
| Purpose | Location | Purpose | Location |
| External battery | J2 | Speaker | JP38 |
| Green PC connector | JP6 | Green PC connector (monitor) | JP39 |
| Power LED & keylock | JP22 | Green PC connector LED | JP41 |
| Turbo LED | JP34 | Reset switch | S1 |
| Turbo switch | JP36 | 32-bit VESA local bus slots | SL1 - SL3 |

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| USER CONFIGURABLE SETTINGS | | |
|--|--------|--------------------------|
| Function | Jumper | Position |
| í Monitor type select color | JP1 | Closed |
| Monitor type select monochrome | JP1 | Open |
| í CMOS memory normal operation | JP2 | pins 2 & 3 closed |
| CMOS memory clear | JP2 | pins 1 & 2 closed |
| Battery type select external | JP2 | Open |
| í Factory configured - do not alter | JP7 | Closed |
| í Factory configured - do not alter | JP30 | N/A |
| í Factory configured - do not alter | JP31 | pins 2 & 3 closed |
| í LRDY wait state select 0 wait states | JP33 | pins 2 & 3 closed |
| LRDY wait state select 1 wait state | JP33 | pins 1 & 2 closed |
| í Factory configured - do not alter | JP40 | pins 1 & 2, 3 & 4 closed |

| DRAM CONFIGURATION | | | | |
|--------------------|--------------|---------------|---------------|---------------|
| Size | Bank 0 | Bank 1 | Bank 2 | Bank 3 |
| 1MB | (4) 256K x 9 | NONE | NONE | NONE |
| 1MB | NONE | (1) 256K x 36 | NONE | NONE |
| 2MB | (4) 256K x 9 | (1) 256K x 36 | NONE | NONE |
| 4MB | (4) 1M x 9 | NONE | NONE | NONE |
| 4MB | (4) 256K x 9 | (1) 256K x 36 | (1) 256K x 36 | (1) 256K x 36 |
| 4MB | NONE | (1) 1M x 36 | NONE | NONE |
| 5MB | (4) 256K x 9 | (1) 1M x 36 | NONE | NONE |
| 6MB | (4) 256K x 9 | (1) 256K x 36 | (1) 1M x 36 | NONE |
| 8MB | (4) 1M x 9 | (1) 1M x 36 | NONE | NONE |
| 8MB | (4) 1M x 9 | NONE | (1) 1M x 36 | NONE |
| 8MB | NONE | (1) 1M x 36 | (1) 1M x 36 | NONE |
| 10MB | (4) 256K x 9 | (1) 256K x 36 | (1) 1M x 36 | (1) 1M x 36 |
| 12MB | (4) 1M x 9 | NONE | (1) 1M x 36 | (1) 1M x 36 |
| 12MB | NONE | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 |
| 16MB | (4) 4M x 9 | NONE | NONE | NONE |
| 16MB | (4) 1M x 9 | (1) 1M x 36 | (1) 1M x 36 | (1) 1M x 36 |
| 16MB | NONE | (1) 4M x 36 | NONE | NONE |
| 17MB | (4) 256K x 9 | (1) 4M x 36 | NONE | NONE |
| 20MB | (4) 1M x 9 | (1) 4M x 36 | NONE | NONE |
| 20MB | (4) 1M x 9 | NONE | (1) 4M x 36 | NONE |
| 20MB | NONE | (1) 1M x 36 | (1) 4M x 36 | NONE |
| 32MB | (4) 4M x 9 | (1) 4M x 36 | NONE | NONE |
| 32MB | (4) 4M x 9 | NONE | (1) 4M x 36 | NONE |
| 32MB | NONE | (1) 4M x 36 | (1) 4M x 36 | NONE |
| 64MB | (4) 16M x 9 | NONE | NONE | NONE |
| 64MB | (4) 4M x 9 | (1) 4M x 36 | (1) 4M x 36 | (1) 4M x 36 |
| 64MB | NONE | (1) 16M x 36 | NONE | NONE |
| 128MB | (4) 16M x 9 | (1) 16M x 36 | NONE | NONE |
| 128MB | (4) 16M x 9 | NONE | (1) 16M x 36 | NONE |
| 128MB | NONE | (1) 16M x 36 | (1) 16M x 36 | NONE |

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| DRAM JUMPER CONFIGURATION | |
|---------------------------|-------------------|
| Size | JP5 |
| 1MB | pins 2 & 3 closed |
| 1MB | pins 1 & 2 closed |
| 2MB | pins 2 & 3 closed |
| 4MB | pins 2 & 3 closed |
| 4MB | pins 2 & 3 closed |
| 4MB | pins 1 & 2 closed |
| 5MB | pins 2 & 3 closed |
| 6MB | pins 2 & 3 closed |
| 8MB | pins 2 & 3 closed |
| 8MB | pins 2 & 3 closed |
| 8MB | pins 1 & 2 closed |
| 10MB | pins 2 & 3 closed |
| 12MB | pins 2 & 3 closed |
| 12MB | pins 1 & 2 closed |
| 16MB | pins 2 & 3 closed |
| 16MB | pins 2 & 3 closed |
| 16MB | pins 1 & 2 closed |
| 17MB | pins 2 & 3 closed |
| 20MB | pins 2 & 3 closed |
| 20MB | pins 2 & 3 closed |
| 20MB | pins 1 & 2 closed |
| 32MB | pins 2 & 3 closed |
| 32MB | pins 2 & 3 closed |
| 32MB | pins 1 & 2 closed |
| 64MB | pins 2 & 3 closed |
| 64MB | pins 2 & 3 closed |
| 64MB | pins 1 & 2 closed |
| 128MB | pins 2 & 3 closed |
| 128MB | pins 2 & 3 closed |
| 128MB | pins 1 & 2 closed |

| CACHE CONFIGURATION | | | |
|---------------------|-------------|-------------|----------------------------|
| Size | Bank 0 | Bank 1 | TAG |
| 64KB | (4) 8K x 8 | (4) 8K x 8 | (1) 8K x 8 |
| 128KB | (4) 32K x 8 | NONE | (1) 8K x 8 |
| 256KB | (4) 32K x 8 | (4) 32K x 8 | (1) 16K x 8 or (1) 32K x 8 |

| CACHE JUMPER CONFIGURATION | | | |
|----------------------------|-------------------|--------|--------|
| Size | JP8 | JP27 | JP28 |
| 64KB | pins 2 & 3 closed | Open | Open |
| 128KB | pins 1 & 2 closed | Closed | Open |
| 256KB | pins 2 & 3 closed | Closed | Closed |

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| CACHE TAG JUMPER CONFIGURATION | |
|--------------------------------|-------------------|
| Size | JP26 |
| 8K x 8 | pins 1 & 2 closed |
| 16K x 8 | pins 2 & 3 closed |
| 32K x 8 | pins 1 & 2 closed |

| CPU TYPE CONFIGURATION | | | | | |
|------------------------|--------|--------|-------|-------|-------|
| Type | JP9 | JP10 | JP11 | JP12 | JP13 |
| CX486M6 | Closed | Open | 1 & 2 | 2 & 3 | Open |
| SL80486SX | Open | Closed | 1 & 2 | 1 & 2 | Open |
| 80486SX | Open | Open | 1 & 2 | 1 & 2 | Open |
| CX486M7 (1x) | Closed | Open | 2 & 3 | 2 & 3 | 1 & 2 |
| CX486M7 (2x) | Closed | Open | 2 & 3 | 2 & 3 | 1 & 2 |
| SL80486DX | Open | Closed | 1 & 2 | 1 & 2 | 1 & 2 |
| 80486DX | Open | Open | 1 & 2 | 1 & 2 | 1 & 2 |
| SL80486DX2 | Open | Closed | 1 & 2 | 1 & 2 | 1 & 2 |
| 80486DX2 | Open | Open | 1 & 2 | 1 & 2 | 1 & 2 |
| 80486DX4 | Open | Closed | 1 & 2 | 1 & 2 | 1 & 2 |
| P24D (WB) | Open | Closed | 2 & 3 | 1 & 2 | 1 & 2 |
| P24D (WT) | Open | Closed | 2 & 3 | 1 & 2 | 1 & 2 |
| P24T | Open | Closed | 2 & 3 | 1 & 2 | 1 & 2 |

Note: Pins designated should be in the closed position.

| CPU TYPE CONFIGURATION (CON'T) | | | | | |
|--------------------------------|--------|-------|--------|--------|--------|
| Type | JP15 | JP19 | JP20 | JP21 | JP22 |
| CX486M6 | Open | Open | Open | Open | Open |
| SL80486SX | Open | Open | Closed | Closed | Open |
| 80486SX | Open | Open | Open | Open | Open |
| CX486M7 (1x) | Open | Open | Open | Open | Closed |
| CX486M7 (2x) | Open | Open | Open | Open | Closed |
| SL80486DX | Open | Open | Closed | Closed | Open |
| 80486DX | Open | Open | Open | Open | Open |
| SL80486DX2 | Open | Open | Closed | Closed | Open |
| 80486DX2 | Open | Open | Open | Open | Open |
| 80486DX4 | Open | Open | Closed | Closed | Open |
| P24D (WB) | Closed | 1 & 2 | Closed | Closed | Open |
| P24D (WT) | Closed | 2 & 3 | Closed | Closed | Open |
| P24T | Closed | Open | Closed | Closed | Open |

Note: Pins designated should be in the closed position.

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| CPU TYPE CONFIGURATION (CON'T) | | | |
|--------------------------------|--------------------------|--------------------------|-------------------|
| Type | JP23 | JP29 | JP32 |
| CX486M6 | pins 2 & 3 closed | pins 1 & 3, 2 & 4 closed | pins 1 & 2 closed |
| SL80486SX | pins 2 & 3 closed | pins 1 & 3, 2 & 4 closed | Open |
| 80486SX | pins 2 & 3 closed | pins 1 & 3, 2 & 4 closed | Open |
| CX486M7 (1x) | pins 1 & 2, 3 & 4 closed | pins 1 & 3, 2 & 4 closed | Open |
| CX486M7 (2x) | pins 1 & 2, 3 & 4 closed | pins 1 & 3, 2 & 4 closed | Open |
| SL80486DX | pins 1 & 2, 3 & 4 closed | pins 1 & 3, 2 & 4 closed | Open |
| 80486DX | pins 1 & 2, 3 & 4 closed | pins 1 & 3, 2 & 4 closed | Open |
| SL80486DX2 | pins 1 & 2, 3 & 4 closed | pins 1 & 3, 2 & 4 closed | Open |
| 80486DX2 | pins 1 & 2, 3 & 4 closed | pins 1 & 3, 2 & 4 closed | Open |
| 80486DX4 | pins 1 & 2, 3 & 4 closed | pins 3 & 5, 4 & 6 closed | Open |
| P24D (WB) | pins 1 & 2, 3 & 4 closed | pins 1 & 3, 2 & 4 closed | pins 2 & 3 closed |
| P24D (WT) | pins 1 & 2, 3 & 4 closed | pins 1 & 3, 2 & 4 closed | pins 2 & 3 closed |
| P24T | pins 1 & 2, 3 & 4 closed | pins 1 & 3, 2 & 4 closed | pins 2 & 3 closed |

| CPU SPEED CONFIGURATION | | |
|-------------------------|--------|--------|
| Speed | JP17 | JP18 |
| 25MHz | Open | Open |
| 33MHz | Closed | Closed |
| 40MHz | Closed | Open |
| 50iMHz | Open | Open |
| 50MHz | Open | Closed |
| 66iMHz | Closed | Closed |
| 75iMHz | Open | Open |
| 100iMHz | Closed | Closed |

| VESA WAIT STATE CONFIGURATION | |
|---|--------|
| Wait states | JP35 |
| 0 wait states | Open |
| 1 wait state | Closed |
| Note: The location of JP35 is unidentified. | |

| BUS SPEED CONFIGURATION | |
|---|--------|
| CPU speed | JP37 |
| <= 33MHz | Open |
| > 33MHz | Closed |
| Note: The location of JP37 is unidentified. | |