PEACOCK AG
80486SX/DX/DX2 (SYMPHONY)

Processor	80486SX/80486DX/80486DX2
Processor Speed	25/33/50(internal)/50/66(internal)MHz
Chip Set	Symphony
Max. Onboard DRAM	32MB
Cache	64/128/256/1024KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	Floppy drive interface, IDE interface, parallel port, serial ports (2)
NPU Options	4167 (not present on all boards)
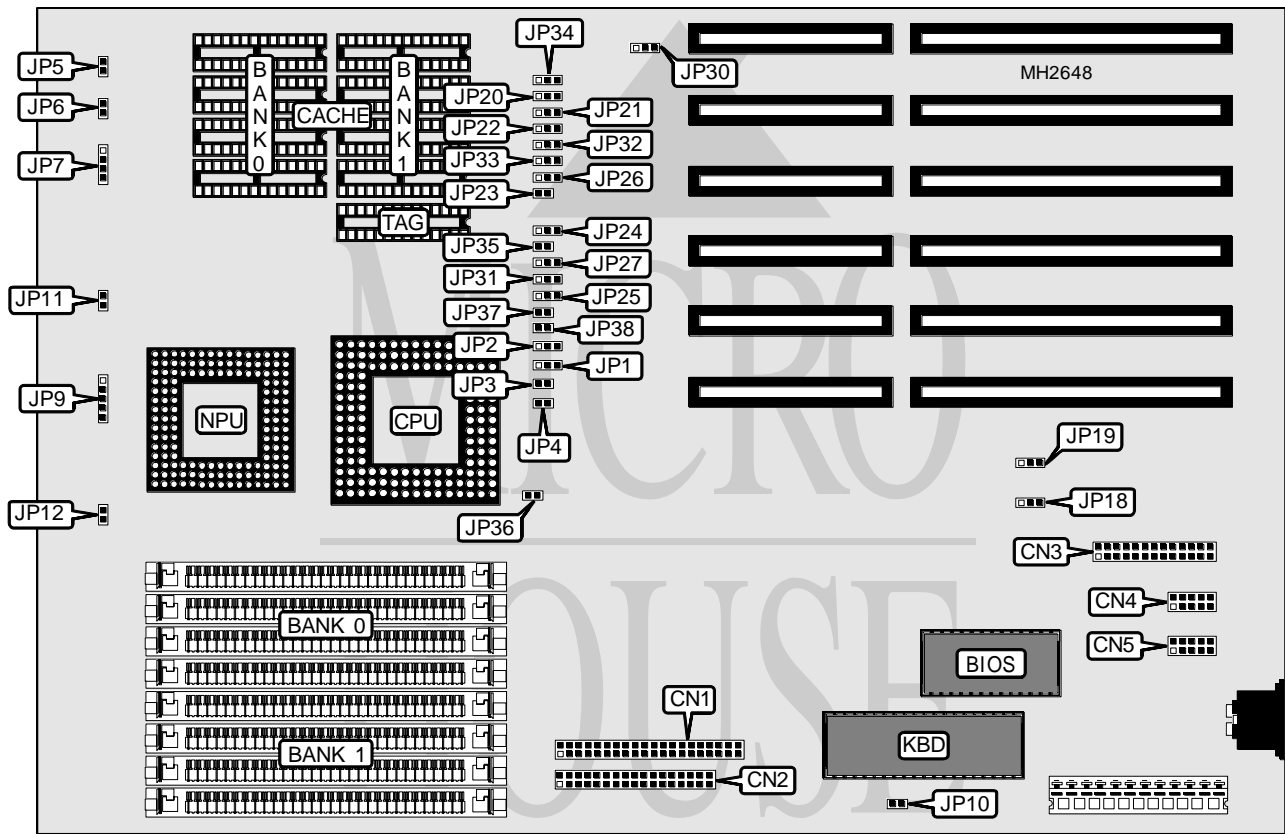
CONNECTIONS			
Purpose	Location	Purpose	Location
IDE interface	CN1	Turbo LED	JP5
Floppy drive interface	CN2	Speaker	JP7
Parallel port	CN3	Power LED & keylock	JP9
Serial port 2	CN4	Reset switch	JP11
Serial port 1	CN5	IDE interface LED	JP12

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í I/O bus speed select CPUCLK=CCLK2	JP2	pins 1 & 2 closed
I/O bus speed select CPUCLK=PCLK	JP2	pins 2 & 3 closed
í Cache support enabled	JP6	Open
Cache support disabled	JP6	Closed
í Monitor type select color	JP10	Closed
Monitor type select monochrome	JP10	Open
í On board I/O enabled	JP18	pins 2 & 3 closed
On board I/O disabled	JP18	pins 1 & 2 closed
í Parallel port IRQ sleect IRQ7	JP19	pins 2 & 3 closed
Parallel port IRQ sleect IRQ5	JP19	pins 1 & 2 closed
í Cache controller timing select 1	JP30	pins 1 & 2 closed
Cache controller timing select 2	JP30	pins 2 & 3 closed
í CPU oscillator matches CPU speed	JP36	Closed
CPU oscillator matches 2x CPU speed	JP36	Open
í CPU algorithm select 3-2-2-2	JP37	Closed
CPU algorithm select 2-1-1-1	JP37	Open
í Cache wait state select 0 wait states	JP38	Open
Cache wait state select 1 wait state	JP38	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
1MB	(4) 128K x 8	(4) 128K x 8	(1) 128K x 8

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CACHE JUMPER CONFIGURATION						
Size	JP20	JP21	JP22	JP23	JP24	JP25
64KB	2 & 3	2 & 3	2 & 3	Open	2 & 3	2 & 3
128KB	1 & 2	Open	1 & 2	Closed	1 & 2	1 & 2
256KB	2 & 3	1 & 2	2 & 3	Open	2 & 3	2 & 3
1MB	2 & 3	1 & 2	2 & 3	Open	2 & 3	2 & 3
Note: Pins designated should be in the closed positon.						

CACHE JUMPER CONFIGURATION (CON'T)						
Size	JP26	JP27	JP31	JP32	JP33	JP34	JP35
64KB	2 & 3	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2	Closed
128KB	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	Closed
256KB	2 & 3	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2	Closed
1MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	Closed
Note: Pins designated should be in the closed positon.						

CPU TYPE CONFIGURATION			
Type	JP1	JP3	JP4
80486SX	pins 2 & 3 closed	Open	Open
80486DX	pins 1 & 2 closed	Closed	Closed
80486DX2	pins 1 & 2 closed	Closed	Closed