PEACOCK AG
EDITION/DLC 1 80486

Processor	80386DX/CX486DLC/80486SX/80487SX/80486DX/80486DX2
Processor Speed	33/66(internal)MHz
Chip Set	UMC
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	254mm x 218mm
I/O Options	32-bit VESA local bus slot
NPU Options	80387DX
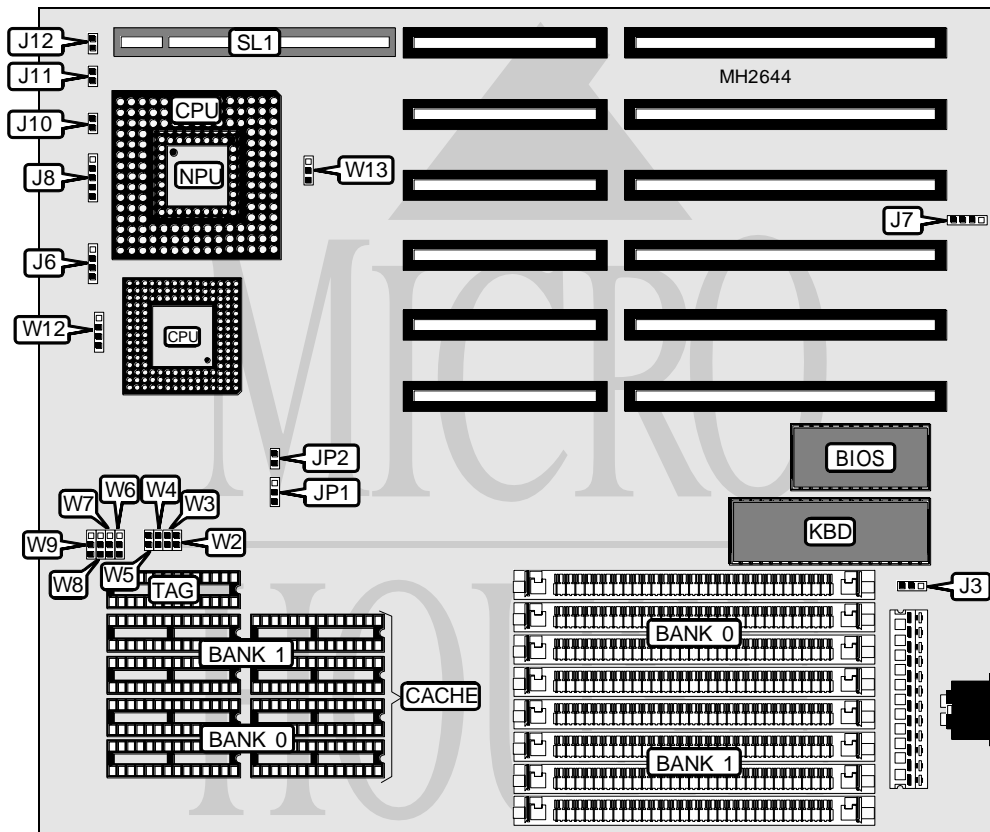
CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J6	Turbo LED	J11
External battery	J7	Reset switch	J12
Power LED & keylock	J8	32-bit VESA local bus slot	SL1
Turbo switch	J10		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select monochrome	J3	pins 2 & 3 closed
Monitor type select color	J3	pins 1 & 2 closed
í NPU disabled	JP2	Open
NPU enabled	JP2	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION								
Size	W2	W3	W4	W5	W6	W7	W8	W9
64KB	Open	Open	Open	Open	1 & 2	Open	Open	1 & 2
128KB	Open	Open	Closed	Closed	2 & 3	1 & 2	2 & 3	1 & 2
256KB	Closed	Closed	Closed	Closed	1 & 2	2 & 3	2 & 3	1 & 2
Note: Pins designated should be in the closed position.								

CPU TYPE CONFIGURATION			
Type	JP1	W12	W13
80386DX	pins 1 & 2 closed	N/A	N/A
CX486DLC	pins 1 & 2 closed	N/A	N/A
80486SX	pins 2 & 3 closed	pins 2 & 3 closed	Open
80487SX	pins 2 & 3 closed	pins 1 & 2, 3 & 4 closed	pins 2 & 3 closed
80486DX	pins 2 & 3 closed	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed
80486DX2	pins 2 & 3 closed	pins 2 & 3 closed	Open