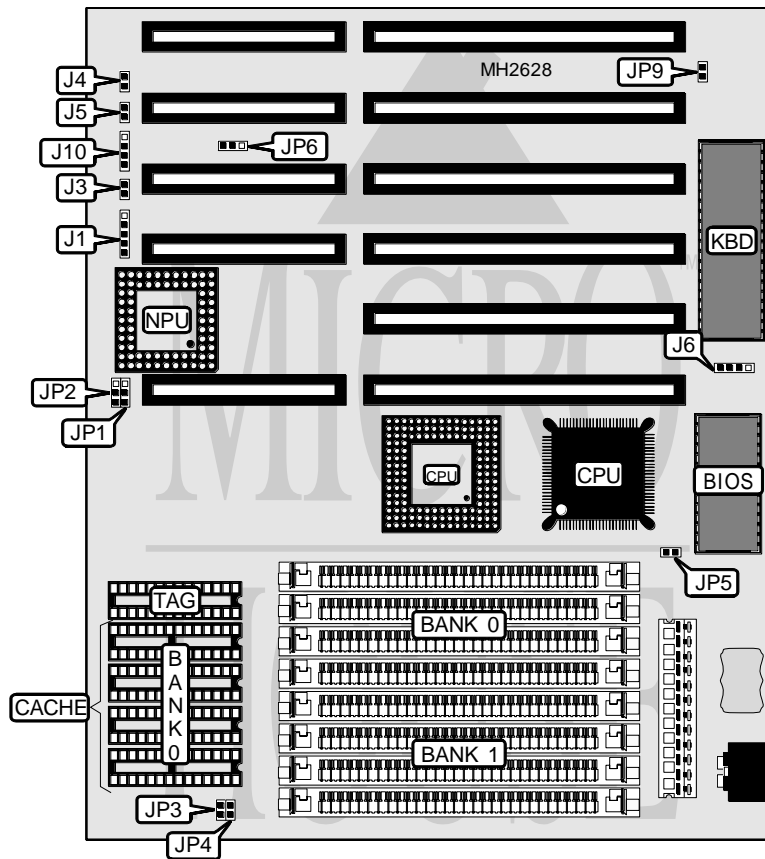


UNIDENTIFIED I3U4913

Processor	80386DX/CX486DLC
Processor Speed	33/40MHz
Chip Set	UMC
Max. Onboard DRAM	32MB
Cache	32/128KB
BIOS	AMI
Dimensions	220mm x 170mm
I/O Options	None
NPU Options	80387DX



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J1	Turbo LED	J5
Reset switch	J3	External battery	J6
Turbo switch	J4	Speaker	J10

Continued on next page. . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í PQFP CPU enabled	JP5	Open
PQFP CPU disabled	JP5	Closed
í NPU disabled	JP6	pins 1 & 2 closed
NPU enabled	JP6	pins 2 & 3 closed
í Monitor type select monochrome	JP9	Open
Monitor type select color	JP9	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION		
Size	Bank 0	TAG
32KB	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	(1) 8K x 8

CACHE JUMPER CONFIGURATION		
Size	JP3	JP4
32KB	Open	Open
128KB	Closed	Closed

CPU SPEED CONFIGURATION		
Speed	JP1	JP2
33MHz	pins 1 & 2 closed	pins 2 & 3 closed
40MHz	pins 1 & 2 closed	pins 1 & 2 closed