Processor	80386DX/80486SX/80487SX/80486DX/80486DX2
Processor Speed	25/33/40/50(internal)/50/66(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	32MB
Cache	64/256KB
BIOS	Unidentified
Dimensions	338mm x 112mm
I/O Options	Floppy drive interface, IDE interface, parallel port, serial ports (2), riser slot
NPU Options	80387DX



CONNECTIONS				
Purpose	Location	Purpose	Location	
Power LED & keylock	J1	Parallel port	19	
Speaker	J2	External battery	J11	
Reset switch	J3	Auxiliary keyboard connector	J12	
IDE interface LED	J4	Serial port 1	J13	
Turbo switch	J5	Serial port 2	J14	
Floppy drive interface	J6	iSBX bus slot	SL1	
IDE interface	J7			

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USER CONFIGURABLE SETTINGS				
Function	Jumper	Position		
í Turbo enabled	J5	Open		
Turbo disabled	J5	Closed		
í Activate NMI to reset CPU when watch dog times out	JP14	pins 2 & 3 closed		
Reset system when watch dog times out	JP14	pins 1 & 2 closed		
Watch dog disabled	JP14	Open		
í Time out period select 500ms	JP15	pins 1 & 2 closed		
Time out period select 1 second	JP15	pins 2 & 3 closed		
í COM2 IRQ select IRQ4	JP18	pins 9 & 10 closed		
COM2 IRQ select IRQ5	JP18	pins 11 & 12 closed		
í Parallel port IRQ select IRQ7	JP18	pins 13 & 14 closed		
Parallel port IRQ select IRQ5	JP18	pins 15 & 16 closed		
í IRQ14 buffered	JP19	pins 1 & 2 closed		
IRQ14 not buffered	JP19	pins 2 & 3 closed		
í IDE interface pin 34 used	JP20	Closed		
IDE interface pin 34 not used	JP20	Open		
í IDE interface enabled	JP21	pins 2 & 3 closed		
IDE interface disabled	JP21	pins 1 & 2 closed		
í Floppy drive interface enabled	JP28	pins 2 & 3 closed		
Floppy drive interface disabled	JP28	pins 1 & 2 closed		
í CMOS memory normal operation	JP29	pins 2 & 3 closed		
CMOS memory clear	JP29	pins 1 & 2 closed		
í Monitor type select color	JP30	Closed		
Monitor type select monochrome	JP30	Open		
í Factory configured - do not alter	JP31	N/A		
í Factory configured - do not alter	JP32	N/A		
í Factory configured - do not alter	JP33	N/A		

	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

		CACHE CONFIGURATION		
Size	Bank 0	DIRTY	TAG 0	TAG 1
64KB	(8) 8K x 8	(1) 16K x 1	(1) 8K x 8	NONE
256KB	(8) 32K x 8	(1) 16K x 1	(1) 8K x 8	(1) 8K x 8

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CACHE JUMPER CONFIGURATION						
Size	JP1	JP2	JP3	JP4	JP5	JP6
64KB	2&3	Open	Open	Open	Open	2&3
256KB	2&3	Closed	Closed	Closed	Closed	2&3
Note: Pins designated should be in the closed position.						

CPU TYPE CONFIGURATION JP9 JP10 Type JP7 JP8 JP11 JP12 JP13 N/A N/A 80386DX 1&2 N/A N/A Open Open 80486SX 2&3 2&3 Open Open 2&3 Open Closed 80487SX 2&3 1&2 2&3 Closed Closed 2&3 Open 80486DX 2&3 1&2 1&2 Closed Open Closed 2&3 8048DX-50 2&3 1&2 1&2 1&2 Closed Closed Closed 80486DX2 2&3 1&2 1&2 Closed Open Closed 2&3 Pins designated should be in the closed position. Note:

PARALLEL PORT CONFIGURATION				
Setting	JP26	JP27		
278	pins 2 & 3 closed	pins 1 & 2 closed		
378	pins 1 & 2 closed	pins 2 & 3 closed		
3BC	pins 2 & 3 closed	pins 2 & 3 closed		
Disabled	pins 1 & 2 closed	pins 1 & 2 closed		

SERIAL PORT 1 CONFIGURATION				
Setting	JP22	JP25		
2E8	pins 2 & 3 closed	pins 2 & 3 closed		
3E8	pins 1 & 2 closed	pins 2 & 3 closed		
3F8	pins 2 & 3 closed	pins 1 & 2 closed		
Disabled	pins 1 & 2 closed	pins 1 & 2 closed		

SERIAL PORT 2 CONFIGURATION				
Setting	JP23	JP24		
2E8	pins 1 & 2 closed	pins 2 & 3 closed		
2F8	pins 2 & 3 closed	pins 1 & 2 closed		
3E8	pins 2 & 3 closed	pins 2 & 3 closed		
Disabled	pins 1 & 2 closed	pins 1 & 2 closed		

SERIAL PORT 2 IRQ CONFIGURATION			
IRQ	JP18		
IRQ3	pins 3 & 4 closed		
IRQ4	pins 1 & 2 closed		
IRQ5	pins 5 & 6 closed		

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ISBX SLOT DMA CONFIGURATION				
Setting	JP16	JP17		
DACK5/DRQ5	pins 1 & 2 closed	pins 1 & 2 closed		
DACK6/DRQ6	pins 3 & 4 closed	pins 3 & 4 closed		
DACK7/DRQ7	pins 5 & 6 closed	pins 5 & 6 closed		
Note: The location of jumper JP16 is unidentified.				