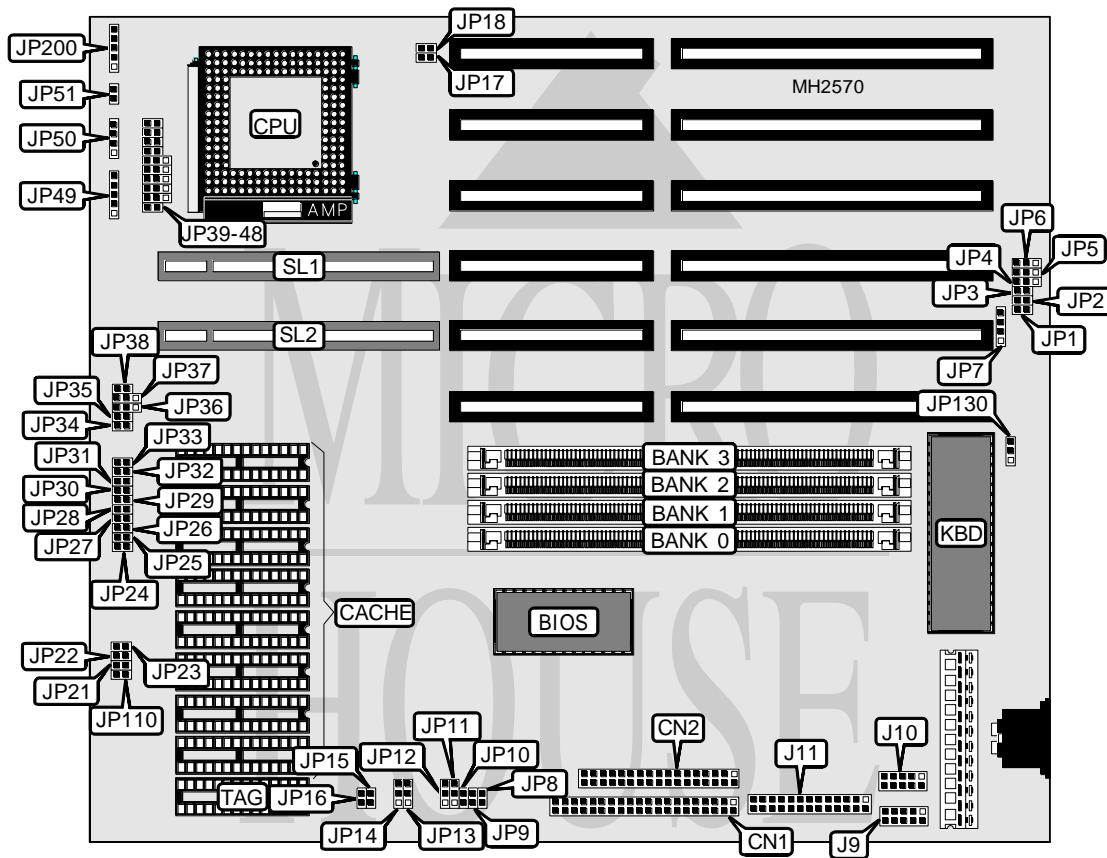


MITAC INTERNATIONAL CORPORATION

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Processor	80486SX/CXM7/80486DX/80486DX2/80486DX4/P23N/P24D/Pentium Overdrive
Processor Speed	25/33/40/50(internal)/50/66(internal)/75(internal)/100(internal)MHz
Chip Set	EFAR
Max. Onboard DRAM	64MB
Cache	64/128/256KB
BIOS	Award
Dimensions	254mm x 218mm
I/O Options	32-bit VESA local bus slots (2), floppy drive interface, IDE interface, parallel port, serial ports (2)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
IDE interface	CN1	Turbo switch	JP45
Floppy drive interface	CN2	Reset switch	JP46
Serial port 2	J9	Power LED & keylock	JP49
Serial port 1	J10	Speaker	JP50
Parallel port	J11	IDE interface LED	JP51
External battery	JP7	D/D board connector	JP200
Turbo LED	JP21	32-bit VESA local bus slots	SL1 & SL2

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP1	N/A
í Factory configured - do not alter	JP2	N/A
í Factory configured - do not alter	JP3	N/A
í CMOS memory normal operation	JP4	pins 2 & 3 closed
CMOS memory clear	JP4	pins 1 & 2 closed
í Factory configured - do not alter	JP5	N/A
í Factory configured - do not alter	JP6	N/A
í Parallel port IRQ select IRQ7	JP13	pins 2 & 3 closed
Parallel port IRQ select IRQ5	JP13	pins 1 & 2 closed
í Factory configured - do not alter	JP14	N/A
í Factory configured - do not alter	JP15	N/A
í Factory configured - do not alter	JP17	N/A
í Factory configured - do not alter	JP23	N/A
í Factory configured - do not alter	JP33	N/A
í Factory configured - do not alter	JP48	N/A
í BIOS type select normal	JP130	pins 1 & 2 closed
BIOS type select flash	JP130	pins 2 & 3 closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(1) 256K x 36	NONE	NONE	NONE
2MB	(1) 256K x 36	(1) 256K x 36	NONE	NONE
2MB	(1) 512K x 36	NONE	NONE	NONE
3MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	NONE
4MB	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36
4MB	(1) 512K x 36	NONE	(1) 512K x 36	NONE
4MB	(1) 1M x 36	NONE	NONE	NONE
5MB	(1) 256K x 36	(1) 1M x 36	NONE	NONE
6MB	(1) 512K x 36	NONE	(1) 1M x 36	NONE
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
8MB	(1) 2M x 36	NONE	NONE	NONE
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	NONE
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 2M x 36	NONE	(1) 2M x 36	NONE
16MB	(1) 4M x 36	NONE	NONE	NONE
20MB	(1) 1M x 36	(1) 4M x 36	NONE	NONE
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
32MB	(1) 8M x 36	NONE	NONE	NONE
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	NONE
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 8M x 36	NONE	(1) 8M x 36	NONE

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CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

Note: The orientation of the banks is unidentified. Bank 0 = U23, 25, 27, & 29. Bank 1 = U24, 26, 28, & 30.

CACHE JUMPER CONFIGURATION					
Size	JP8	JP9	JP10	JP11	JP12
64KB	Open	Open	Open	pins 1 & 2 closed	pins 1 & 2 closed
128KB	Closed	Closed	Open	pins 2 & 3 closed	pins 2 & 3 closed
256KB	Closed	Closed	Closed	pins 1 & 2 closed	pins 1 & 2 closed

CPU TYPE CONFIGURATION						
Type	JP16	JP18	JP24	JP27	JP28	JP29
80486SX	Closed	Open	Open	Open	Closed	Open
CX M7	Open	Closed	Closed	Closed	Open	Closed
80486DX	Closed	Open	Open	Open	Closed	Open
80486DX2	Closed	Open	Open	Open	Closed	Open
80486DX4	Closed	Open	Open	Open	Closed	Open
P23N	Closed	Open	Open	Open	Closed	Open
P24D	Open	Closed	Closed	Open	Closed	Open
P24T	Open	Closed	Closed	Open	Closed	Open

CPU TYPE CONFIGURATION (CON'T)						
Type	JP34	JP35	JP38	JP39	JP40	JP41
80486SX	Closed	Closed	Closed	Open	Open	2 & 3
CX M7	Open	Open	Open	Closed	2 & 3	1 & 2
80486DX	Closed	Closed	Closed	Closed	2 & 3	1 & 2
80486DX2	Closed	Closed	Closed	Closed	2 & 3	1 & 2
80486DX4	Closed	Closed	Closed	Closed	2 & 3	1 & 2
P23N	Closed	Closed	Closed	Closed	1 & 2	1 & 2
P24D	Open	Open	Open	Closed	2 & 3	1 & 2
P24T	Open	Open	Open	Closed	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

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CPU TYPE CONFIGURATION (CON'T)					
Type	JP42	JP43	JP44	JP47	JP110
80486SX	2 & 3	2 & 3	2 & 3	Open	Open
CX M7	1 & 2	1 & 2	1 & 2	Closed	Closed
80486DX	2 & 3	2 & 3	2 & 3	Open	Open
80486DX2	2 & 3	2 & 3	2 & 3	Open	Open
80486DX4	2 & 3	2 & 3	2 & 3	Open	Open
P23N	2 & 3	2 & 3	2 & 3	Open	Open
P24D	2 & 3	2 & 3	2 & 3	Closed	Open
P24T	2 & 3	2 & 3	2 & 3	Open	Open

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION (CX M7 ONLY)	
Type	JP25
CX486DX	Open
CX486DX2	Closed

CPU SPEED CONFIGURATION			
Speed	JP30	JP31	JP32
25MHz	Closed	Open	Open
33MHz	Closed	Closed	Closed
40MHz	Closed	Closed	Open
50iMHz	Closed	Open	Open
50MHz	Open	Open	Closed
66iMHz	Closed	Closed	Closed
75iMHz	Closed	Open	Open
100iMHz	Closed	Closed	Closed

CPU SPEED CONFIGURATION (80486DX4 ONLY)		
Speed	JP22	JP26
1x	N/A	Open
2x	Closed	N/A
2.5x	N/A	N/A
3x	Open	Closed

VESA WAIT STATE CONFIGURATION	
Wait states	JP37
0 wait states	pins 1 & 2 closed
1 wait state	pins 2 & 3 closed

BUS SPEED CONFIGURATION	
CPU speed	JP36
<= 33MHz	pins 1 & 2 closed
> 33MHz	pins 2 & 3 closed