UNIDENTIFIED 486SC-H

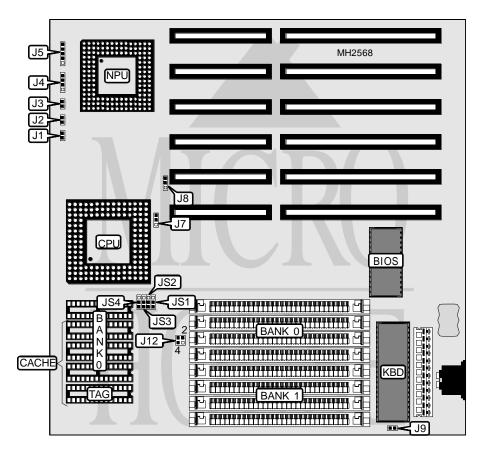
80486SX/80487SX/80486DX/80486DX2 **Processor**

Processor Speed 20/25/33/50(internal)MHz

Chip Set Unidentified Max. Onboard DRAM 32MB Cache 32/128KB **BIOS** AMI

Dimensions 220mm x 220mm

I/O Options None **NPU Options** 4167



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	J1	Speaker	J4
Turbo switch	J2	Power LED & keylock	J5
Reset switch	J3		

Continued on next page. . .

UNIDENTIFIED 486SC-H

. . . continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CPU clock speed select 1x	J8	pins 1 & 2 closed
CPU clock speed select 2x	J8	pins 2 & 3 closed
í Monitor type select monochrome/EGA/VGA	J9	Open
Monitor type select color	J9	Closed
í Cache wait state select 1 wait state	J12	Open
Cache wait state select 0 wait states	J12	pins 3 & 4 closed

	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

	CACHE CONFIGURATION	
Size	Bank 0	TAG
32KB	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	(1) 8K x 8

CACHE JUMPER CONFIGURATION				
Size	JS1	JS2	JS3	JS4
32KB	pins 1 & 2 closed			
128KB	pins 2 & 3 closed			

CPU TYPE CONFIGURATION		
Туре	J7	
80486SX	pins 1 & 2 closed	
80487SX	pins 2 & 3 closed	
80486DX	pins 2 & 3 closed	
80486DX2	pins 2 & 3 closed	