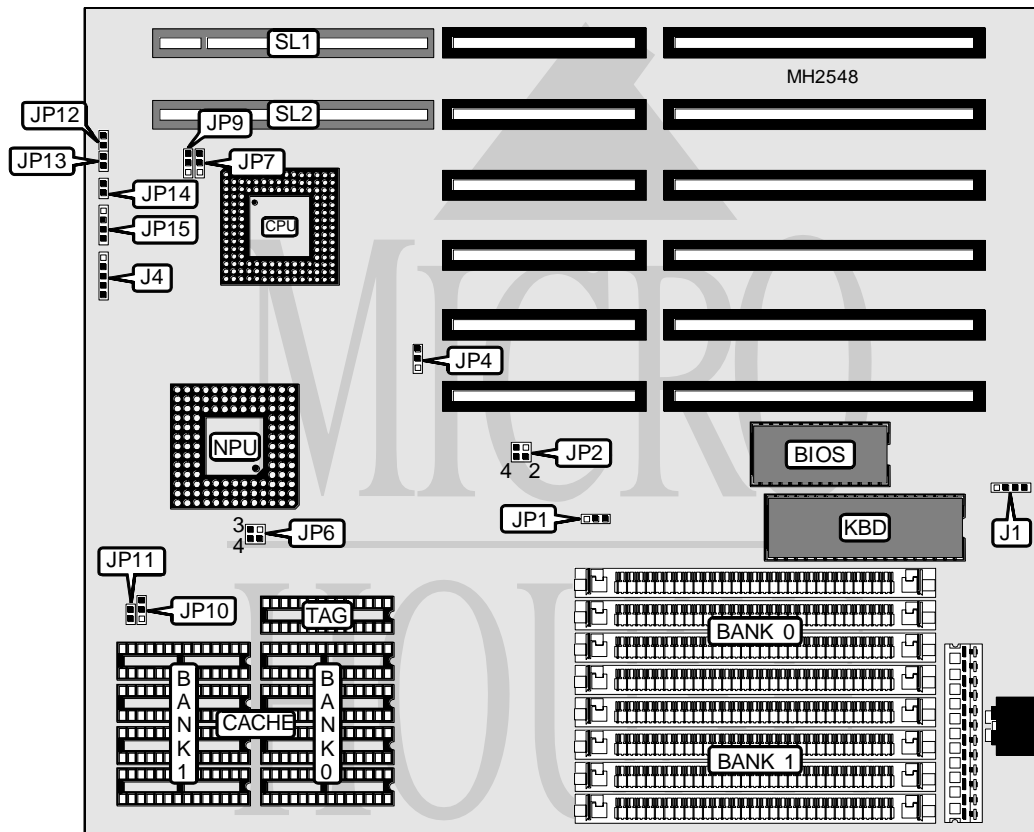


UNIDENTIFIED PANDA 386 V

Processor	AM386DX/AM386DXL/80386DX/TI486DLC/CX486DLC/TI486DLC2/CX486DLC2
Processor Speed	33/40/50MHz
Chip Set	Panda
Max. Onboard DRAM	128MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	254mm x 220mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	80387DX/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Reset switch	JP14
Power LED & keylock	J4	Speaker	JP15
Turbo LED	JP12	32-bit VESA local bus slots	SL1 & SL2
Turbo switch	JP13		

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DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
1MB	NONE	(4) 256K x 9
2MB	(4) 256K x 9	(4) 256K x 9
4MB	NONE	(4) 1M x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
16MB	NONE	(4) 4M x 9
17MB	(4) 4M x 9	(4) 256K x 9
17MB	(4) 256K x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE
64MB	NONE	(4) 16M x 9
65MB	(4) 16M x 9	(4) 256K x 9
65MB	(4) 256K x 9	(4) 16M x 9
68MB	(4) 16M x 9	(4) 1M x 9
68MB	(4) 1M x 9	(4) 16M x 9
80MB	(4) 16M x 9	(4) 4M x 9
80MB	(4) 4M x 9	(4) 16M x 9
128MB	(4) 16M x 9	(4) 16M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8 or (1) 32K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION			
Size	JP6	JP10	JP11
64KB	Open	pins 1 & 2 closed	Open
128KB	pins 1 & 2 closed	pins 2 & 3 closed	Closed
256KB	pins 1 & 2, 3 & 4 closed	pins 1 & 2 closed	Closed

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CPU TYPE CONFIGURATION		
Type	JP1	JP4
AM386DX	pins 2 & 3 closed	pins 1 & 2 closed
AM386DXL	pins 2 & 3 closed	pins 1 & 2 closed
80386DX	pins 2 & 3 closed	pins 1 & 2 closed
TI486DLC	pins 1 & 2 closed	pins 2 & 3 closed
CX486DLC	pins 1 & 2 closed	pins 2 & 3 closed
TI486DLC2	pins 1 & 2 closed	pins 2 & 3 closed
CX486DLC2	pins 1 & 2 closed	pins 2 & 3 closed

CPU CLOCK SPEED CONFIGURATION	
Speed	JP2
50MHz	Open
66MHz	pins 3 & 4 closed
80MHz	pins 1 & 2 closed

VESA WAIT STATE CONFIGURATION	
Wait states	JP9
0 wait states	pins 1 & 2 closed
1 wait state	pins 2 & 3 closed

BUS SPEED CONFIGURATION	
CPU speed	JP7
<= 33MHz	pins 1 & 2 closed
> 33MHz	pins 2 & 3 closed