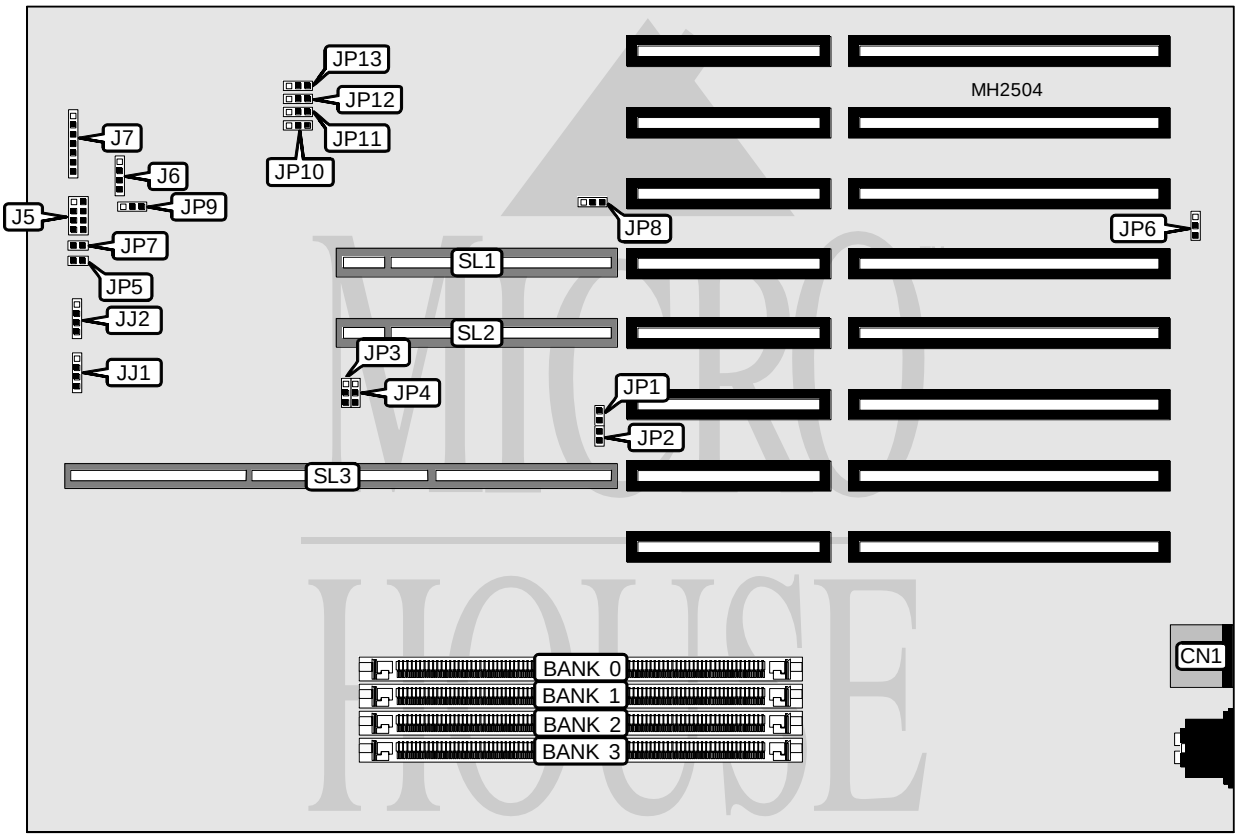
ACER, INC.
ACERPOWER425SE/433E/466DE (J3 PROJECT)

Processor	80486SX/80487SX/ODP486SX/80486DX/80486DX2/Pentium Overdrive/Pentium (depends on CPU card installed)
Processor Speed	25/33/40/50(internal)/50/66(internal)/66MHz
Chip Set	Unidentified
Max. Onboard DRAM	256MB
Cache	64/128/256/1024KB (depends on CPU card installed)
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	External CPU card, 32-bit VESA local bus slots (2), PS/2 mouse port
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse port	CN1	Chassis fan power	JJ1
SCSI interface LED	J5	Chassis fan power	JJ2
Speaker	J6	32-bit VESA local bus slots	SL1 & SL2
Optional mouse/keyboard	J7	External CPU slot	SL3

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP5	N/A
í Password disabled	JP6	pins 2 & 3 closed
Password enabled	JP6	pins 1 & 2 closed
í CMOS memory normal operation	JP7	Open
CMOS memory clear	JP7	Closed
í IRQ select IRQ11	JP8	pins 2 & 3 closed
IRQ select IRQ10	JP8	pins 1 & 2 closed
í Onboard buzzer enabled	JP9	pins 1 & 2 closed
External speaker enabled	JP9	pins 2 & 3 closed

DRAM CONFIGURATION (SYSTEM BOARD)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
4MB	(1) 1M x 36	NONE	NONE	NONE
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	NONE
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 4M x 36	NONE	NONE	NONE
20MB	(1) 1M x 36	(1) 4M x 36	NONE	NONE
24MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	NONE
24MB	(1) 1M x 36	(1) 4M x 36	(1) 1M x 36	NONE
28MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 1M x 36
28MB	(1) 1M x 36	(1) 4M x 36	(1) 1M x 36	(1) 1M x 36
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
36MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	NONE
36MB	(1) 4M x 36	(1) 4M x 36	(1) 1M x 36	NONE
40MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
40MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	(1) 1M x 36
40MB	(1) 4M x 36	(1) 4M x 36	(1) 1M x 36	(1) 1M x 36
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	NONE
52MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
52MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 1M x 36
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 16M x 36	NONE	NONE	NONE
72MB	(1) 1M x 36	(1) 1M x 36	(1) 16M x 36	NONE
80MB	(1) 4M x 36	(1) 16M x 36	NONE	NONE
84MB	(1) 1M x 36	(1) 4M x 36	(1) 16M x 36	NONE
84MB	(1) 4M x 36	(1) 16M x 36	(1) 1M x 36	NONE
88MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 16M x 36
88MB	(1) 4M x 36	(1) 16M x 36	(1) 1M x 36	(1) 1M x 36
96MB	(1) 4M x 36	(1) 4M x 36	(1) 16M x 36	NONE
96MB	(1) 4M x 36	(1) 16M x 36	(1) 4M x 36	NONE
100MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	(1) 16M x 36
100MB	(1) 4M x 36	(1) 16M x 36	(1) 4M x 36	(1) 1M x 36

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DRAM CONFIGURATION (SYSTEM BOARD CON'T)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
112MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 16M x 36
112MB	(1) 4M x 36	(1) 16M x 36	(1) 4M x 36	(1) 4M x 36
128MB	(1) 16M x 36	(1) 16M x 36	NONE	NONE
132MB	(1) 16M x 36	(1) 16M x 36	(1) 1M x 36	NONE
136MB	(1) 1M x 36	(1) 1M x 36	(1) 16M x 36	(1) 16M x 36
136MB	(1) 16M x 36	(1) 16M x 36	(1) 1M x 36	(1) 1M x 36
144MB	(1) 4M x 36	(1) 16M x 36	(1) 16M x 36	NONE
144MB	(1) 16M x 36	(1) 16M x 36	(1) 4M x 36	NONE
148MB	(1) 1M x 36	(1) 4M x 36	(1) 16M x 36	(1) 16M x 36
148MB	(1) 16M x 36	(1) 16M x 36	(1) 4M x 36	(1) 1M x 36
160MB	(1) 4M x 36	(1) 4M x 36	(1) 16M x 36	(1) 16M x 36
160MB	(1) 4M x 36	(1) 16M x 36	(1) 4M x 36	(1) 16M x 36
160MB	(1) 16M x 36	(1) 16M x 36	(1) 4M x 36	(1) 4M x 36
192MB	(1) 16M x 36	(1) 16M x 36	(1) 16M x 36	NONE
208MB	(1) 4M x 36	(1) 16M x 36	(1) 16M x 36	(1) 16M x 36
208MB	(1) 16M x 36	(1) 16M x 36	(1) 4M x 36	(1) 16M x 36
256MB	(1) 16M x 36	(1) 16M x 36	(1) 16M x 36	(1) 16M x 36

VESA WAIT STATE CONFIGURATION	
Wait states	JP3
0 wait states	pins 1 & 2 closed
1 wait state	pins 2 & 3 closed

BUS SPEED CONFIGURATION	
CPU speed	JP4
<= 33MHz	pins 1 & 2 closed
> 33MHz	pins 2 & 3 closed

EXTERNAL BATTERY CONFIGURATION				
Type	JP10	JP11	JP12	JP13
Dallas	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
Benchmarq	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed

AIC-7770 CONFIGURATION		
Setting	JP1	JP2
Installed	Open	Open
Not installed	Closed	Closed

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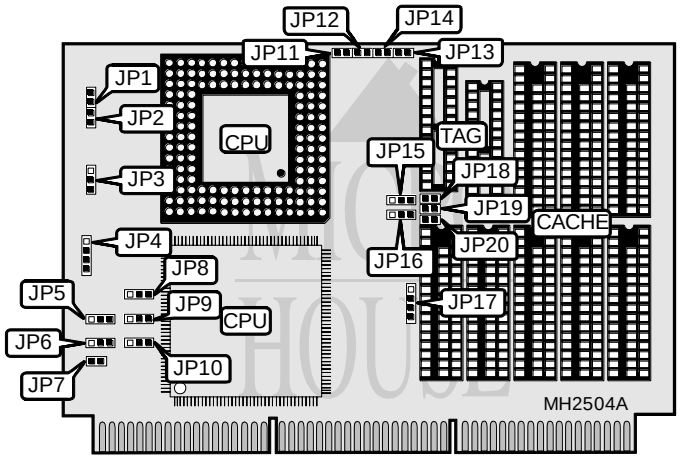

80486 CPU CARD

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í LADS for VLB select 33MHz	JP3	pins 1 & 2 closed
LADS for VLB select 50MHz	JP3	pins 2 & 3 closed
í Factory configured - do not alter	JP5	N/A
í Factory configured - do not alter	JP6	N/A
í Factory configured - do not alter	JP7	N/A
í Local bus disabled	JP10	pins 1 & 2 closed
Local bus enabled	JP10	pins 2 & 3 closed

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG	TAG (DIRTY)
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	(1) 16K x 1
128KB	(4) 32K x 8	NONE	(1) 8K x 8	(1) 16K x 1
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	(1) 16K x 1
1MB	(4) 128K x 8	(4) 128K x 8	(1) 128K x 8	(1) 64K x 1
Note: The orientation of the cache banks and TAG banks are unidentified.				

CACHE JUMPER CONFIGURATION							
Size	JP11	JP12	JP13	JP14	JP18	JP19	JP20
64KB	Open	Open	Open	Open	Open	Open	Open
128KB	Closed	Open	Open	Open	Open	Closed	Open
256KB	Closed	Closed	Open	Open	Open	Closed	Closed
1MB	Closed	Closed	Closed	Closed	Closed	Closed	Closed

CACHE BANK CONFIGURATION		
Type	JP15	JP16
Single bank	pins 1 & 2 closed	pins 1 & 2 closed
Double bank	pins 2 & 3 closed	pins 2 & 3 closed

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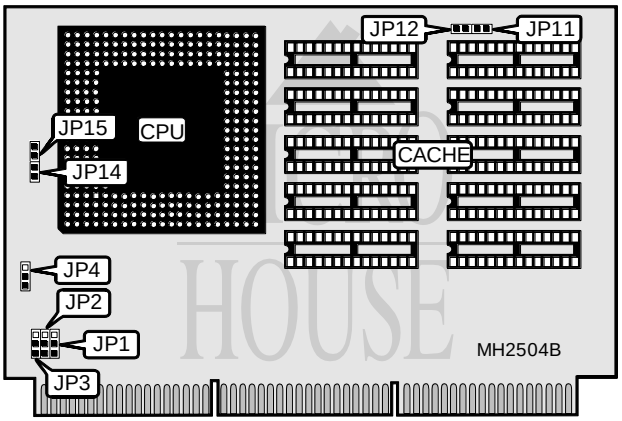
CPU SPEED CONFIGURATION				
Type	JP1	JP2	JP8	JP9
25MHz	Open	Open	pins 1 & 2 closed	pins 1 & 2 closed
33MHz	Open	Closed	pins 1 & 2 closed	pins 2 & 3 closed
40MHz	Closed	Closed	pins 2 & 3 closed	pins 1 & 2 closed
50iMHz	Open	Open	pins 1 & 2 closed	pins 1 & 2 closed
50MHz	Closed	Closed	pins 2 & 3 closed	pins 2 & 3 closed
66iMHz	Open	Closed	pins 1 & 2 closed	pins 2 & 3 closed

CPU TYPE CONFIGURATION	
Type	JP4
80486SX	pins 1 & 2 closed
ODP486SX	pins 2 & 3 closed
80487SX	pins 2 & 3 closed
80486DX	Open
80486DX2	Open
Pentium Overdrive	pins 2 & 3 closed

BUS SPEED CONFIGURATION	
CPU speed	JP17
<= 33MHz	pins 1 & 2, 3 & 4 closed
> 33MHz	pins 2 & 3 closed



PENTIUM CPU CARD

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Local bus disabled	JP4	pins 2 & 3 closed
Local bus enabled	JP4	pins 1 & 2 closed

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CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG	TAG (DIRTY)
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	(1) 16K x 1
1MB	(4) 128K x 8	(4) 128K x 8	(1) 32K x 8	(1) 64K x 1
Note: The orientation of the cache banks and TAG banks are unidentified. The location of the TAG is unidentified.				

CACHE JUMPER CONFIGURATION				
Size	JP11	JP12	JP14	JP15
256KB	Open	Open	Open	Open
1MB	Closed	Closed	Closed	Closed

CPU FREQUENCY CONFIGURATION			
Speed	JP1	JP2	JP3
33MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
40MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed
50MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed
66MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed
80MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed