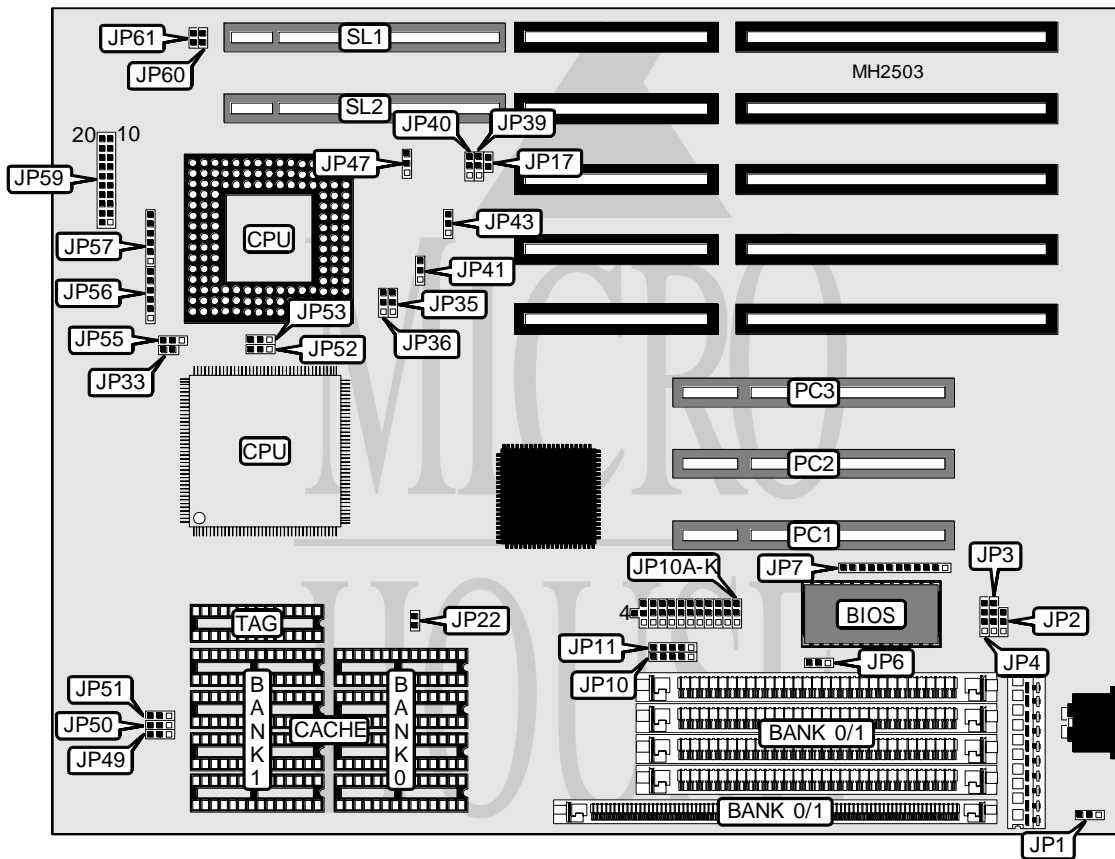


MECER CORPORATION

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Processor	SL80486SX/80486SX/CX486DX/SL80486DX/80486DX/SL80486DX2/ 80486DX2/P24D/Pentium Overdrive
Processor Speed	25/33/40/50(internal)/50/66(internal)MHz
Chip Set	Contaq
Max. Onboard DRAM	128MB
Cache	128/256/512/1024KB
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (2), 32-bit PCI slots (3), green PC connector
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Auxiliary keyboard connector	JP1	Turbo LED	JP59 pins 11 - 12
External battery	JP3	Green PC LED	JP59 pins 13 - 14
External battery	JP4	Turbo switch	JP59 pins 16 - 17
Green PC connector	JP6	Reset switch	JP59 pins 19 - 20
Power LED & keylock	JP59 pins 1 - 4	32-bit PCI slots	PC1 - PC3
Speaker	JP59 pins 7 - 10	32-bit VESA local bus slots	SL1 & SL2

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP2	pins 1 & 2 closed
CMOS memory clear	JP2	pins 2 & 3 closed
í PQFP CPU disabled	JP33	Closed
PQFP CPU enabled	JP33	Open
í PCI clock delay select 1 delay	JP43	pins 1 & 2 closed
PCI clock delay select 3 delays	JP43	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(1) 256K x 36
2MB	(1) 256K x 36	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(1) 1M x 36
5MB	(1) 1M x 36	(4) 256K x 9
8MB	(4) 1M x 9	(1) 1M x 36
8MB	(1) 1M x 36	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(1) 4M x 36
17MB	(1) 4M x 36	(4) 256K x 9
20MB	(4) 1M x 9	(1) 4M x 36
20MB	(1) 4M x 36	(4) 1M x 9
32MB	(4) 4M x 9	(1) 4M x 36
32MB	(1) 4M x 36	(4) 4M x 9
64MB	(4) 16M x 9	NONE
65MB	(4) 256K x 9	(1) 16M x 36
65MB	(1) 16M x 36	(4) 256K x 9
68MB	(4) 1M x 9	(1) 16M x 36
68MB	(1) 16M x 36	(4) 1M x 9
80MB	(4) 4M x 9	(1) 16M x 36
80MB	(1) 16M x 36	(4) 4M x 9
128MB	(4) 16M x 9	(1) 16M x 36
128MB	(1) 16M x 36	(4) 16M x 9

DRAM JUMPER CONFIGURATION		
Type	JP10	JP11
Bank 0 = 30-pin & Bank 1 = 72-pin	pins 1 & 2 closed	pins 2 & 3 closed
Bank 0 = 72-pin & Bank 1 = 30-pin	pins 2 & 3 closed	pins 1 & 2 closed
Bank 0/1 = double side 72-pin	Open	pins 1 & 2, 3 & 4 closed

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CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
256KB	(4) 64K x 8	NONE	(1) 64K x 8
512KB	(4) 64K x 8	(4) 64K x 8	(1) 32K x 8
512KB	(4) 128K x 8	NONE	(1) 32K x 8
1MB	(4) 128K x 8	(4) 128K x 8	(1) 128K x 8

CACHE JUMPER CONFIGURATION			
Size	JP49	JP50	JP51
128KB	pins 1 & 2 closed	pins 2 & 3 closed	Open
256KB	pins 1 & 2 closed	pins 1 & 2 closed	Open
256KB	pins 1 & 2 closed	pins 2 & 3 closed	Open
512KB	pins 2 & 3 closed	Open	pins 1 & 2 closed
512KB	pins 2 & 3 closed	pins 2 & 3 closed	Open
1MB	pins 2 & 3 closed	Open	pins 2 & 3 closed

CPU TYPE CONFIGURATION				
Type	JP35	JP36	JP41	JP52
80486SX	1 & 2	Open	1 & 2	1 & 2, 3 & 4
SL80486SX	2 & 3	Open	1 & 2	2 & 3
CX486DX	2 & 3	1 & 2	2 & 3	1 & 2, 3 & 4
80486DX	2 & 3	Open	1 & 2	1 & 2, 3 & 4
SL80486DX	2 & 3	Open	1 & 2	1 & 2, 3 & 4
80486DX2	1 & 2	Open	1 & 2	1 & 2, 3 & 4
SL80486DX2	2 & 3	Open	1 & 2	1 & 2, 3 & 4
P24D (WB)	2 & 3	2 & 3	1 & 2	1 & 2, 3 & 4
P24D (WT)	2 & 3	2 & 3	1 & 2	1 & 2, 3 & 4
Pentium Overdrive (WB)	2 & 3	1 & 2	1 & 2	1 & 2, 3 & 4
Pentium Overdrive (WT)	2 & 3	1 & 2	1 & 2	1 & 2, 3 & 4

Note: WB = write back & WT = write through. Pins designated should be in the closed position.

CPU TYPE CONFIGURATION				
Type	JP53	JP55	JP56	JP57
80486SX	Open	Open	Open	Open
SL80486SX	1 & 2	Open	3 & 4, 5 & 6	Open
CX486DX	2 & 3	2 & 3	4 & 5	5 & 6
80486DX	Open	2 & 3	Open	Open
SL80486DX	1 & 2	2 & 3	3 & 4, 5 & 6	Open
80486DX2	Open	2 & 3	Open	Open
SL80486DX2	1 & 2	2 & 3	3 & 4, 5 & 6	Open
P24D (WB)	1 & 2, 3 & 4	2 & 3	3 & 4, 5 & 6	2 & 3, 4 & 5
P24D (WT)	1 & 2, 3 & 4	2 & 3	3 & 4, 5 & 6	1 & 2, 4 & 5
Pentium Overdrive (WB)	1 & 2	1 & 2	5 & 6	3 & 4
Pentium Overdrive (WT)	1 & 2	1 & 2	5 & 6	1 & 2

Note: WB = write back & WT = write through. Pins designated should be in the closed position.

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CPU SPEED CONFIGURATION					
Speed	JP17	JP22	JP39	JP40	JP47
25MHz	Closed	Closed	1 & 2	2 & 3	2 & 3
33MHz	Closed	Open	2 & 3	1 & 2	2 & 3
40MHz	Open	Open	1 & 2	2 & 3	1 & 2
50iMHz	Closed	Closed	1 & 2	2 & 3	2 & 3
50MHz	Open	Open	2 & 3	1 & 2	1 & 2
66iMHz	Closed	Open	2 & 3	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

VESA WAIT STATE CONFIGURATION	
Wait states	JP60
0 wait states	Open
1 wait state	Closed

BUS SPEED CONFIGURATION	
CPU speed	JP61
<= 33MHz	Open
> 33MHz	Closed

PCI IRQ CONFIGURATION (IRQ3)	
IRQ	JP10A
IRQ3 PCI compliant	pins 1 & 2 closed
IRQ3 PCI non compliant	pins 2 & 3 closed

PCI IRQ CONFIGURATION (IRQ4)	
IRQ	JP10B
IRQ4 PCI compliant	pins 1 & 2 closed
IRQ4 PCI non compliant	pins 2 & 3 closed

PCI IRQ CONFIGURATION (IRQ5)	
IRQ	JP10C
IRQ5 PCI compliant	pins 1 & 2 closed
IRQ5 PCI non compliant	pins 2 & 3 closed

PCI IRQ CONFIGURATION (IRQ7)	
IRQ	JP10D
IRQ7 PCI compliant	pins 1 & 2 closed
IRQ7 PCI non compliant	pins 2 & 3 closed

PCI IRQ CONFIGURATION (IRQ10)	
IRQ	JP10E
IRQ10 PCI compliant	pins 1 & 2 closed
IRQ10 PCI non compliant	pins 2 & 3 closed

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PCI IRQ CONFIGURATION (IRQ11)	
IRQ	JP10F
IRQ11 PCI compliant	pins 1 & 2 closed
IRQ11 PCI non compliant	pins 2 & 3 closed

PCI IRQ CONFIGURATION (IRQ12)	
IRQ	JP10G
IRQ12 PCI compliant	pins 1 & 2 closed
IRQ12 PCI non compliant	pins 2 & 3 closed

PCI IRQ CONFIGURATION (IRQ15)	
IRQ	JP10H
IRQ15 PCI compliant	pins 1 & 2 closed
IRQ15 PCI non compliant	pins 2 & 3 closed

PCI IRQ CONFIGURATION (IRQ9)	
IRQ	JP10I
IRQ9 PCI compliant	pins 1 & 2 closed
IRQ9 PCI non compliant	pins 2 & 3 closed

PCI IRQ CONFIGURATION (IRQ6)	
IRQ	JP10J
IRQ6 PCI compliant	pins 1 & 2 closed
IRQ6 PCI non compliant	pins 2 & 3 closed

PCI IRQ CONFIGURATION (IRQ14)	
IRQ	JP10K
IRQ14 PCI compliant	pins 1 & 2 closed
IRQ14 PCI non compliant (1 card installed)	pins 2 & 3 closed
IRQ14 PCI non compliant (2 cards installed)	pins 2 & 4 closed

PCI COMPLIANCE CONFIGURATION	
Slot	JP7
Slot 1 compliant	pins 1 & 2 closed
Slot 1 non-compliant	pins 2 & 3 closed
Slot 2 compliant	pins 4 & 5 closed
Slot 2 non-compliant	pins 5 & 6 closed
Slot 3 compliant	pins 7 & 8 closed
Slot 3 non-compliant	pins 8 & 9 closed
Slot 3 non-compliant *	pins 11 & 12 closed

Note: * Slot 3 has a second non-compliant PCI card installed.