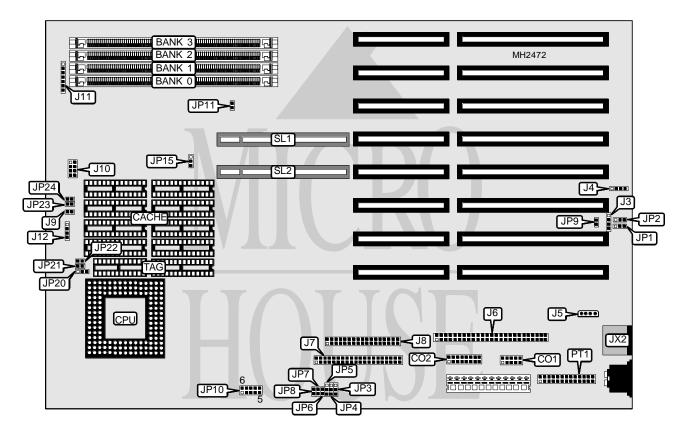
ACER, INC. ACERPOWER SV/V/DV (V10 PROJECT)

Processor	80486SX/80486DX/80486DX2
Processor Speed	20/25/33/40/50(internal)/50/66(internal)MHz
Chip Set	Unidentified
Max. Onboard DRAM	64MB
Cache	64/128/256KB
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (2), floppy drive interface, green PC connector, green VGA connector, IDE interface, SCSI connector, parallel port, PS/2 mouse port, serial ports (2)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	CO1	Chassis fan power	19
Serial port 2	CO2	LED connector	J10
External battery	J3	Optional keyboard/mouse	J11
Green VGA connector	J4	IDE interface LED	J12
Green PC connector	J5	PS/2 mouse port	JX2
SCSI interface	J6	Parallel port	PT1
IDE interface	J7	32-bit VESA local bus slots	SL1 & SL2
Floppy drive interface	J8		

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USER CONFIGURABLE SETTINGS			
Function	Jumper	Position	
í Password disabled	JP1	pins 2 & 3 closed	
Password enabled	JP1	pins 1 & 2 closed	
í Acer copyright enabled	JP2	pins 1 & 2 closed	
Acer copyright disabled	JP2	pins 2 & 3 closed	
í SCSI I/O address select 340-35F	JP6	Open	
SCSI I/O address select 140-15F	JP6	Closed	
í M5105 enabled	JP7	Closed	
M5105 disabled	JP7	Open	
í Bidirectional parallel port enabled	JP8	Closed	
Bidirectional parallel port disabled	JP8	Open	
í Battery type select internal	JP9	Closed	
Battery type select external	JP9	Open	
í Reset key enabled	JP23	Closed	
Reset key disabled	JP23	Open	
í Keylock disabled	JP24	Closed	
Keylock enabled	JP24	Open	

DRAM CONFIGURATION					
Size	On board	Bank 0	Bank 1	Bank 2	Bank 3
4MB	4MB	NONE	NONE	NONE	NONE
8MB	4MB	NONE	(1) 1M x 36	NONE	NONE
12MB	4MB	NONE	(1) 1M x 36	(1) 1M x 36	NONE
16MB	4MB	NONE	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	Disabled	(1) 4M x 36	NONE	NONE	NONE
20MB	4MB	NONE	(1) 4M x 36	NONE	NONE
24MB	4MB	NONE	(1) 1M x 36	(1) 4M x 36	NONE
32MB	Disabled	(1) 4M x 36	(1) 4M x 36	NONE	NONE
36MB	4MB	NONE	(1) 4M x 36	(1) 4M x 36	NONE
40MB	4MB	NONE	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
48MB	Disabled	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	NONE
64MB	Disabled	(1) 4M x 36			
Note: This board has 4MB of memory factory installed. If memory is disabled, use the DRAM jumper configuration table.					

DRAM JUMPER CONFIGURATION		
Туре	JP15	
On board 4MB RAM enabled	pins 2 & 3 closed	
On board 4MB RAM disabled pins 1 & 2 closed		

		CACHE CONFIGURATIO	N	
Size	Bank 0	Bank 1	TAG	TAG (DIRTY)
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8 or 32K x 8	(1) 16K x 4
128KB	(4) 32K x 8	NONE	(1) 8K x 8 or 32K x 8	(1) 16K x 4
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	(1) 16K x 4
Note: The orientation of the cache banks and TAG banks are unidentified.				

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CACHE JUMPER CONFIGURATION				
Size	JP20	JP21	JP22	
64KB	pins 2 & 3 closed	Open	Open	
128KB	pins 1 & 2 closed	Closed	Open	
256KB	pins 2 & 3 closed	Closed	Closed	

CPU SPEED CONFIGURATION			
Speed	JP10		
20MHz	pins 1 & 6 closed		
25MHz	pins 2 & 7 closed		
33MHz	pins 3 & 8 closed		
40MHz	pins 4 & 9 closed		
50iMHz	pins 2 & 7 closed		
50MHz	pins 5 & 10 closed		
66iMHz	pins 3 & 8 closed		

VESA WAIT STATE CONFIGURATION			
Wait state JP11			
0 wait states	Open		
1 wait state	Closed		

SCSI IRQ CONFIGURATION		
IRQ	JP3	
Disabled	Open	
IRQ10	pins 1 & 2 closed	
IRQ11	pins 2 & 3 closed	

SCSI DMA CONFIGURATION			
DMA	JP4	JP5	
Disabled	Open	Open	
DMA 5	pins 1 & 2 closed	pins 1 & 2 closed	
DMA 6	pins 2 & 3 closed	pins 2 & 3 closed	