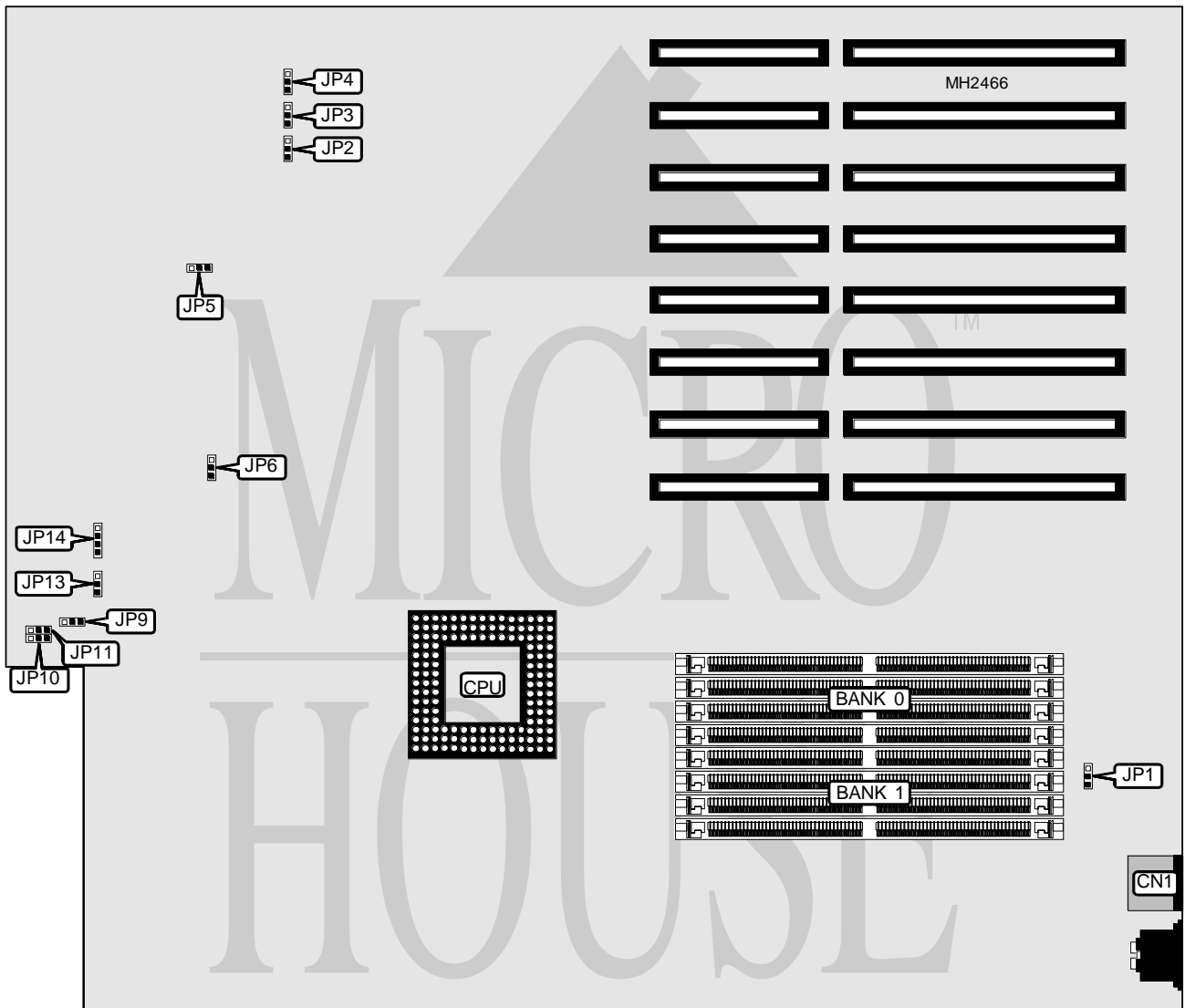


ACER, INC.

ACERFRAME 3000SP33

Processor	80486SX
Processor Speed	25/33MHz
Chip Set	Unidentified
Max. Onboard DRAM	64MB
Cache	128KB
BIOS	Unidentified
Dimensions	355mm x 304mm
I/O Options	PS/2 mouse port
NPU Options	4167 (on optional card)



CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse port	CN1	Speaker	JP14

Continued on next page. . .

ACER, INC.
ACERFRAME 3000SP33

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í UPS installed	JP1	Open
UPS not installed	JP1	pins 2 & 3 closed
í Password enabled	JP2	pins 1 & 2 closed
Password disabled	JP2	pins 2 & 3 closed
í I/O recovery time select recovery time added	JP5	pins 1 & 2 closed
I/O recovery time select no recovery time added	JP5	pins 2 & 3 closed
í Interrupt type select latched	JP6	pins 1 & 2 closed
Interrupt type select unlatched	JP6	pins 2 & 3 closed
í Page hit cycle enabled	JP10	pins 2 & 3 closed
Page hit cycle disabled	JP10	pins 1 & 2 closed
í Cache hit cycle enabled	JP11	pins 2 & 3 closed
Cache hit cycle disabled	JP11	pins 1 & 2 closed
í Speaker not installed at JP14	JP13	pins 2 & 3 closed
Speaker installed at JP14	JP13	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	(4) 256K x 36	NONE
8MB	(4) 256K x 36	(4) 256K x 36
8MB	(4) 512K x 36	NONE
12MB	(4) 512K x 36	(4) 256K x 36
16MB	(4) 512K x 36	(4) 512K x 36
16MB	(4) 1M x 36	NONE
20MB	(4) 1M x 36	(4) 256K x 36
24MB	(4) 1M x 36	(4) 512K x 36
32MB	(4) 1M x 36	(4) 1M x 36
32MB	(4) 2M x 36	NONE
36MB	(4) 2M x 36	(4) 256K x 36
40MB	(4) 2M x 36	(4) 512K x 36
48MB	(4) 2M x 36	(4) 1M x 36
64MB	(4) 2M x 36	(4) 2M x 36

CACHE CONFIGURATION			
Size	Bank 0	TAG (U126)	DIRTY (U100-U101)
128KB	(16) 8K x 9	(1) 16K x 1	(2) 8K x 8

Note: Bank 0 = U1 - U6 , U11 - U14, U17 - U20, U25 - U26. The location of cache is unidentified.

CPU SPEED CONFIGURATION	
Speed	JP9
25MHz	pins 1 & 2 closed
33MHz	pins 2 & 3 closed

Continued on next page. . .

ACER, INC.
ACERFRAME 3000SP33

... continued from previous page

BIOS CONFIGURATION		
Size	JP3	JP4
27128	pins 2 & 3 closed	pins 2 & 3 closed
27256	pins 1 & 2 closed	pins 2 & 3 closed
27512	pins 1 & 2 closed	pins 1 & 2 closed