SIEMENS NIXDORF INFORMATIONSSYSTEME AG S26361-D800

80486SX/80486SX2/80486DX/80486DX2/80486DX4/Pentium Overdrive **Processor**

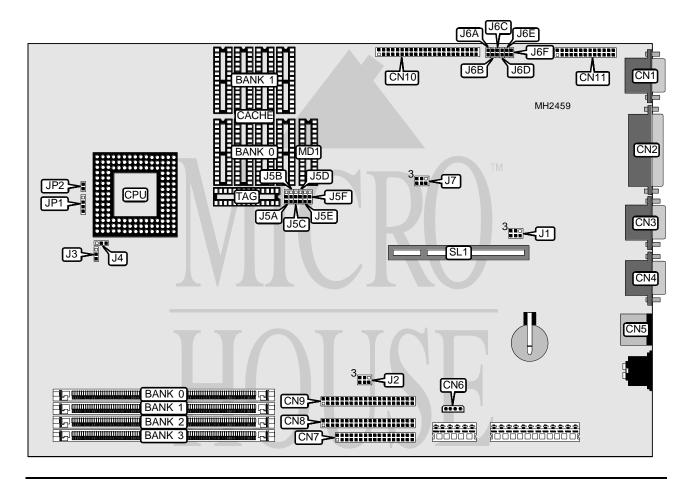
Processor Speed 25/33/50(internal)/66(internal)/75(internal)/100(internal)MHz

Unidentified **Chip Set** Max. Onboard DRAM 128MB 128/256KB Cache **BIOS** Unidentified **Dimensions** 330mm x 218mm

I/O Options 32-bit PCI slots (1), floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse

port, serial ports (2), VGA feature connector, VGA port

NPU Options None



CONNECTIONS			
Purpose	Location	Purpose	Location
VGA port	CN1	IDE interface 1	CN8
Parallel port	CN2	IDE interface 2	CN9
Serial port 2	CN3	Imageport connector	CN10
Serial port 1	CN4	VGA feature connector	CN11
PS/2 mouse port	CN5	Speaker	JP1
Optional power connector	CN6	LED connector	JP2
Floppy drive interface	CN7	32-bit PCI slot	SL1

Continued on next page. . .

SIEMENS NIXDORF INFORMATIONSSYSTEME AG S26361-D800

. . . continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í L2 cache installed	J5D	pins 1 & 2 closed
L2 cache not installed	J5D	pins 2 & 3 closed
í Cache type select HIT	J5E	pins 1 & 2 closed
Cache type select L2WB	J5E	pins 2 & 3 closed
í Cache type select write through	J5F	pins 1 & 2 closed
Cache type select write back (Pentium Overdrive)	J5F	pins 2 & 3 closed
í Factory configured - do not alter	J6A	Closed
í Flash BIOS mode select normal	J6B	Open
Flash BIOS mode select recovery	J6B	Closed
í Floppy drive interface enabled	J6C	Closed
Floppy drive interface disabled	J6C	Open
í Flash BIOS write protect enabled	J6D	Open
Flash BIOS write protect disabled	J6D	Closed
í CMOS memory normal operation	J6E	Open
CMOS memory clear	J6E	Closed
í Factory configured - do not alter	J7	pins 2 & 3 closed

		DRAM CONFIGURATION	N	
Size	Bank 0	Bank 1	Bank 2	Bank 3
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
16MB	(1) 2M x 36	(1) 2M x 36	NONE	NONE
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 8M x 36	(1) 8M x 36	NONE	NONE
128MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG	MD1
128KB	(4) 32K x 8	NONE	(1) 8K x 8	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION			
Size	J5B	J5C	
128KB	pins 1 & 2 closed	pins 1 & 2 closed	
256KB	pins 2 & 3 closed	pins 2 & 3 closed	

CPU TYPE CONFIGURATION		
Туре	J3	
80486SX	pins 1 & 2 closed	
80486SX2	pins 1 & 2 closed	
80486DX	pins 2 & 3 closed	
80486DX2	pins 2 & 3 closed	
80486DX4	pins 2 & 3 closed	
Pentium Overdrive	pins 2 & 3 closed	

Continued on next page. . .

SIEMENS NIXDORF INFORMATIONSSYSTEME AG S26361-D800

. . . continued from previous page

	CPU TYPE CONFIGURATION	
Туре	J2	J5A
Non SL enhanced CPU	pins 2 & 3, 5 & 6 closed	pins 2 & 3 closed
SL enhanced CPU	pins 1 & 2, 4 & 5 closed	pins 1 & 2 closed

CPU SPEED CONFIGURATION		
Speed	J6F	
25MHz	Open	
33MHz	Closed	
50iMHz	Open	
66iMHz	Closed	
75iMHz	Open	
100iMHz	Closed	

CPU VOLTAGE CONFIGURATION		
Voltage	J4	
3.3v	pins 1 & 2 closed	
5v	pins 2 & 3 closed	

DMA CHANNEL CONFIGURATION		
DMA J1		
DMA 1	pins 1 & 2, 4 & 5 closed	
DMA 3	pins 2 & 3, 5 & 6 closed	