FUJIKAMA USA, INC.
286 AT TURBO MAINBOARD

Processor	80286
Processor Speed	8/16MHz
Chip Set	Undentified
Max. Onboard DRAM	4MB
Cache	None
BIOS	AMI
Dimensions	240mm x 218mm
I/O Options	None
NPU Options	80287
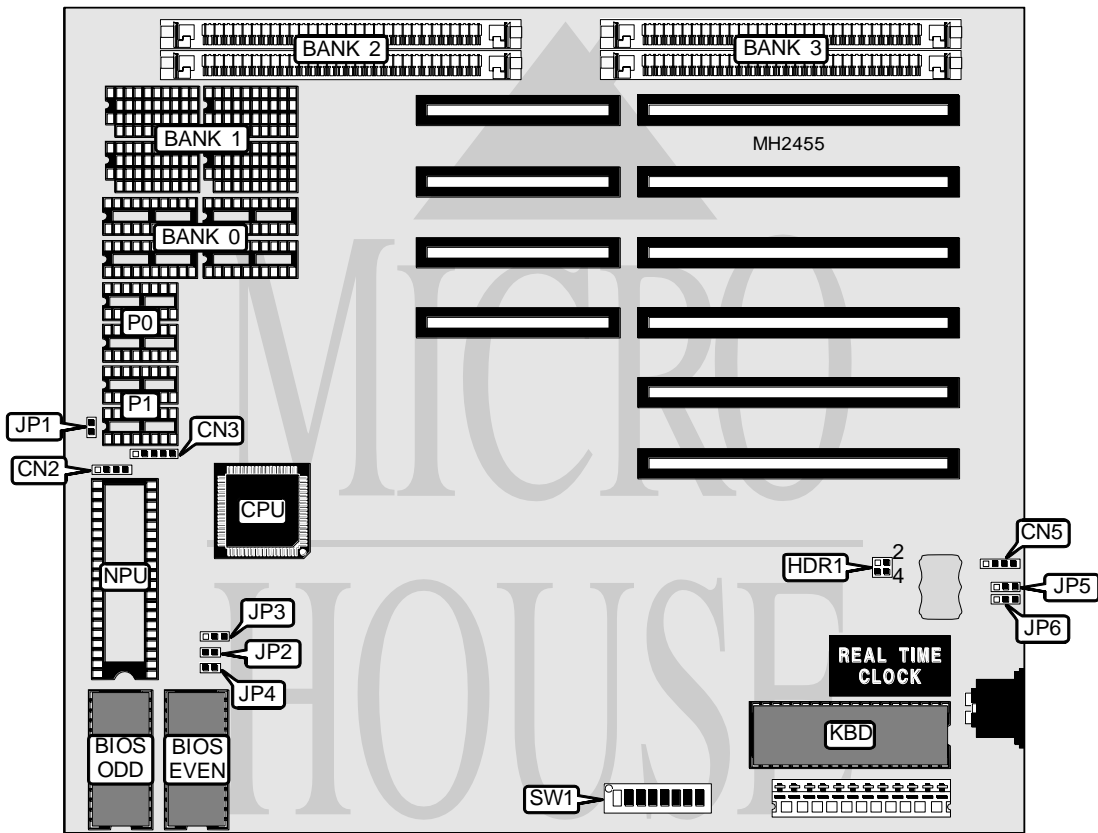
CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	CN2	Turbo LED	JP1
Power LED & keylock	CN3	Turbo switch	JP2
External battery	CN5	Reset switch	JP4

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	HDR1	pins 1 & 2 closed
CMOS memory clear	HDR1	pins 3 & 4 closed
í BIOS type select 27256	JP3	pins 2 & 3 closed
BIOS type select 27128	JP3	pins 1 & 2 closed
í Power good signal detect from board	JP5	pins 1 & 2 closed
Power good signal detect from power supply	JP5	pins 2 & 3 closed
í Clock speed control external switch	JP6	pins 1 & 2 closed
Clock speed control keyboard	JP6	pins 2 & 3 closed
í Parity check enabled	SW1/1	On
Parity check disabled	SW1/1	Off
í Shadow RAM disabled	SW1/2	On
Shadow RAM enabled	SW1/2	Off
í RAM wait state select 1 wait state	SW1/3	Off
RAM wait state select 0 wait states	SW1/3	On
í ROM wait state select 3 wait states	SW1/4	Off
ROM wait state select 2 wait states	SW1/4	On
í Monitor type select monochrome	SW1/7	Off
Monitor type select color	SW1/7	On
í Factory configured - do not alter	SW1/8	Off

DRAM CONFIGURATION						
Size	Bank 0	Parity 0	Bank 1	Parity 1	Bank 2	Bank 3
512KB	(4) 44256	(2) 41256	NONE	NONE	NONE	NONE
640KB	(4) 44256	(2) 41256	(4) 4464	(2) 4164	NONE	NONE
1MB	(4) 44256	(2) 41256	(4) 44256	(2) 41256	NONE	NONE
2MB	NONE	NONE	NONE	NONE	(2) 1M x 9	NONE
4MB	NONE	NONE	NONE	NONE	(2) 1M x 9	(2) 1M x 9

DRAM SWITCH CONFIGURATION		
Size	SW1/5	SW1/6
512KB	On	On
640KB	On	Off
1MB	On	Off
2MB	Off	On
4MB	Off	Off