BIOSTAR MICROTECH INTERNATIONAL, INC.
M B - 1 5 0 0 / 1 5 7 5 / 1 5 9 0 P C T

Processor	Pentium
Processor Speed	75/90/100MHz
Chip Set	Unidentified
Max. Onboard DRAM	128MB
Cache	256/512/1024/2048KB
BIOS	Award
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (2), 32-bit PCI slots (3)
NPU Options	None
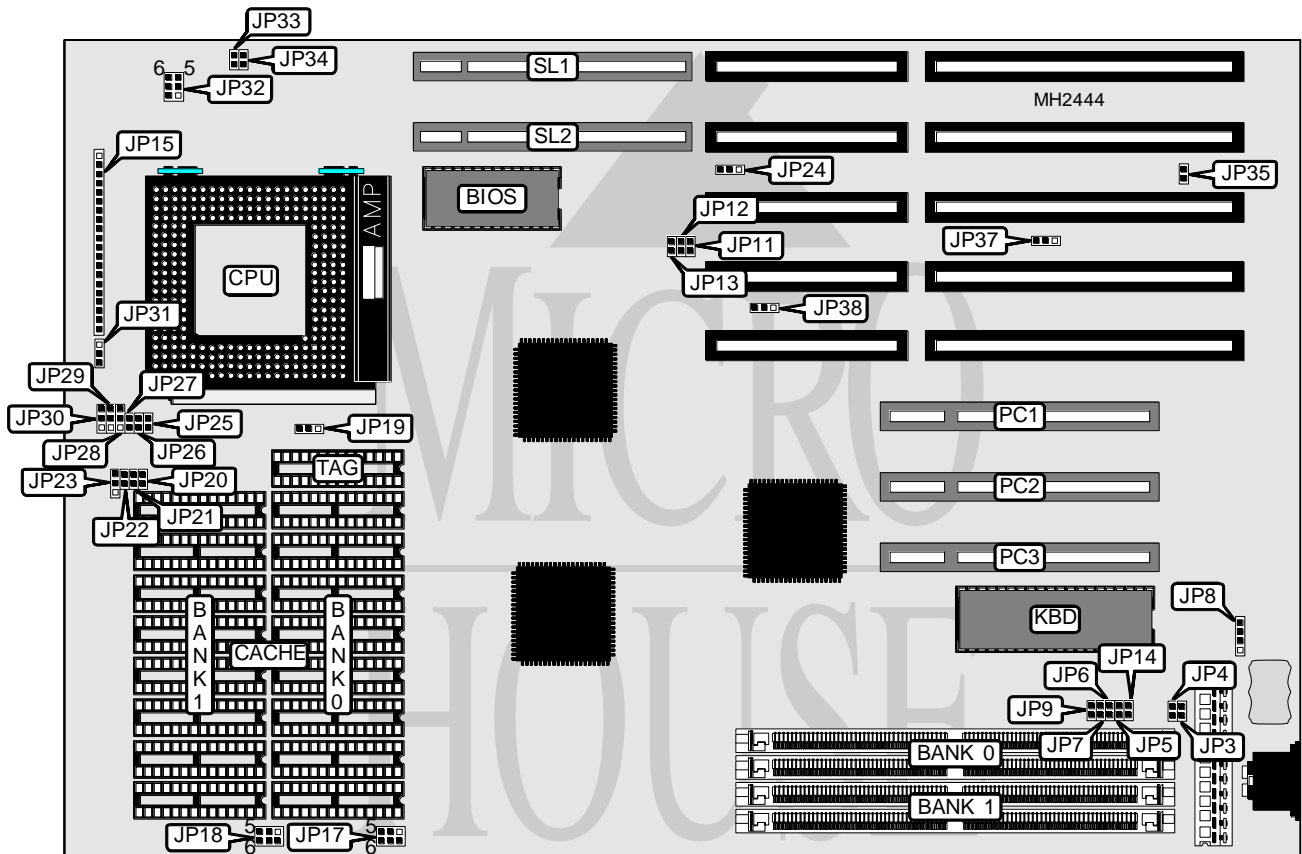
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP8	Turbo switch	JP15 (pins 14 - 16)
Speaker	JP15 (pins 1 - 4)	+5v ground	JP15 (pins 17 - 18)
Power LED & keylock	JP15 (pins 5 - 9)	Chassis fan power	JP32
Turbo LED	JP15 (pins 10 - 11)	32-bit VESA local bus slots	PC1 - PC3
Reset switch	JP15 (pins 12 - 13)	32-bit PCI bus slots	SL1 & SL2

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Host data wait state select 1 wait state	JP4	Open
Host data wait state select 0 wait states	JP4	Closed
í Back to back I/O delay select 3 wait states	JP6	Open
Back to back I/O delay select 0 wait states	JP6	Closed
í CMOS memory normal operation	JP8	pins 2 & 3 closed
CMOS memory clear	JP8	pins 3 & 4 closed
Battery type select external	JP8	Closed
í Monitor type select color	JP14	Open
Monitor type select monochrome	JP14	Closed
í Factory configured - do not alter	JP19	Open
í Buffer type select stand alone	JP26	Open
Buffer type select loaded component	JP26	Closed
í Factory configured - do not alter	JP31	Open
í Stop clock select enabled	JP33	Open
Stop clock select disabled	JP33	Closed
í Bus/core ratio select 2/3 ratio	JP34	Open
Bus/core ratio select 1/2 ratio	JP34	Closed
í Factory configured - do not alter	JP35	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
2MB	(2) 256K x 36	NONE
4MB	(2) 512K x 36	NONE
4MB	(2) 256K x 36	(2) 256K x 36
6MB	(2) 256K x 36	(2) 512K x 36
8MB	(2) 1M x 36	NONE
8MB	(2) 512K x 36	(2) 512K x 36
10MB	(2) 256K x 36	(2) 1M x 36
12MB	(2) 512K x 36	(2) 1M x 36
16MB	(2) 2M x 36	NONE
16MB	(2) 1M x 36	(2) 1M x 36
18MB	(2) 256K x 36	(2) 2M x 36
20MB	(2) 512K x 36	(2) 2M x 36
24MB	(2) 1M x 36	(2) 2M x 36
32MB	(2) 4M x 36	NONE
32MB	(2) 2M x 36	(2) 2M x 36
34MB	(2) 256K x 36	(2) 4M x 36
36MB	(2) 512K x 36	(2) 4M x 36
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	NONE
66MB	(2) 256K x 36	(2) 8M x 36
68MB	(2) 512K x 36	(2) 8M x 36
72MB	(2) 1M x 36	(2) 8M x 36

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DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 8M x 36	(2) 4M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
256KB	(8) 32K x 8	NONE	(1) 8K x 8
512KB	(8) 32K x 8	(8) 32K x 8	(1) 32K x 8
512KB	(8) 64K x 8	NONE	(1) 32K x 8
1MB	(8) 64K x 8	(8) 64K x 8	(1) 32K x 8
1MB	(8) 128K x 8	NONE	(1) 32K x 8
2MB	(8) 128K x 8	(8) 128K x 8	(1) 64K x 8

CACHE JUMPER CONFIGURATION							
Size	JP20	JP21	JP22	JP23	JP28	JP29	JP30
256KB	Open	Open	Open	Open	1 & 2	Open	1 & 2
512KB	Open	Open	Closed	Open	2 & 3	Open	2 & 3
512KB	Open	Open	Closed	Open	1 & 2	2 & 3	1 & 2
1MB	Open	Closed	Closed	Open	2 & 3	1 & 2	2 & 3
1MB	Open	Closed	Closed	2 & 3	1 & 2	2 & 3	1 & 2
2MB	Closed	Closed	Closed	1 & 2	2 & 3	1 & 2	2 & 3
Note: Pins designated should be in the closed position.							

CACHE VOLTAGE CONFIGURATION		
Voltage	JP17	JP18
3v	pins 3 & 5, 4 & 6 closed	pins 3 & 5, 4 & 6 closed
5v	pins 1 & 3, 2 & 4 closed	pins 1 & 3, 2 & 4 closed
Note: JP17 = Bank 0 and JP18 = Bank 1.		

CPU SPEED CONFIGURATION			
Speed	JP11	JP12	JP13
75MHz	Closed	Open	Open
90MHz	Open	Closed	Open
100MHz	Closed	Open	Closed

VESA WAIT STATE CONFIGURATION	
Wait states	JP5
0 wait states	Open
1 wait state	Closed

BUS SPEED CONFIGURATION	
CPU speed	JP3
CPU clock/2	Open
= 50MHz	Closed

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AT CLOCK CONFIGURATION		
Clock	JP7	JP9
LCLK/3	Open	Closed
LCLK/4	Closed	Open

PCI IRQ CONFIGURATION		
IRQ	JP37	JP38
IRQ14	pins 2 & 3 closed	pins 1 & 2 closed
IRQ15	pins 1 & 2 closed	pins 2 & 3 closed

DRIVE ENABLE CONFIGURATION		
Speed	JP25	JP27
50MHz	Open	Open
60MHz	Closed	Closed
66MHz	Closed	Closed

FLASH MEMORY CONFIGURATION	
Setting	JP24
EPROM	Open
5v flash	pins 2 & 3 closed
12v flash	pins 1 & 2 closed