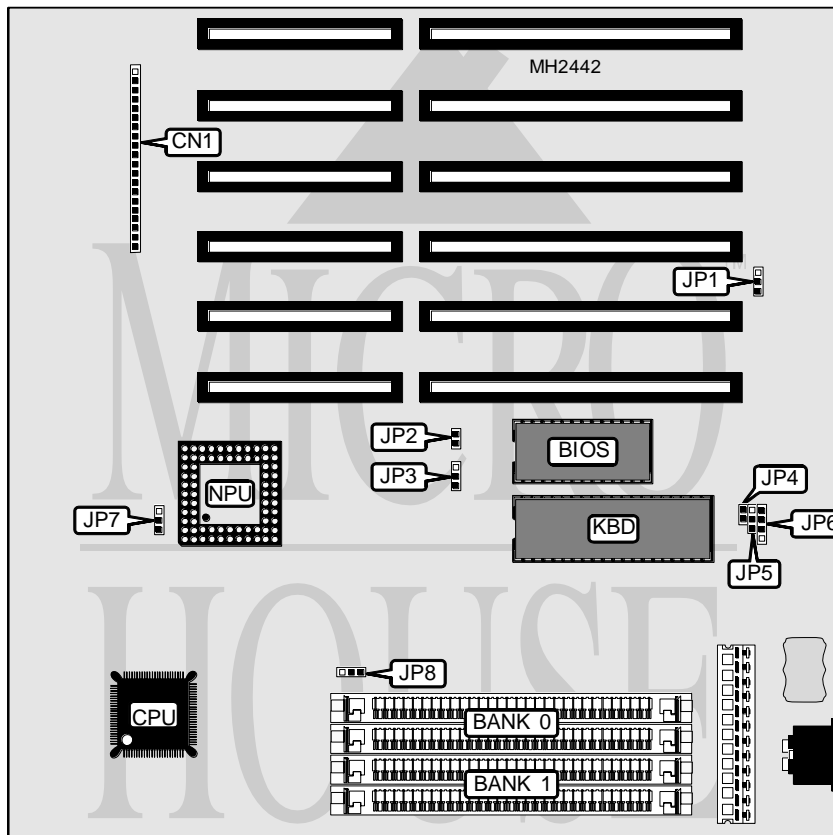


BIOSTAR MICROTECH INTERNATIONAL CORPORATION MB-1325/1333ACA-S

Processor 80386SX/CX486SLC
Processor Speed 25/33MHz
Chip Set Unidentified
Max. Onboard DRAM 16MB
Cache None
BIOS AMI
Dimensions 220mm x 220mm
I/O Options None
NPU Options 80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	CN1 (pins 1 - 4)	Turbo switch	CN1 (pins 14 - 16)
Power LED & keylock	CN1 (pins 5 - 9)	+5v ground	CN1 (pins 17 - 18)
Turbo LED	CN1 (pins 10 - 11)	External battery	JP6
Reset switch	CN1 (pins 12 - 13)		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 1 & 2 closed
CMOS memory clear	JP1	pins 2 & 3 closed
í Parity check enabled	JP2	Open
Parity check disabled	JP2	Closed
í Chip type select 146818	JP4	Open
Chip type select Dallas	JP4	Closed
í Monitor type select monochrome	JP5	pins 1 & 2 closed
Monitor type select color	JP5	pins 2 & 3 closed
í Clock mode select synchronous	JP7	pins 1 & 2 closed
Clock mode select asynchronous	JP7	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9

CPU SPEED CONFIGURATION		
Speed	JP3	JP8
25MHz	pins 2 & 3 closed	pins 1 & 2 closed
33MHz	pins 2 & 3 closed	pins 2 & 3 closed