BIOSTAR MICROTECH INTERNATIONAL CORPORATION MB-1333VNA-S

Processor	80386SX/CX486SLC
Processor Speed	33MHz
Chip Set	Unidentified
Max. Onboard DRAM	16MB
Cache	None
BIOS	AMI
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80387SX
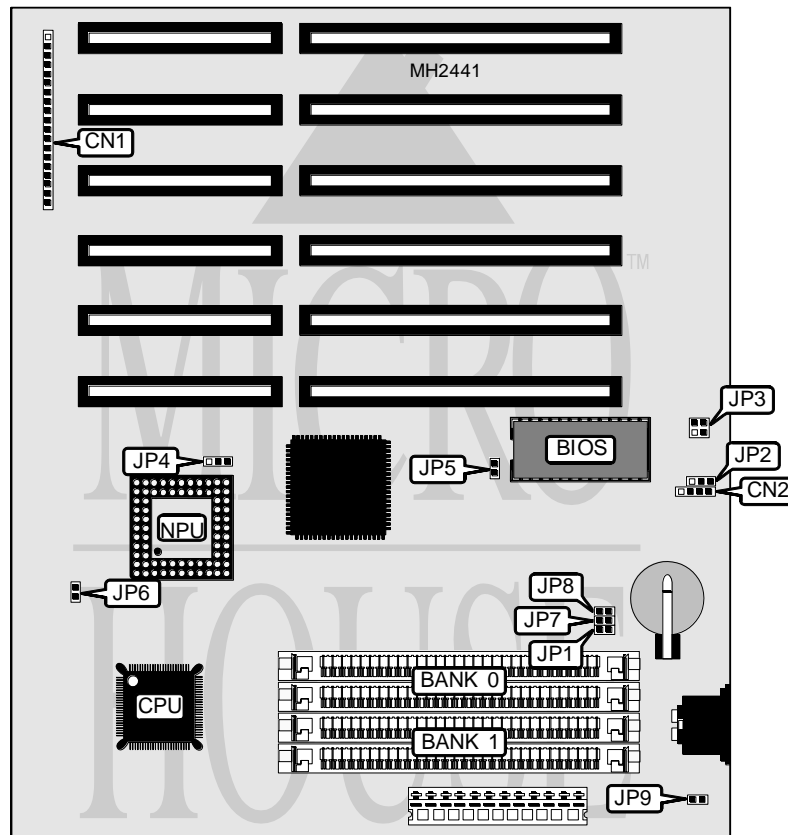
CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	CN1 (pins 1 - 4)	Turbo switch	CN1 (pins 14 - 16)
Power LED & keylock	CN1 (pins 5 - 9)	+5v ground	CN1 (pins 17 - 18)
Turbo LED	CN1 (pins 10 - 11)	External battery	CN2
Reset switch	CN1 (pins 12 - 13)		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select monochrome	JP1	Open
Monitor type select color	JP1	Closed
í Battery type select internal	JP2	pins 1 & 2 closed
Battery type select external	JP2	pins 2 & 3 closed
í CMOS memory normal operation	JP3	pins 1 & 2 closed
CMOS memory clear	JP3	pins 3 & 4 closed
í AT bus clock select synchronous	JP4	pins 2 & 3 closed
AT bus clock select asynchronous	JP4	pins 1 & 2 closed
í Factory configured - do not alter	JP5	Closed
í Pipeline mode disabled	JP6	Open
Pipeline mode enabled	JP6	Closed
í Factory configured - do not alter	JP9	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512K	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9

TURBO CONTROL CONFIGURATION		
Type	JP7	JP8
8042 controller (30 pin)	Open	N/A
8042 controller (23 pin)	N/A	Closed