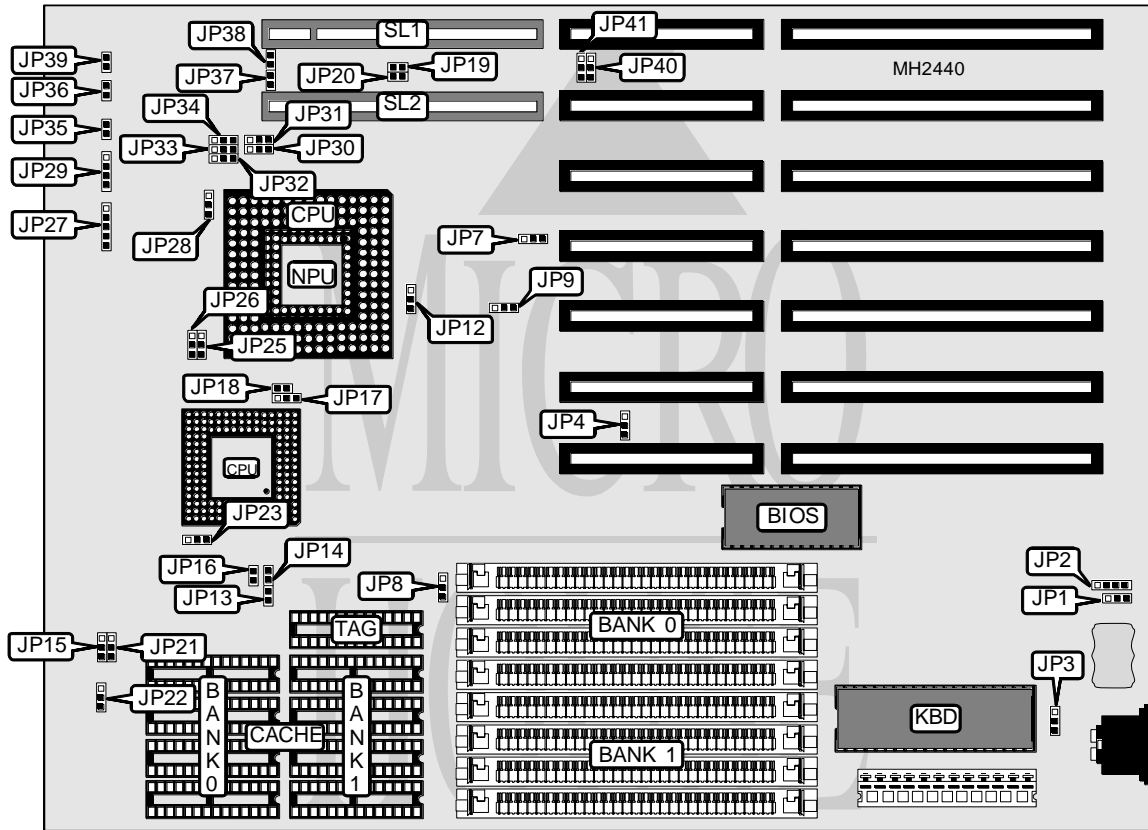


SEANIX TECHNOLOGY, INC.

3486-VLB

Processor	80386DX/CX486DLC/80486SX/80487SX/80486DX/80486DX2
Processor Speed	20/25/33/40/50(internal)/50/66(internal)MHz
Chip Set	EFAR
Max. Onboard DRAM	32MB
Cache	32/64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	80387DX



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP2	Turbo LED	JP36
Power LED & keylock	JP27	Turbo switch	JP39
Speaker	JP29	32-bit VESA local bus slots	SL1 & SL2
Reset switch	JP35		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation/internal battery	JP1	pins 1 & 2 closed
CMOS memory clear/external battery	JP1	pins 2 & 3 closed
í Factory configured - do not alter	JP3	Open
í Factory configured - do not alter	JP20	Open
í Factory configured - do not alter	JP40	pins 2 & 3 closed
í Factory configured - do not alter	JP41	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
32KB	(4) 8K x 8	NONE	(1) 8K x 8
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION							
Size	JP13	JP14	JP15	JP16	JP21	JP22	JP23
32KB	Open	Open	2 & 3	Open	1 & 2	2 & 3	2 & 3
64KB	Open	Open	2 & 3	Closed	1 & 2	1 & 2	1 & 2
128KB	Closed	Open	1 & 2	Closed	2 & 3	1 & 2	2 & 3
256KB	Closed	Closed	1 & 2	Closed	1 & 2	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION								
Type	JP4	JP7	JP8	JP9	JP12	JP17	JP18	JP19
80386SX	1 & 2	1 & 2	1 & 2	1 & 2	Open	Open	Open	Closed
CX486DLC	2 & 3	1 & 2	2 & 3	1 & 2	Open	Open	Open	Closed
80486SX	2 & 3	2 & 3	1 & 2	2 & 3	Open	2 & 3	Open	Open
80487SX	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2	Closed	Open
80486DX	2 & 3	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2	Closed	Open
80486DX2	2 & 3	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2	Closed	Open

Note: Pins designated should be in the closed position.

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CPU TYPE CONFIGURATION (CON'T)							
Type	JP25	JP26	JP28	JP31	JP32	JP37	JP38
80386SX	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3	Open	Open
CX486DLC	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3	Open	Open
80486SX	2 & 3	2 & 3	2 & 3	2 & 3	1 & 2	Closed	Closed
80487SX	2 & 3	2 & 3	2 & 3	2 & 3	1 & 2	Closed	Closed
80486DX	2 & 3	2 & 3	2 & 3	2 & 3	1 & 2	Closed	Closed
80486DX2	2 & 3	2 & 3	2 & 3	2 & 3	1 & 2	Closed	Closed

Note: Pins designated should be in the closed position.

VESA ID BUS CONFIGURATION	
ID	JP34
ID2 = 0	pins 2 & 3 closed
ID2 = 1	pins 1 & 2 closed

VESA ID BUS CONFIGURATION	
ID	JP33
ID3 = 0	pins 2 & 3 closed
ID3 = 1	pins 1 & 2 closed

VESA ID BUS CONFIGURATION	
ID	JP30
ID4 = 0	pins 2 & 3 closed
ID4 = 1	pins 1 & 2 closed