SEANIX TECHNOLOGY, INC.
4 8 6 L C 1

Processor	80386DX/80486SX/80487SX/80486DX/80486DX2
Processor Speed	20/25/33/40/50(internal)/50/66(internal)MHz
Chip Set	Contaq
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	None
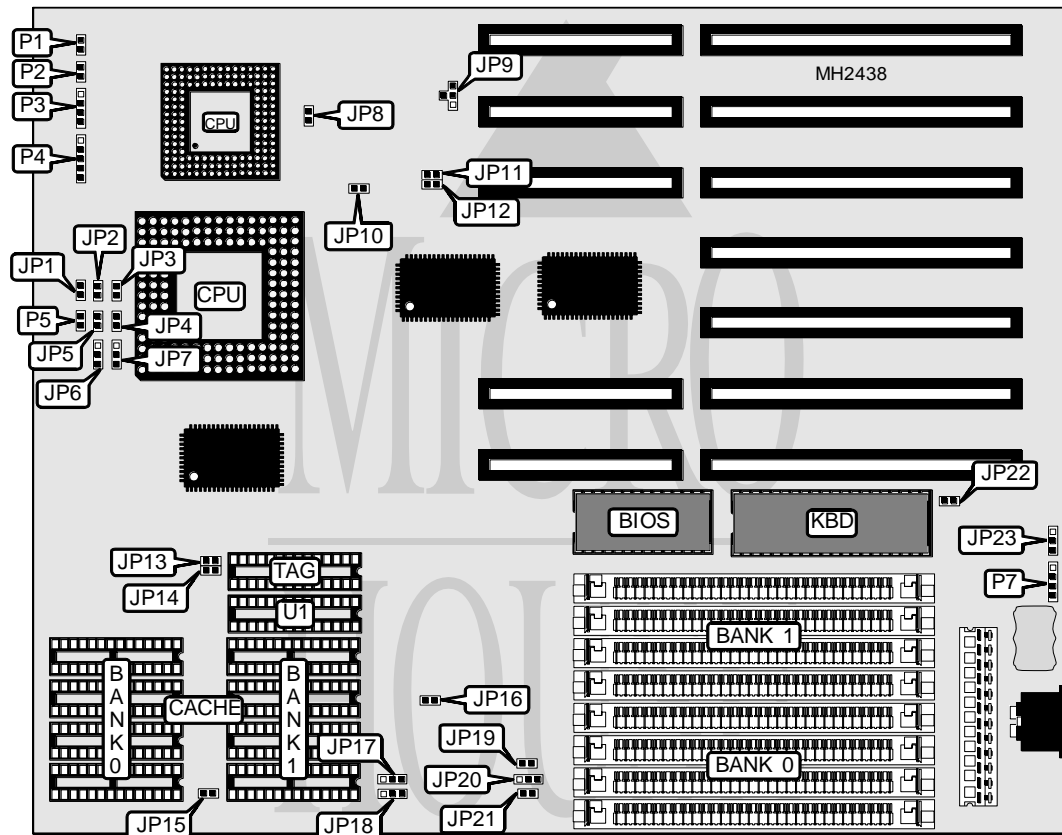
CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	P1	Power LED & keylock	P4
Turbo switch	P2	Reset switch	P5
Speaker	P3	External battery	P7

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Monitor type select color	JP22	Closed
Monitor type select monochrome	JP22	Open
í CMOS memory normal operation	JP23	pins 1 & 2 closed
CMOS memory clear	JP23	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG	DIRTY (U1)
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION						
Size	JP13	JP14	JP15	JP16	JP19	JP20
64KB	Open	Open	Open	Open	2 & 3	Open
128KB	Closed	Open	Closed	Open	1 & 2	2 & 3
256KB	Closed	Closed	Closed	Closed	2 & 3	1 & 2
Note: Pins designated should be in the closed position.						

CPU TYPE CONFIGURATION							
Type	JP1	JP2	JP3	JP4	JP5	JP6	JP7
80386DX	Open	Open	Open	Open	Open	Open	Open
CX486DLC	Closed	Closed	Closed	Open	Open	Open	Open
80486SX	Open	Open	Open	Open	Open	2 & 3	1 & 2
80487SX	Open	Open	Open	Closed	Closed	2 & 3	1 & 2
80486DX	Open	Open	Open	Open	Closed	2 & 3	2 & 3
80486DX2	Open	Open	Open	Open	Closed	2 & 3	2 & 3
Note: Pins designated should be in the closed position.							

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CPU TYPE CONFIGURATION (CON'T)							
Type	JP8	JP9	JP10	JP17	JP18	JP21	JP24
80386DX	Closed	Open	Open	2 & 3	2 & 3	2 & 3	1 & 2
CX486DLC	Closed	Open	Open	2 & 3	2 & 3	2 & 3	N/A
80486SX	Open	1 & 2	Closed	1 & 2	1 & 2	1 & 2	2 & 3
80487SX	Open	1 & 2	Closed	1 & 2	1 & 2	1 & 2	2 & 3
80486DX	Open	1 & 2	Closed	1 & 2	1 & 2	1 & 2	2 & 3
80486DX2	Open	1 & 2	Closed	1 & 2	1 & 2	1 & 2	2 & 3
Note: Pins designated should be in the closed position. JP24 may not be present on all boards. The location is unidentified.							

80386 ATCLK CONFIGURATION		
ATCLK	JP11	JP12
4	Closed	Closed
6	Closed	Open
8	Open	Closed
10	Open	Open

80486 ATCLK CONFIGURATION		
ATCLK	JP11	JP12
3	Closed	Open
4	Open	Closed
5	Open	Open
6	Closed	Closed