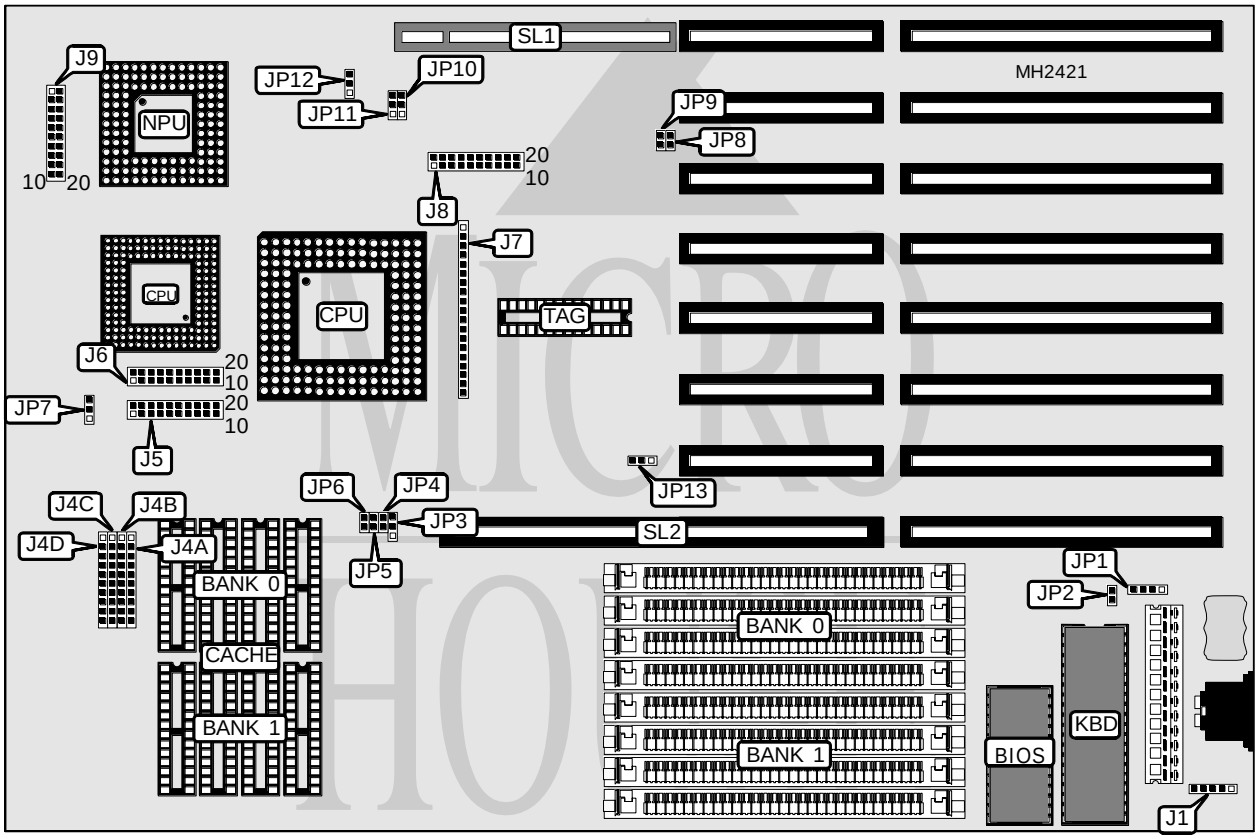
FUJIKAMA USA, INC.
CONTAQ 486/386

Processor	80386DX/80486SX/P23/80487SX/P23T/80486DX/80486DX2
Processor Speed	16/20/25/33/40/50(internal)/50/66(internal)MHz
Chip Set	Contaq
Max. Onboard DRAM	64MB (32MB on external memory card)
Cache	64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit external memory card, 32-bit VESA local bus slots (1)
NPU Options	80387DX/3167



CONNECTIONS			
Purpose	Location	Purpose	Location
Optional keyboard connector	J1	Reset switch	J9 (pins 19 - 20)
Power LED & keylock	J9 (pins 1 - 5)	External battery	JP1
Speaker	J9 (pins 6 - 10)	32-bit external memory card	SL1
Turbo LED	J9 (pins 12 - 13)	32-bit VESA local bus slots	SL2
Turbo switch	J9 (pins 15 - 17)		

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FUJIKAMA USA, INC.
CONTAQ 486/386

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Battery type select internal	JP1	pins 2 & 3 closed
CMOS memory clear	JP1	Open
Battery type select external	JP1	Closed
í Monitor type select monochrome	JP2	Open
Monitor type select color	JP2	Closed
í Factory configured - do not alter	JP3	Open
í Factory configured - do not alter	JP4	Open
í Factory configured - do not alter	JP5	Open
í Factory configured - do not alter	JP6	Open
í OSC select OSC1	JP10	pins 1 & 2 closed
OSC select OSC2	JP10	pins 2 & 3 closed
í Normal setup used	JP13	pins 1 & 2 closed
SCO UNIX 3.22 with 64MB installed (Weitek will be disabled)	JP13	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
5MB	(4) 1M x 9	(4) 256K x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
17MB	(4) 256K x 9	(4) 4M x 9
17MB	(4) 4M x 9	(4) 256K x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9
Note: The external memory card uses the same configuration.		

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
Note: The 128KB configuration is not available on the 486 system.			

CACHE JUMPER CONFIGURATION				
Size	J4A	J4B	J4C	J4D
64KB	Open	Open	pins 1 - 10 closed	pins 1 - 10 closed
128KB	Open	pins 1 - 10 closed	Open	pins 1 - 10 closed
256KB	pins 1 - 10 closed	Open	pins 1 - 10 closed	Open

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FUJIKAMA USA, INC.
CONTAQ 486/386

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CPU TYPE CONFIGURATION			
Type	J5	J6	J8
80386	pins 1 - 10 closed	pins 1 - 10 closed	pins 1 - 10 closed
80486	pins 11 - 20 closed	pins 11 - 20 closed	pins 11 - 20 closed

CPU TYPE CONFIGURATION	
Type	J7
80486SX	pins 10 - 19 closed
P23	pins 10 - 19 closed
80487SX	pins 1 - 10 closed
P23T	pins 1 - 10 closed
80486DX	pins 5 - 14 closed
80486DX2	pins 5 - 14 closed

CPU SPEED CONFIGURATION			
Speed	JP10	JP11	JP12
20MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
25MHz (80386)	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
25MHz (80486)	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
40MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed
50iMHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
50MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
Note: OSC 1: 50MHz & OSC 2: 80MHz.			

CPU SPEED CONFIGURATION			
Speed	JP10	JP11	JP12
16MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
25MHz (80386)	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
25MHz (80486)	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
33MHz (80386)	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed
33MHz (80486)	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed
50iMHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
50MHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
66iMHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed
Note: OSC 1: 50MHz & OSC 2: 66MHz.			

CPU SPEED CONFIGURATION			
Speed	JP10	JP11	JP12
16MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed
20MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
33MHz (80386)	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
33MHz (80486)	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
40MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed
66iMHz	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed
Note: OSC 1: 66MHz & OSC 2: 80MHz.			

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FUJIKAMA USA, INC.
CONTAQ 486/386

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CPU BUS CLOCK CONFIGURATION (ASYNCHRONOUS)			
Type	JP7	JP8	JP9
Asychronous	pins 2 & 3 closed	Open	Open

CPU BUS CLOCK CONFIGURATION (SYNCHRONOUS)				
ATCLK (486)	ATCLK (386)	JP7	JP8	JP9
SYS/6	2SYS/4	pins 1 & 2 closed	Closed	Closed
SYS/4	2SYS/8	pins 1 & 2 closed	Closed	Open
SYS/3	2SYS/6	pins 1 & 2 closed	Open	Closed
2SYS/5	2SYS/10	pins 1 & 2 closed	Open	Open