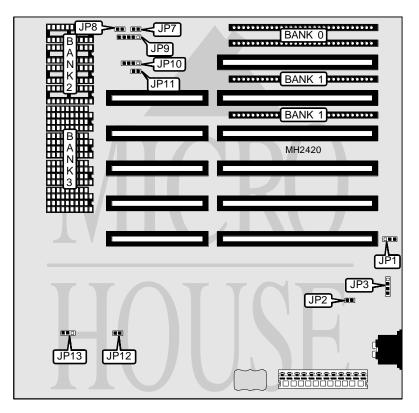
UNIDENTIFIED MAT 286

| Processor |
|-------------------|
| Processor Speed |
| Chip Set |
| Max. Onboard DRAM |
| Cache |
| BIOS |
| Dimensions |
| I/O Options |
| NPU Options |
| |

80286 12/16MHz Unidentified 4MB None Quadtel 220mm x 220mm None 80287



| CONNECTIONS | | | | | |
|------------------|----------|---------------------|----------|--|--|
| Purpose | Location | Purpose | Location | | |
| External battery | JP3 | Power LED & keylock | JP9 | | |
| Turbo switch | JP7 | Speaker | JP10 | | |
| Reset switch | JP8 | Turbo LED | JP11 | | |

Continued on next page. . .

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| USER CONFIGURABLE SETTINGS | | | | | |
|----------------------------------|--------|-------------------|--|--|--|
| Function | Jumper | Position | | | |
| í Turbo switch enabled | JP1 | pins 2 & 3 closed | | | |
| Turbo switch disabled | JP1 | pins 1 & 2 closed | | | |
| í Monitor type select color | JP2 | Closed | | | |
| Monitor type select monochrome | JP2 | Open | | | |
| í AT bus clock speed select Y3/4 | JP12 | Closed | | | |
| AT bus clock speed select Y2/4 | JP12 | Open | | | |
| í NPU speed select Y3/3 | JP13 | pins 1 & 2 closed | | | |
| NPU speed select Y4/3 | JP13 | pins 2 & 3 closed | | | |

| DRAM CONFIGURATION | | | | | | |
|--------------------|--------------|------------|------------|------------|--|--|
| Size | Bank 0 | Bank 1 | Bank 2 | Bank 3 | | |
| 512KB | NONE | NONE | (4) 44256 | NONE | | |
| 512KB | (2) 256K x 9 | NONE | NONE | NONE | | |
| 640KB | NONE | NONE | (4) 44256 | (4) 4464 | | |
| 1MB | NONE | NONE | (4) 44256 | (4) 44256 | | |
| 2MB | (2) 1M x 9 | NONE | NONE | NONE | | |
| 2MB | NONE | NONE | (4) 441000 | NONE | | |
| 2.5MB | NONE | NONE | (4) 44256 | (4) 441000 | | |
| 2.5MB | NONE | (2) 1M x 9 | (4) 44256 | NONE | | |
| 4MB | NONE | NONE | (4) 441000 | (4) 441000 | | |
| 4MB | (2) 1M x 9 | (2) 1M x 9 | NONE | NONE | | |