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Processor	80386SX
Processor Speed	16MHz
Chip Set	Unidentified
Max. Onboard DRAM	4MB
Cache	None
BIOS	Award
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80387SX
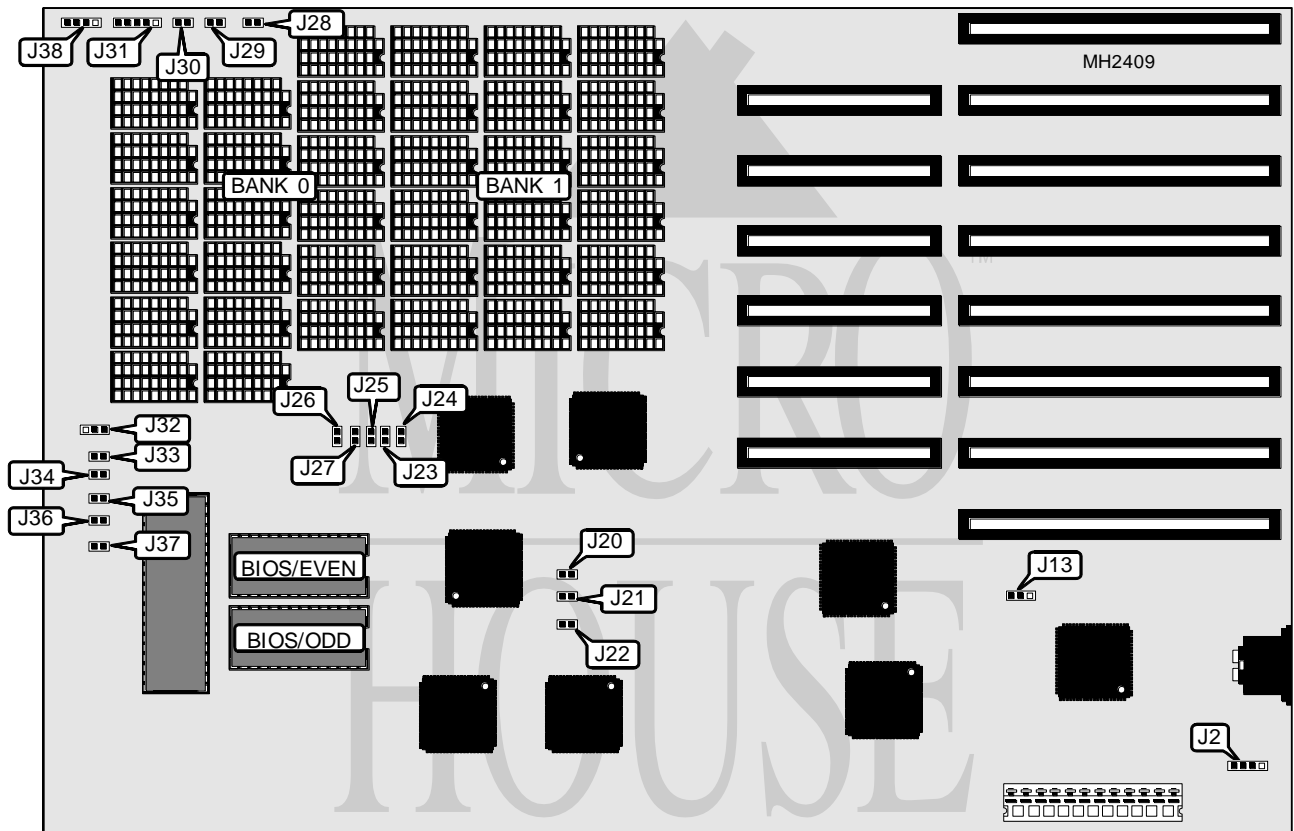
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Turbo LED	J30
Turbo switch	J28	Power LED & keylock	J31
Reset switch	J29	Speaker	J38

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Parity checking enabled	J13	pins 1 & 2 closed
Parity checking disabled	J13	pins 2 & 3 closed
í Factory configured - do not alter	J20	N/A
í BIOS type select 27256	J21	Closed
BIOS type select 27128	J21	Open
í ROM wait state select 3 wait states	J22	Open
ROM wait state select 2 wait states	J22	Closed
í RAM read wait state select 1 wait state	J23	Open
RAM read wait state select 0 wait state	J23	Closed
í RAM write wait state select 1 wait state	J25	Open
RAM write wait state select 0 wait state	J25	Closed
í Turbo switch select jumper	J32	pins 1 & 2 closed
Turbo switch select keyboard	J32	pins 2 & 3 closed
í ROM to RAM mapping disabled	J33	Closed
ROM to RAM mapping enabled	J33	Open
í Monitor type select CGA	J37	Closed
Monitor type select Mono/EGA/VGA	J37	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(18) 41256	NONE
1MB	(18) 41256	(18) 41256
2MB	(18) 411000	NONE
4MB	(18) 411000	(18) 411000

DRAM JUMPER CONFIGURATION			
Size	J34	J35	J36
512KB	Closed	Closed	Open
1MB	Closed	Open	Closed
2MB	Open	Closed	Open
4MB	Open	Open	Closed

PAGEMODE CONFIGURATION		
Function	Jumper	Position
í Interleave disabled	J24	Open
Interleave enabled (requires both memory banks full)	J24	Closed
í Pagemode disabled	J26	Open
Pagemode enabled	J26	Closed
í Pagemode controller installed	J27	Open
Pagemode controller not installed	J27	Closed
Note: J23 & J25 must be open to operate in pagemode.		