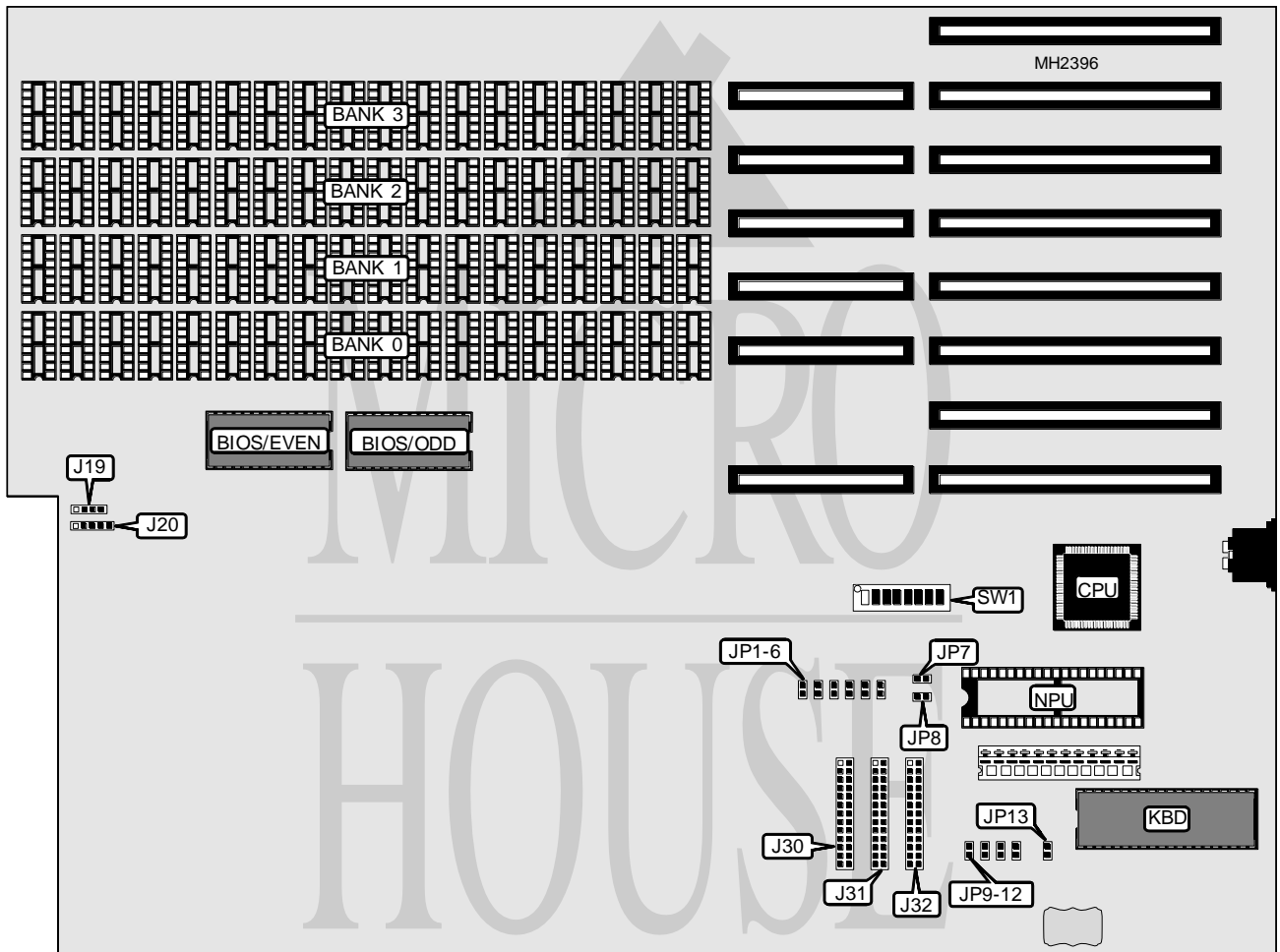


# ADVANCED LOGIC RESEARCH, INC.

## DART 12.5

<b>Processor</b>	80286
<b>Processor Speed</b>	16/20MHz
<b>Chip Set</b>	ALR
<b>Max. Onboard DRAM</b>	2MB
<b>Cache</b>	None
<b>BIOS</b>	Phoenix
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	Serial port, parallel port
<b>NPU Options</b>	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J19	Serial port 1	J31
Power LED & keylock	J20	Serial port 2 (optional)	J32
Parallel port	J30		

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**DART 12.5**

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í Factory configured - do not alter	JP12	N/A
í Turbo enabled	JP13	Closed
Turbo disabled	JP13	Open
í Monitor type select monochrome	SW1/1	Off
Monitor type select color	SW1/1	On
í Memory mode select system memory between 0-640KB	SW1/2	On
Memory mode select system memory between 0-512KB	SW1/2	Off

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(18) 41256	NONE	NONE	NONE
1MB	(18) 41256	(18) 41256	NONE	NONE
1.5KB	(18) 41256	(18) 41256	(18) 41256	NONE
2MB	(18) 41256	(18) 41256	(18) 41256	(18) 41256

DRAM SWITCH CONFIGURATION		
Size	SW1/3	SW1/4
512KB	On	On
1MB	On	Off
1.5MB	Off	On
2MB	Off	Off

BIOS TYPE CONFIGURATION						
Type	JP1	JP2	JP3	JP4	JP5	JP6
27128	Closed	Open	Closed	Closed	Open	Open
27256	Closed	Open	Open	Open	Closed	Closed

SERIAL PORT SWITCH CONFIGURATION			
Serial 1	Serial 2	SW1/5	SW1/6
Disabled	Disabled	On	On
COM1	Disabled	Off	On
COM2	Disabled	On	Off
COM1	COM2	Off	Off
COM1	Disabled	Off	On
COM1	COM2	On	Off
Disabled	Disabled	Off	Off

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## DART 12.5

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SERIAL PORT JUMPER CONFIGURATION				
Serial 1	Serial 2	JP9	JP10	JP11
Disabled	Disabled	Open	Open	Open
COM1	Disabled	Open	Closed	Open
COM2	Disabled	Closed	Open	Closed
COM1	COM2	Open	Closed	Closed
COM1	Disabled	Open	Closed	Open
COM1	COM2	Open	Closed	Closed
Disabled	Disabled	Open	Open	Open

PARALLEL PORT CONFIGURATION				
Parallel	JP7	JP8	SW1/7	SW1/8
LPT1	Closed	Open	On	On
LPT2	Open	Closed	Off	On
Disabled	Open	Open	Off	Off