TMC RESEARCH CORPORATION
PAT3XAL (VER. 2.0)

Processor	80386SX/CX486SLC
Processor Speed	20/25/33/40MHz
Chip Set	VLSI
Max. Onboard DRAM	16MB
Cache	None
BIOS	Unidentified
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80387SX
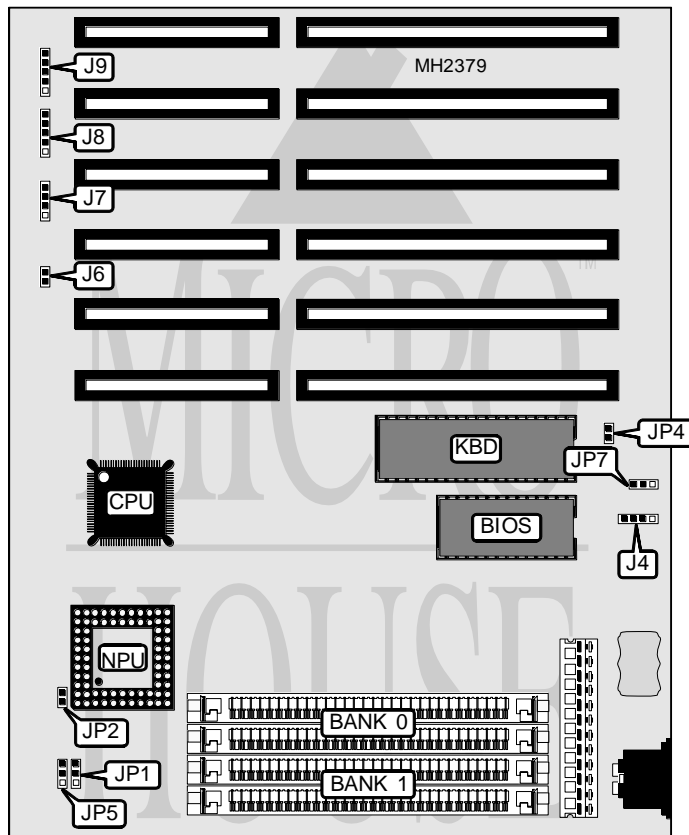
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J4	Turbo LED	J8 (pins 1 - 2)
Reset switch	J6	Turbo switch	J8 (pins 4 - 5)
Speaker	J7	Power LED & keylock	J9

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í NPU synchronous with CPU	JP2	Open
NPU asynchronous with CPU	JP2	Closed
í Monitor type select monochrome	JP4	Open
Monitor type select color	JP4	Closed
í Battery type select internal	JP7	pins 1 & 2 closed
Battery type select external	JP7	Open
CMOS memory clear	JP7	pins 2 & 3 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9

CPU SPEED CONFIGURATION		
Speed	JP1	JP5
20MHz	pins 2 & 3 closed	pins 1 & 2 closed
25MHz	pins 1 & 2 closed	pins 2 & 3 closed
33MHz	pins 2 & 3 closed	pins 2 & 3 closed
40MHz	pins 1 & 2 closed	pins 1 & 2 closed