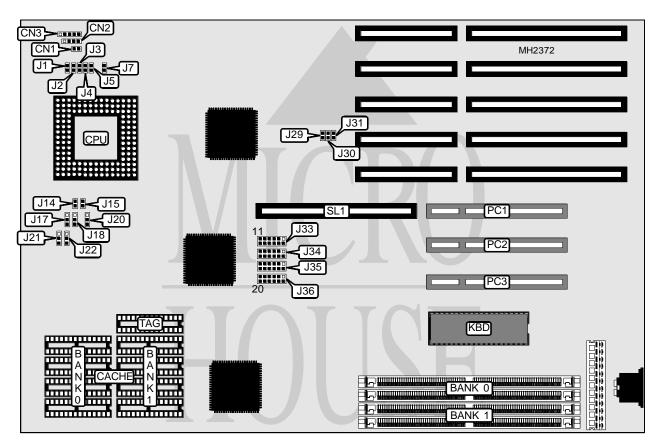
CHAINTECH COMPUTER COMPANY, LTD. 4861CP

Processor Processor Speed Chip Set Max. Onboard DRAM Cache BIOS Dimensions I/O Options NPU Options 80486SX/80487SX/80486DX/ODP486SX/80486DX2 25/33/50(internal)/66(internal)MHz Intel 128MB 128/256/512KB Award/Phoenix 330mm x 218mm 32-bit PCI local bus slots (3), special slot (1) None



CONNECTIONS			
Purpose Location Purpose Location			
Reset switchCN132-bit PCI bus slotsPCI1 - PCI3			
Speaker CN2 Special slot SL1			
Power LED & keylock CN3			
Note: The purpose of SL1 is unspecified by the manufacturer.			

Continued on next page. . .

CHAINTECH COMPUTER CO. LTD. 486ICP

... continued from previous page

USER CONFIGURABLE SETTINGS			
Function Jumper Position			
í Factory configured - do not alter	J7	Open	
í Factory configured - do not alter	J20	Open	
í Factory configured - do not alter	J22	Open	

	DRAM CONFIGURATION	
Size	Bank O	Bank 1
2MB	(2) 256K x 36	NONE
4MB	(2) 256K x 36	(2) 256K x 36
4MB	(2) 512K x 36	NONE
6MB	(2) 256K x 36	(2) 512K x 36
6MB	(2) 512K x 36	(2) 256K x 36
8MB	(2) 512K x 36	(2) 512K x 36
8MB	(2) 1M x 36	NONE
10MB	(2) 256K x 36	(2) 1M x 36
10MB	(2) 1M x 36	(2) 256K x 36
12MB	(2) 512K x 36	(2) 1M x 36
12MB	(2) 1M x 36	(2) 512K x 36
16MB	(2) 1M x 36	(2) 1M x 36
16MB	(2) 2M x 36	NONE
18MB	(2) 256K x 36	(2) 2M x 36
18MB	(2) 2M x 36	(2) 256K x 36
20MB	(2) 512K x 36	(2) 2M x 36
20MB	(2) 2M x 36	(2) 512K x 36
24MB	(2) 1M x 36	(2) 2M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 2M x 36	(2) 2M x 36
32MB	(2) 4M x 36	NONE
34MB	(2) 256K x 36	(2) 4M x 36
34MB	(2) 4M x 36	(2) 256K x 36
36MB	(2) 512K x 36	(2) 4M x 36
36MB	(2) 4M x 36	(2) 512K x 36
40MB	(2) 1M x 36	(2) 4M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 2M x 36	(2) 4M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 4M x 36	(2) 4M x 36
64MB	(2) 8M x 36	NONE
66MB	(2) 256K x 36	(2) 8M x 36
66MB	(2) 8M x 36	(2) 256K x 36
68MB	(2) 512K x 36	(2) 8M x 36
68MB	(2) 8M x 36	(2) 512K x 36
72MB	(2) 1M x 36	(2) 8M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 2M x 36	(2) 8M x 36

Continued on next page. . .

CHAINTECH COMPUTER CO. LTD. 486ICP

. . . continued from previous page

DRAM CONFIGURATION (CON'T)				
Size	Bank 0	Bank 1		
80MB	(2) 8M x 36	(2) 2M x 36		
96MB	(2) 4M x 36	(2) 8M x 36		
96MB	(2) 8M x 36	(2) 4M x 36		
128MB	(2) 8M x 36	(2) 8M x 36		

CACHE CONFIGURATION			
Size Bank 0 Bank 1 TAG			
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB (4) 32K x 8 (4) 32K x 8 (1) 32K x 8		(1) 32K x 8	
512KB	(4) 128K x 8	NONE	(1) 32K x 8

CACHE JUMPER CONFIGURATION				
Size J14 J15 J17 J18				
128KB	Open	Open	pins 2 & 3 closed	pins 1 & 2 closed
256KB	256KB Closed Open pins 1 & 2 closed pins 2 & 3 closed			
512KB	Closed	Closed	pins 2 & 3 closed	pins 2 & 3 closed

	CPU TYPE CONFIGURATION				
Туре	J1	J2	J3	J4	J5
80486SX	Open	Open	Open	Closed	Open
ODP486SX	Open	Closed	Closed	Open	Closed
80487SX	Open	Closed	Closed	Open	Closed
80486DX	Closed	Open	Closed	Open	Closed
80486DX2	Closed	Open	Closed	Open	Closed

	CPU SPEED CONFIGURATION				
Speed	J21	JP29	JP30	JP31	
25MHz	pins 1 & 2 closed	Open	Open	Closed	
33MHz	pins 2 & 3 closed	Closed	Open	Open	
50iMHz	pins 1 & 2 closed	Open	Open	Closed	
66iMHz	pins 2 & 3 closed	Closed	Open	Open	

PCI IRQ CONFIGURATION (INT A)		
IRQ	J33	
IRQ5	pins 11 & 12 closed	
IRQ9	pins 9 & 10 closed	
IRQ10	pins 7 & 8 closed	
IRQ11	pins 5 & 6 closed	
IRQ14	pins 3 & 4 closed	
IRQ15	pins 1 & 2 closed	

Continued on next page. . .

CHAINTECH COMPUTER CO. LTD. 486ICP

... continued from previous page

PCI IRQ CONFIGURATION (INT B)		
IRQ	J36	
IRQ5	pins 11 & 12 closed	
IRQ9	pins 9 & 10 closed	
IRQ10	pins 7 & 8 closed	
IRQ11	pins 5 & 6 closed	
IRQ14	pins 3 & 4 closed	
IRQ15	pins 1 & 2 closed	

PCI IRQ CONFIGURATION (INT C)		
IRQ	J35	
IRQ5	pins 11 & 12 closed	
IRQ9	pins 9 & 10 closed	
IRQ10	pins 7 & 8 closed	
IRQ11	pins 5 & 6 closed	
IRQ14	pins 3 & 4 closed	
IRQ15	pins 1 & 2 closed	

PCI IRQ CONFIGURATION (INT D)		
IRQ	J34	
IRQ5	pins 11 & 12 closed	
IRQ9	pins 9 & 10 closed	
IRQ10	pins 7 & 8 closed	
IRQ11	pins 5 & 6 closed	
IRQ14	pins 3 & 4 closed	
IRQ15	pins 1 & 2 closed	