NORTHGATE COMPUTER SYSTEMS, INC. NORTHGATE ENERGY STAR LX400A/G REV. 3.1

Processor
Processor Speed
Chip Set
Max. Onboard DRAM
Cache
BIOS
Dimensions
I/O Options
NPU Options

804865X/804875X/80486DX/80486DX2 25/33/40/50(internal)/50/66(internal)MHz SIS 32MB 64/128/256KB AMI 254mm x 220mm 32-bit VESA local bus slots (2), PS/2 mouse port, green PC connector None



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	J3	Flash LED	J10
Turbo switch	J4	PS/2 mouse port	J11
Speaker	J5	Green PC connector	J12
Reset switch	J6	Green PC connector (monitor)	J28
Power LED & keylock	J7	32-bit VESA local bus slots	SL1 & SL2

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USER CONFIGURABLE SETTINGS			
Function	Jumper	Position	
í Factory configured - do not alter	JP1	Open	
í Monitor type select color	JP2	Closed	
Monitor type select monochrome	JP2	Open	
í On board mouse enabled	JP12	pins 1 & 2 closed	
On board mouse disabled	JP12	pins 2 & 3 closed	
í Factory configured - do not alter	JP13	pins 2 & 3 closed	
í Power good signal detect from power supply	JP14	pins 1 & 2 closed	
Power good signal detect from board	JP14	pins 2 & 3 closed	
í Factory configured - do not alter	JP21	Open	
í Factory configured - do not alter	JP22	Open	
í Factory configured - do not alter	JP23	Open	
í Factory configured - do not alter	JP100	Open	

DRAM CONFIGURATION				
Size	Bank 0	Bank 1		
1MB	(4) 256K x 9	NONE		
2MB	(4) 256K x 9	(4) 256K x 9		
4MB	(4) 1M x 9	NONE		
8MB	(4) 1M x 9	(4) 1M x 9		
16MB	(4) 4M x 9	NONE		
20MB	(4) 1M x 9	(4) 4M x 9		
32MB	(4) 4M x 9	(4) 4M x 9		

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG	
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8	
128KB	(4) 32K x 8	NONE	(1) 8K x 8	
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	

CACHE JUMPER CONFIGURATION					
Size	SR1	SR2	SR3	SR4	SR5
64KB	2 & 3	2&3	1 & 2	1&2	1&2
128KB	1 & 2	1&2	2 & 3	2&3	1&2
256KB	2 & 3	2&3	2 & 3	2&3	2&3
Note: Pins designated should be in the closed position.					

CPU TYPE CONFIGURATION			
Туре	JP6	JP7	JP8
80486SX	Open	Open	pins 2 & 3 closed
80487SX	Closed	pins 2 & 3 closed	pins 1 & 2 closed
80486DX	Closed	pins 1 & 2 closed	pins 1 & 2 closed
80486DX2	Closed	pins 1 & 2 closed	pins 1 & 2 closed

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CPU SPEED CONFIGURATION				
Speed	JP10	JP11		
25MHz	pins 2 & 3 closed	pins 2 & 3 closed		
33MHz	pins 2 & 3 closed	pins 1 & 2 closed		
40MHz	pins 1 & 2 closed	pins 2 & 3 closed		
50iMHz	pins 2 & 3 closed	pins 2 & 3 closed		
50MHz	pins 1 & 2 closed	pins 1 & 2 closed		
66iMHz	pins 2 & 3 closed	pins 1 & 2 closed		

VESA WAIT STATE CONFIGURATION				
Wait states	JP24	JP26		
0 wait states	pins 1 & 2 closed	pins 1 & 2 closed		
1 wait state	pins 2 & 3 closed	pins 2 & 3 closed		

BUS SPEED CONFIGURATION				
CPU speed	JP25	JP27		
<= 33MHz	pins 1 & 2 closed	pins 1 & 2 closed		
> 33MHz	pins 2 & 3 closed	pins 2 & 3 closed		