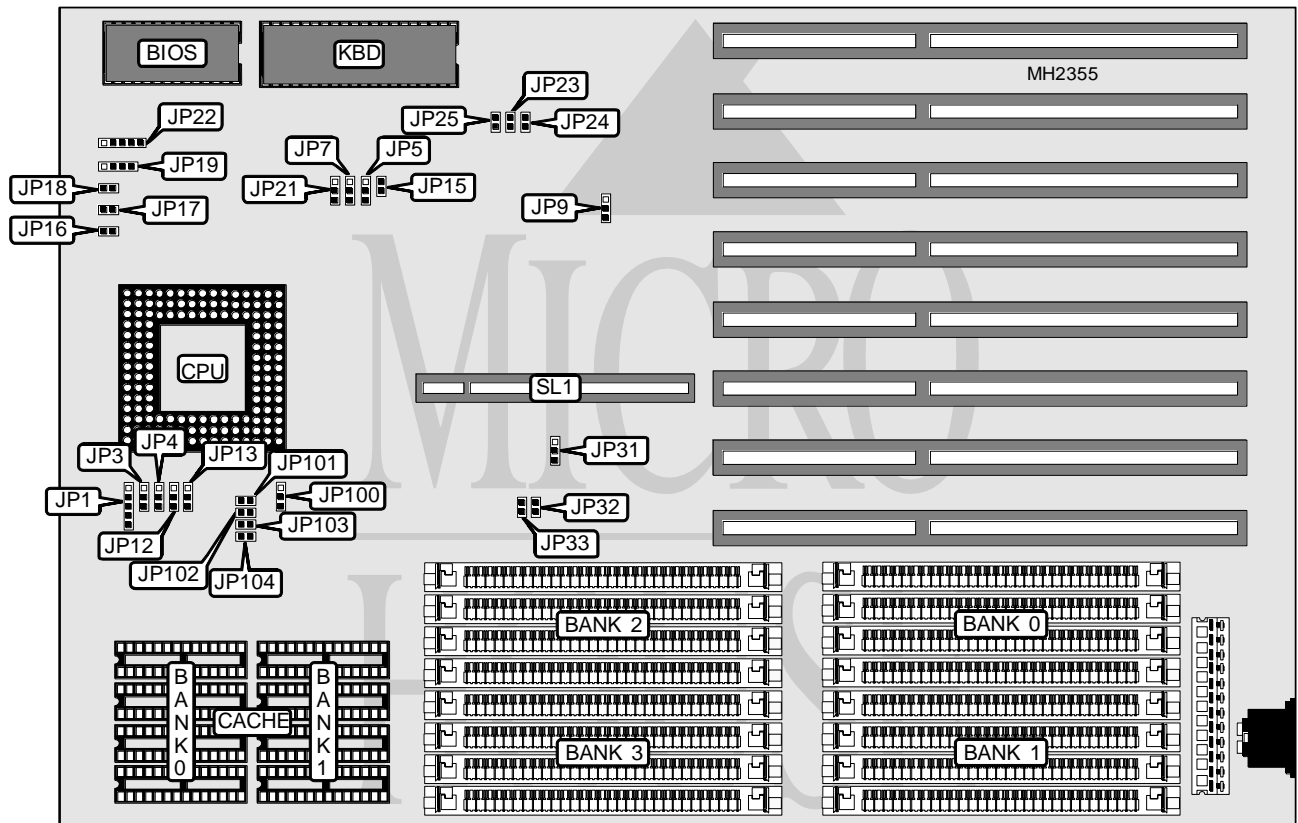


LION COMPUTERS, INC. SUPER EISA REV. 1

Processor	80486SX/80487SX/80486DX/ODP486SX/80486DX2
Processor Speed	20/25/33/40/50(internal)/50/66(internal)/80(internal)MHz
Chip Set	Unidentified
Max. Onboard DRAM	256MB
Cache	256/512/1024KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Turbo LED	JP16	Speaker	JP19
Turbo switch	JP17	Power LED & keylock	JP22
Reset switch	JP18	32-bit VESA local bus slots	SL1

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP9	pins 1 & 2 closed
í Factory configured - do not alter	JP21	pins 1 & 2 closed
í Factory configured - do not alter	JP23	Closed
í Monitor type select monochrome	JP24	Closed
Monitor type select color	JP24	Open
í CMOS memory normal operation	JP25	Open
CMOS memory clear	JP25	Closed
í Factory configured - do not alter	JP31	Closed
í Factory configured - do not alter	JP32	Closed
í Factory configured - do not alter	JP33	Closed
í Factory configured - do not alter	JP100	pins 1 & 2 closed
í Oscillator type select normal	JP101	Open
Oscillator type select clock generation	JP101	Closed

RAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 1M x 9	NONE	NONE	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9
20MB	(4) 1M x 9	(4) 4M x 9	NONE	NONE
24MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	NONE
40MB	(4) 1M x 9	(4) 1M x 9	(4) 4M x 9	(4) 4M x 9
16MB	(4) 4M x 9	NONE	NONE	NONE
32MB	(4) 4M x 9	(4) 4M x 9	NONE	NONE
48MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	NONE
64MB	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9	(4) 4M x 9
64MB	(4) 16M x 9	NONE	NONE	NONE
128MB	(4) 16M x 9	(4) 16M x 9	NONE	NONE
144MB	(4) 16M x 9	(4) 16M x 9	(4) 4M x 9	NONE
160MB	(4) 16M x 9	(4) 16M x 9	(4) 4M x 9	(4) 4M x 9
192MB	(4) 16M x 9	(4) 16M x 9	(4) 16M x 9	NONE
256MB	(4) 16M x 9	(4) 16M x 9	(4) 16M x 9	(4) 16M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
256KB	(4) 32K x 8	(4) 32K x 8	(3) 64K x 4
512KB	(4) 128K x 8	NONE	(3) 64K x 4
1MB	(4) 128K x 8	(4) 128K x 8	(3) 64K x 4

Note: The location of the TAG chips are unidentified.

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CACHE JUMPER CONFIGURATION					
Size	JP1	JP3	JP4	JP12	JP13
256KB	2 & 3	1 & 2	1 & 2	1 & 2	1 & 2
512KB	1 & 2	1 & 2	2 & 3	1 & 2	2 & 3
1MB	4 & 5	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION			
Type	JP5	JP7	JP15
80486SX	Open	pins 2 & 3 closed	Open
ODP486SX	pins 2 & 3 closed	pins 1 & 2 closed	Closed
80487SX	pins 2 & 3 closed	pins 1 & 2 closed	Closed
80486DX	pins 1 & 2 closed	pins 1 & 2 closed	Closed
80486DX2	pins 1 & 2 closed	pins 1 & 2 closed	Closed

CPU CLOCK SPEED CONFIGURATION			
Speed	JP102	JP103	JP104
20MHz	Open	Open	Open
25MHz	Open	Open	Closed
32MHz	Open	Closed	Open
33MHz	Closed	Closed	Closed
40MHz	Open	Closed	Closed
50iMHz	Open	Open	Closed
50MHz	Closed	Open	Open
66iMHz	Closed	Closed	Closed
66MHz	Closed	Open	Closed
80iMHz	Closed	Closed	Open