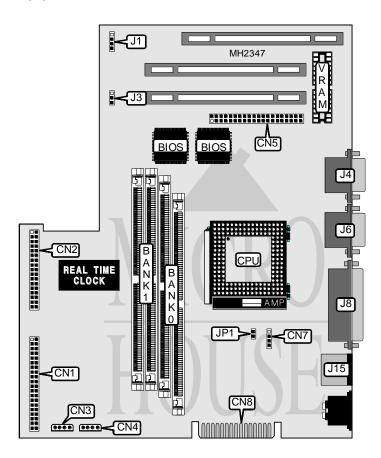
## REPLY CORPORATION POWERBOARD 50/50Z

Processor	804865X2/80486DX2/80486DX4
Processor Speed	25/33/50(internal)/66(internal)/100(internal)MHz
Chip Set	C & T
Max. Onboard DRAM	64MB
Cache	None
BIOS	Reply
Dimensions	254mm x 220mm
I/O Options	Floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse port, serial port, VGA
	port
NPU Options	None



CONNECTIONS					
Purpose	Location	Purpose	Location		
IDE interface	CN1	Power supply connector	CN8		
Floppy drive interface	CN2	Speaker	J3		
Hard drive power	CN3	VGA port	J4		
Hard drive power	CN4	Serial port	J6		
IDE interface	CN5	Parallel port	J8		
Chassis fan power	CN7	PS/2 mouse port	J15		

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## REPLY CORPORATION POWERBOARD 50/50Z

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USER CONFIGURABLE SETTINGS			
Function	Jumper	Position	
í CMOS memory normal operation	J1	N/A	
CMOS memory clear	J1	N/A	
Note: The CMOS configuration is cleared by switching the jumper from one position to the other. If jumper is on pins 2 & 3, to clear CMOS, switch to pins 1 & 2 closed. If jumper is on pins 1 & 2, to clear CMOS, switch to			

pins 2 & 3 closed.

DRAM CONFIGURATION			
Size	Bank 0	Bank 1	
8MB	(2) 1M x 36	NONE	
16MB	(2) 1M x 36	(2) 1M x 36	
16MB	(2) 2M x 36	NONE	
24MB	(2) 1M x 36	(2) 2M x 36	
24MB	(2) 2M x 36	(2) 1M x 36	
32MB	(2) 2M x 36	(2) 2M x 36	
32MB	(2) 4M x 36	NONE	
40MB	(2) 1M x 36	(2) 4M x 36	
40MB	(2) 4M x 36	(2) 1M x 36	
48MB	(2) 2M x 36	(2) 4M x 36	
48MB	(2) 4M x 36	(2) 2M x 36	
64MB	(2) 4M x 36	(2) 4M x 36	
64MB	(2) 8M x 36	NONE	

CPU SPEED CONFIGURATION			
Speed	JP1		
25MHz	Closed		
33MHz	Open		
50iMHz	Closed		
66iMHz	Open		
100iMHz	Open		

VIDEO MEMORY CONFIGURATION				
Size	Bank 0	Bank 1		
1MB	(2) 411000	NONE		
2MB	(2) 411000	(2) 411000		
Note: Bank 0 is factory installed and the location is unidentfied.				