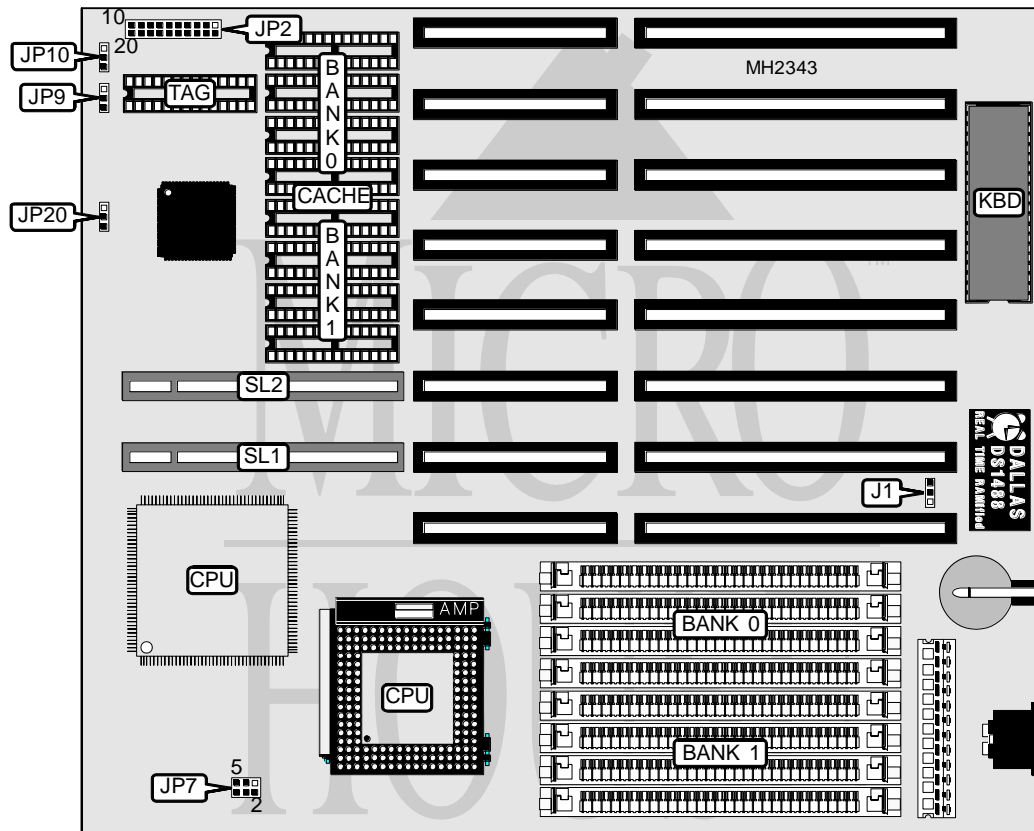


ACER, INC. V 4 3 3 S

Processor	CX486S/80486SX/80486DX/80486DX2
Processor Speed	25/33/40/50(internal)/50/66(internal)MHz
Chip Set	SIS
Max. Onboard DRAM	32MB
Cache	128/256KB
BIOS	AMI
Dimensions	254mm x 220mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	JP2 (pins 1 - 5)	Turbo switch	JP2 (pins 15 - 17)
Speaker	JP2 (pins 6 - 10)	Reset switch	JP2 (pins 19 - 20)
Turbo LED	JP2 (pins 12 - 13)	32-bit VESA local bus slots	SL1 & SL2

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	J1	Open
CMOS memory clear	J1	pins 2 & 3 closed
í Fast A20 select CMOS configuration	JP20	pins 2 & 3 closed
Fast A20 select keyboard	JP20	pins 1 & 2 closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9
8MB	(4) 2M x 9	NONE
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION		
Size	JP9	JP10
128KB	pins 1 & 2 closed	pins 1 & 2 closed
256KB	pins 2 & 3 closed	pins 2 & 3 closed

CPU SPEED CONFIGURATION	
Speed	JP7
25MHz	pins 5 & 6 closed
33MHz	pins 1 & 2 closed
40MHz	pins 1 & 2, 3 & 4 closed
50iMHz	pins 5 & 6 closed
50MHz	pins 3 & 4 closed
66iMHz	pins 1 & 2 closed