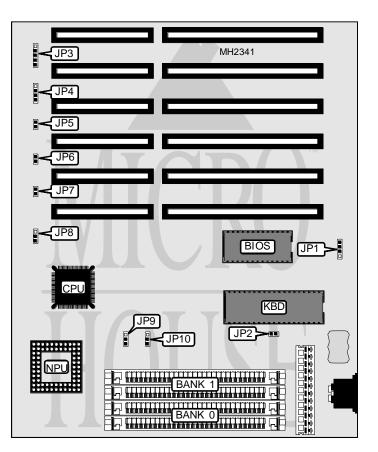
## BIOSTAR MICROTECH INTERNATIONAL CORPORATION MB-1333/40RCS-A

Processor
Processor Speed
Chip Set
Max. Onboard DRAM
Cache
BIOS
Dimensions
I/O Options
NPU Options

80386SX/CX486SLC 33/40MHz Unidentified 16MB None AMI 220mm x 220mm None 80387SX



CONNECTIONS				
Purpose	Location	Purpose	Location	
External battery	JP1	Turbo LED	JP5	
Power LED & keylock	JP3	Turbo switch	JP6	
Speaker	JP4	Reset switch	JP7	

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## BIOSTAR MICROTECH INTERNATIONAL CORPORATION MB-1333/40RCS-A

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USER CONFIGURABLE SETTINGS					
Function	Jumper	Position			
í CMOS memory normal operation	JP1	pins 2 & 3 closed			
CMOS memory clear	JP1	pins 3 & 4 closed			
External battery installed	JP1	pins 1, 2, 3 & 4 closed			
í Monitor type select monochrome	JP2	Open			
Monitor type select color	JP2	Closed			

	DRAM CONFIGURATION	
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9

CPU SPEED CONFIGURATION				
Speed	JP8	JP9	JP10	
33MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed	
40MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed	