CHAINTECH COMPUTER COMPANY, LTD.
3 2 5 S / 3 3 3 S / 3 3 3 S C / 3 4 0 S C

Processor	AM386DX/80386DX
Processor Speed	25/33/40MHz
Chip Set	AMD
Max. Onboard DRAM	8MB (325S/333S) / 16MB (333SC/340SC)
Cache	64KB (333SC/340SC)
BIOS	AMI
Dimensions	330mm x 217mm
I/O Options	None
NPU Options	80387DX/3167
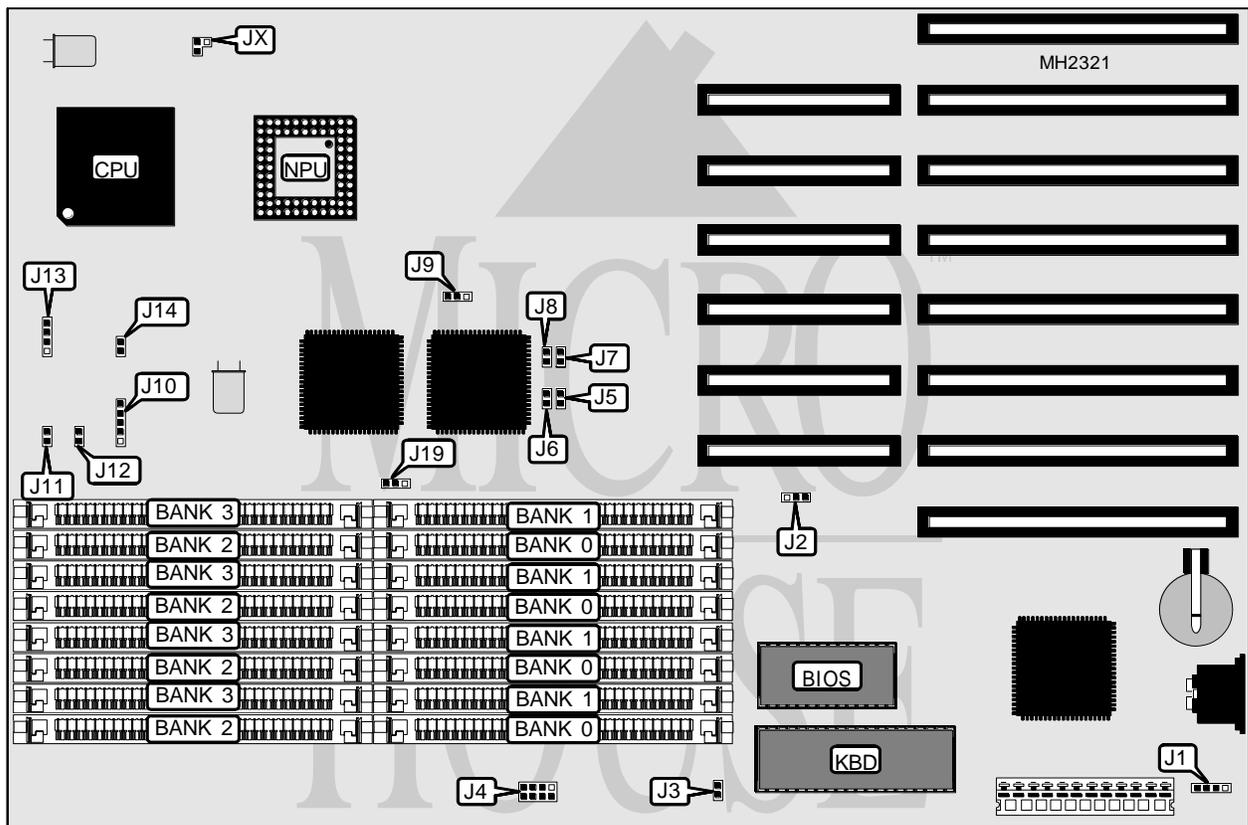
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo switch	J12
Power LED & keylock	J10	Speaker	J13
Turbo LED	J11	Reset switch	J14

CHAINTECH COMPUTER CO. LTD.
325S/333S/333SC/340SC

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	J2	pins 1 & 2 closed
í Monitor type select color	J3	Closed
Monitor type select monochrome	J3	Open
í Cache size select 32KB	J9	pins 1 & 2 closed
Cache size select 64KB	J9	pins 2 & 3 closed
í Factory configured - do not alter	J19	pins 2 & 3 closed
í NPU synchronous with CPU	JX	pins 1 & 2 closed
NPU asynchronous with CPU	JX	pins 2 & 3 closed
Note: Cache configuration is for 333SC/340SC only.		

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 256K x 9	(4) 1M x 9	NONE	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE
12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9

8042 SPEED SELECT	
8042 pin number	J4
í 23	pins 5 & 6 closed
24	pins 7 & 8 closed
29	pins 1 & 2 closed
30	pins 3 & 4 closed

MODEL TYPE SELECT				
Type	J5	J6	J7	J8
325S/333S	Closed	Closed	Closed	Closed
333SC/340SC	Open	Open	Open	Open