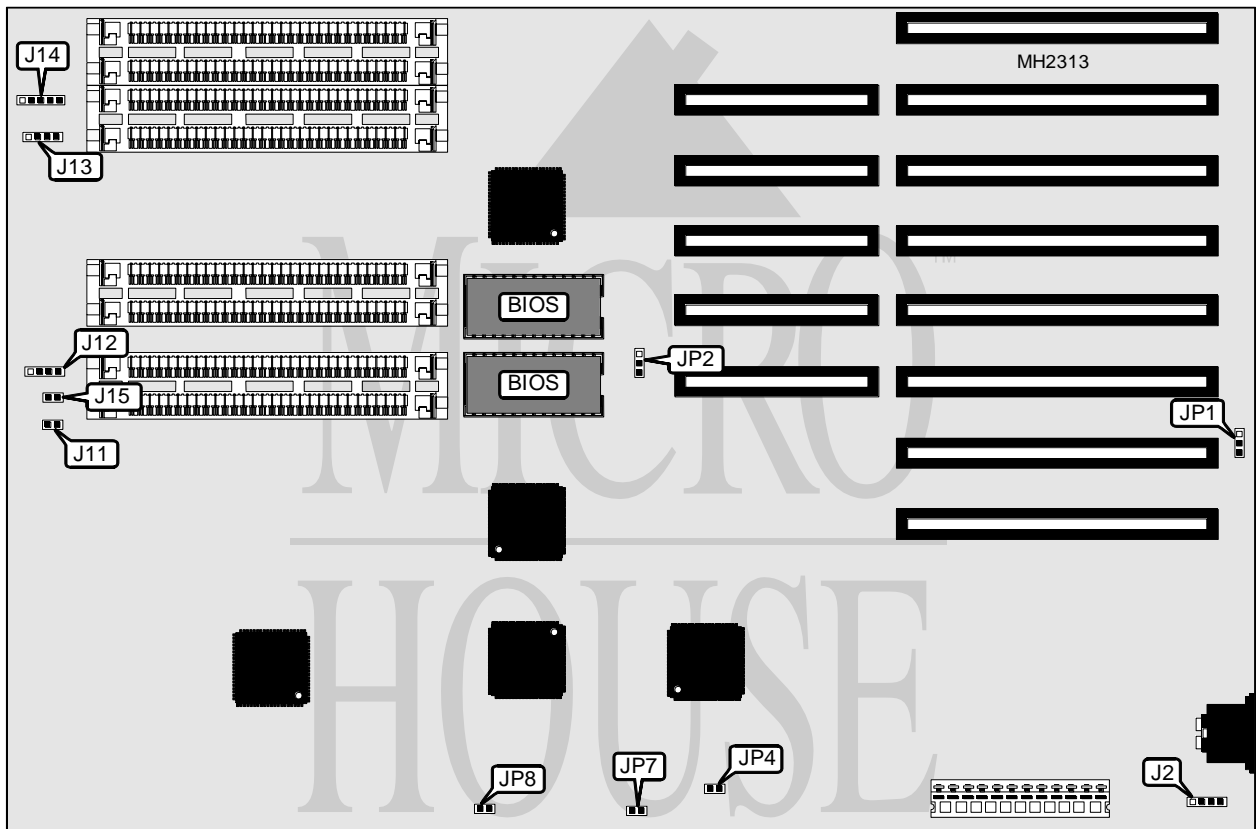


OGIVAR INC. INTERSYSTEM 386SX

Processor	80386SX (exact location unidentified)
Processor Speed	16/20MHz
Chip Set	Unidentified
Max. Onboard DRAM	8MB
Cache	None
BIOS	Unidentified
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	80387SX (exact location unidentified)



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Speaker	J13
Reset switch	J11	Power LED & keylock	J14

Continued on next page. . .

OGIVAR INC.
INTERSYSTEM 386SX

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	J12	N/A
í Factory configured - do not alter	J15	N/A
í Battery type select external	JP1	Open
Battery type select internal	JP1	pins 1 & 2 closed
í BIOS type select 27256	JP2	pins 2 & 3 closed
BIOS type select 27128	JP2	pins 1 & 2 closed
í Monitor type select color	JP4	Closed
Monitor type select monochrome	JP4	Open
í NPU disabled	JP7	Closed
NPU enabled	JP7	Open
í Pipeline mode enabled	JP8	Closed
Pipeline mode disabled	JP8	Open

Note: NPU must be synchronous with CPU.

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(2) 256K x 9	NONE	NONE	NONE
1MB	(2) 256K x 9	(2) 256K x 9	NONE	NONE
2MB	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9	(2) 256K x 9
4MB	(2) 1M x 9	(2) 1M x 9	NONE	NONE
5MB	(2) 256K x 9	(2) 256K x 9	(2) 1M x 9	(2) 1M x 9
8MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9

Note: Banks locations are unidentified and are labeled on the board in the following manner:

- Bank 0: SIMM 4 & 8
- Bank 1: SIMM 3 & 7
- Bank 2: SIMM 2 & 6
- Bank 3: SIMM 1 & 5