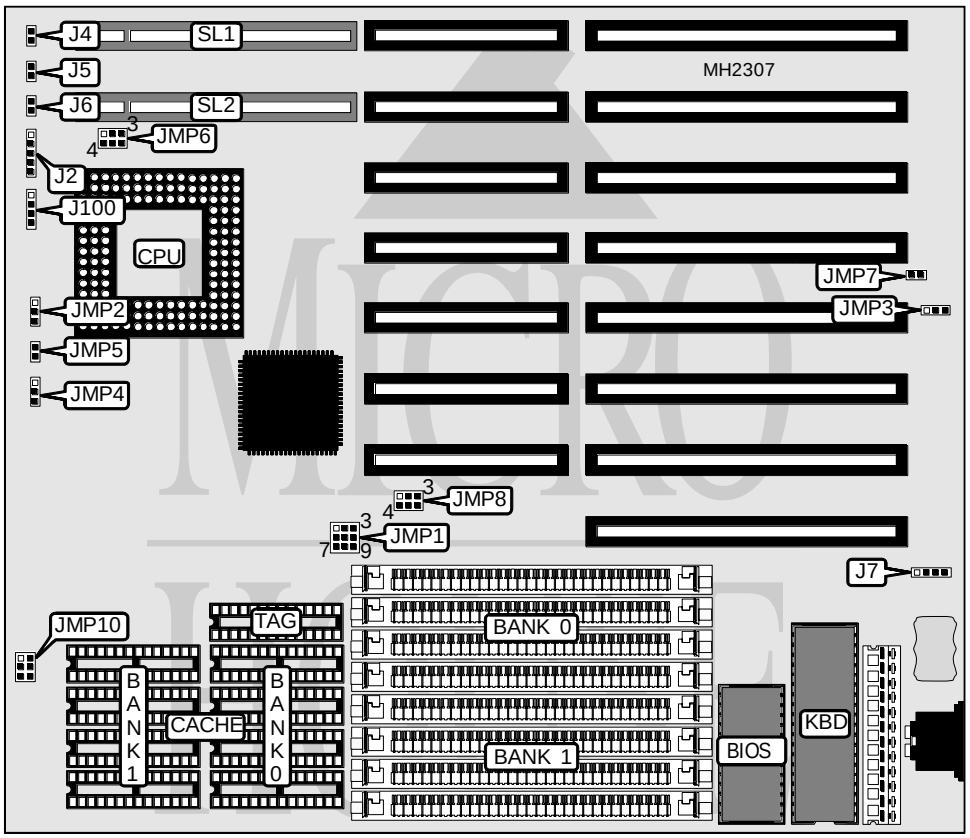
CHAINTECH COMPUTER COMPANY, LTD.
4 2 5 U X L / 4 3 3 U X L / 4 2 5 U C L / 4 3 3 U C L

Processor	80486SX/80487SX/80486DX/ODP486SX/80486DX2
Processor Speed	25/33/40/50(internal)/66(internal)MHz
Chip Set	USAI
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	254mm x 220mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	J2	External battery	J7
Turbo switch	J4	Speaker	J100
Turbo LED	J5	32-bit VESA local bus slots	SL1 & SL2
Reset switch	J6		

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Turbo enabled	J4	Closed
Turbo disabled	J4	Open
í Factory configured - do not alter	JMP3	Open
í Factory configured - do not alter	JMP4	Open
í Factory configured - do not alter	JMP5	Open
í Factory configured - do not alter	JMP7	Open
í Factory configured - do not alter	JMP10	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
1MB	NONE	(4) 256K x 9
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
4MB	NONE	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
16MB	NONE	(4) 4M x 9
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION		
Size	JMP1	JMP8
64KB	pins 1 & 2, 4 & 5, 7 & 8 closed	pins 1 & 2, 4 & 5 closed
128KB	pins 2 & 3, 4 & 5, 8 & 9 closed	pins 2 & 3, 4 & 5 closed
256KB	pins 2 & 3, 5 & 6, 8 & 9 closed	pins 2 & 3, 5 & 6 closed

CPU TYPE CONFIGURATION	
Type	JMP2
80486SX	pins 1 & 2 closed
ODP486SX	pins 2 & 3 closed
80487SX	pins 2 & 3 closed
80486DX	pins 2 & 3 closed
80486DX2	pins 2 & 3 closed

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VESA SPEED CONFIGURATION	
Speed	JMP6
33MHz	pins 1 & 4, 3 & 6 closed
40MHz	pins 1 & 4, 2 & 5 closed
50MHz	pins 1 & 4 closed