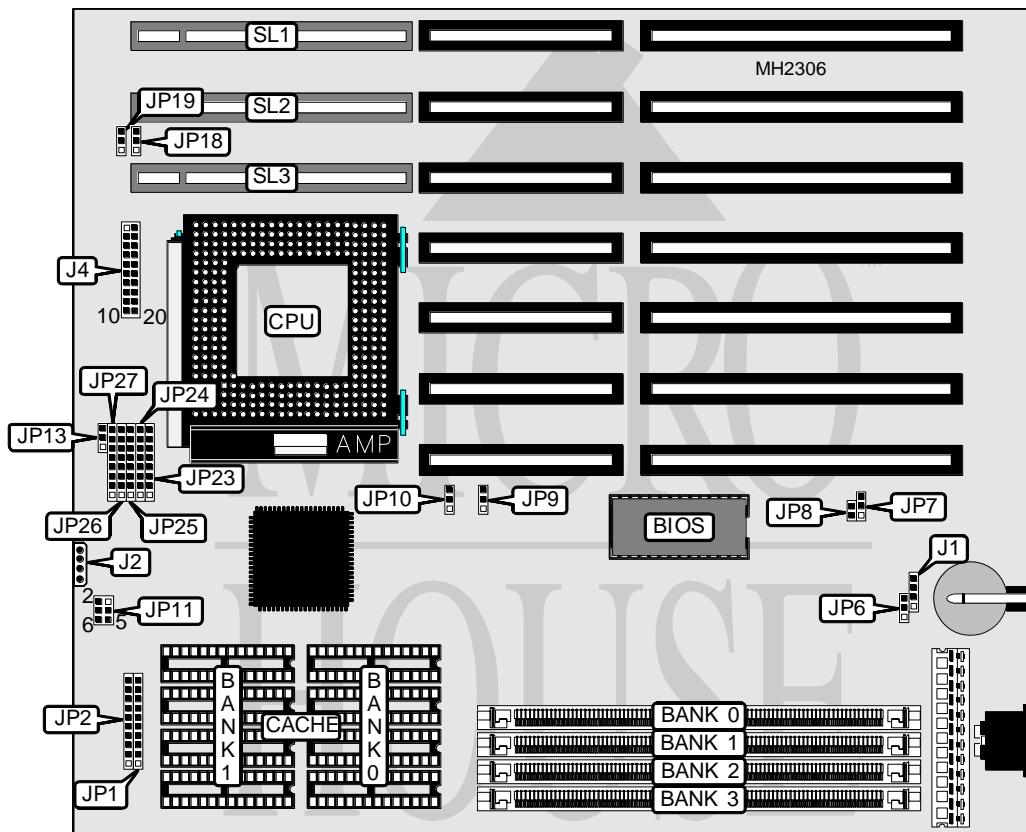


ACER, INC. VI15G

Processor	80486SX/CX486DX/AMD486DX/80486DX/ODP486SX/CX486DX2/AMD486DXL/AMD486DX2/80486DX2
Processor Speed	25/33/40/50(internal)/50/66(internal)MHz
Chip Set	SIS
Max. Onboard DRAM	128MB
Cache	128/256/512KB
BIOS	AMI
Dimensions	254mm x 220mm
I/O Options	32-bit VESA local bus slots (3), green PC connector
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J1	Turbo LED (green PC LED)	J4 (pins 12 - 13)
Chassis fan power	J2	Turbo switch (green PC break)	J4 (pins 15 - 16)
Chassis fan power	J3	Reset switch	J4 (pins 19 - 20)
Power LED & keylock	J4 (pins 1 - 5)	32-bit VESA local bus slots	SL1 - SL3
Speaker	J4 (pins 6 - 10)		
Note: The location of J3 is unidentified.			

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP6	pins 1 & 2 closed
CMOS memory clear	JP6	pins 2 & 3 closed
í Factory configured - do not alter	JP7	Open
í Factory configured - do not alter	JP8	Open

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(1) 256K x 36	NONE	NONE	NONE
2MB	(1) 256K x 36	(1) 256K x 36	NONE	NONE
2MB	(1) 512K x 36	NONE	NONE	NONE
4MB	(1) 256K x 36	(1) 256K x 36	(1) 512K x 36	NONE
4MB	(1) 512K x 36	(1) 512K x 36	NONE	NONE
4MB	(1) 1M x 36	NONE	NONE	NONE
5MB	(1) 256K x 36	(1) 1M x 36	NONE	NONE
6MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	NONE
6MB	(1) 512K x 36	(1) 1M x 36	NONE	NONE
8MB	(1) 256K x 36	(1) 256K x 36	(1) 512K x 36	(1) 1M x 36
8MB	(1) 512K x 36	(1) 512K x 36	(1) 1M x 36	NONE
8MB	(1) 1M x 36	(1) 1M x 36	NONE	NONE
8MB	(1) 2M x 36	NONE	NONE	NONE
10MB	(1) 256K x 36	(1) 256K x 36	(1) 1M x 36	(1) 1M x 36
12MB	(1) 512K x 36	(1) 512K x 36	(1) 1M x 36	(1) 1M x 36
12MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	NONE
16MB	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36
16MB	(1) 2M x 36	(1) 2M x 36	NONE	NONE
16MB	(1) 4M x 36	NONE	NONE	NONE
17MB	(1) 256K x 36	(1) 4M x 36	NONE	NONE
18MB	(1) 256K x 36	(1) 256K x 36	(1) 4M x 36	NONE
18MB	(1) 512K x 36	(1) 4M x 36	NONE	NONE
20MB	(1) 512K x 36	(1) 512K x 36	(1) 4M x 36	NONE
20MB	(1) 1M x 36	(1) 4M x 36	NONE	NONE
24MB	(1) 512K x 36	(1) 512K x 36	(1) 1M x 36	(1) 4M x 36
24MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	NONE
24MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	NONE
32MB	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36	(1) 2M x 36
32MB	(1) 4M x 36	(1) 4M x 36	NONE	NONE
32MB	(1) 8M x 36	NONE	NONE	NONE
36MB	(1) 512K x 36	(1) 512K x 36	(1) 4M x 36	(1) 4M x 36
36MB	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36	NONE
36MB	(1) 1M x 36	(1) 8M x 36	NONE	NONE
40MB	(1) 1M x 36	(1) 1M x 36	(1) 4M x 36	(1) 4M x 36
40MB	(1) 1M x 36	(1) 1M x 36	(1) 8M x 36	NONE
48MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	NONE
48MB	(1) 4M x 36	(1) 8M x 36	NONE	NONE

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DRAM CONFIGURATION (CON'T)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
64MB	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36
64MB	(1) 16M x 36	NONE	NONE	NONE
64MB	(1) 4M x 36	(1) 4M x 36	(1) 8M x 36	NONE
64MB	(1) 8M x 36	(1) 8M x 36	NONE	NONE
65MB	(1) 256K x 36	(1) 16M x 36	NONE	NONE
68MB	(1) 1M x 36	(1) 16M x 36	NONE	NONE
68MB	(1) 1M x 36	(1) 8M x 36	(1) 8M x 36	NONE
72MB	(1) 1M x 36	(1) 1M x 36	(1) 16M x 36	NONE
72MB	(1) 1M x 36	(1) 1M x 36	(1) 8M x 36	(1) 8M x 36
80MB	(1) 4M x 36	(1) 16M x 36	NONE	NONE
80MB	(1) 4M x 36	(1) 8M x 36	(1) 8M x 36	NONE
96MB	(1) 4M x 36	(1) 4M x 36	(1) 16M x 36	NONE
96MB	(1) 4M x 36	(1) 4M x 36	(1) 8M x 36	(1) 8M x 36
96MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	NONE
128MB	(1) 16M x 36	(1) 16M x 36	NONE	NONE
128MB	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36	(1) 8M x 36

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
128KB	(4) 32K x 8	NONE
256KB	(4) 32K x 8	(4) 32K x 8
256KB	(4) 64K x 8	NONE
512KB	(4) 128K x 8	NONE

CACHE JUMPER CONFIGURATION		
Size	JP1	JP2
128KB	pins 1 & 2, 4 & 5, 8 & 9 closed	pins 1 & 2, 4 & 5, 8 & 9 closed
256KB	pins 2 & 3, 5 & 6, 8 & 9 closed	pins 2 & 3, 5 & 6, 8 & 9 closed
256KB	pins 1 & 2, 5 & 6, 8 & 9 closed	pins 1 & 2, 4 & 5, 8 & 9 closed
512KB	pins 1 & 2, 5 & 6, 7 & 8 closed	pins 1 & 2, 4 & 5, 7 & 8 closed

CPU TYPE CONFIGURATION				
Type	JP9	JP10	JP13	JP23
CX486S	2 & 3	2 & 3	1 & 2	2 & 3, 5 & 6, 7 & 8
SL80486SX	1 & 2	1 & 2	1 & 2	3 & 4, 5 & 6, 7 & 8
80486SX	1 & 2	1 & 2	Open	6 & 7
AMD486DXL	2 & 3	2 & 3	2 & 3	5 & 6, 7 & 8
80486DX/DX2	1 & 2	1 & 2	Open	5 & 6, 7 & 8
P24D	1 & 2	2 & 3	1 & 2	1 & 2, 3 & 4, 5 & 6, 7 & 8

Note: Pins designated should be in the closed position.

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CPU TYPE CONFIGURATION (CON'T)				
Type	JP24	JP25	JP26	JP27
CX486S	2 & 3, 7 & 8	2 & 3	4 & 5	2 & 3, 7 & 8
SL80486SX	7 & 8	Open	1 & 2, 5 & 6	4 & 5, 7 & 8
80486SX	Open	Open	Open	7 & 8
AMD486DXL	3 & 4, 5 & 6	Open	Open	7 & 8
80486DX/DX2	7 & 8	Open	Open	7 & 8
P24D	7 & 8	1 & 2, 5 & 6	2 & 3, 5 & 6, 7 & 8	4 & 5, 6 & 7

Note: Pins designated should be in the closed position.

CPU SPEED CONFIGURATION	
Speed	JP11
25MHz	pins 1 & 2, 5 & 6 closed
33MHz	pins 1 & 2, 3 & 4 closed
40MHz	pins 5 & 6 closed
50iMHz	pins 1 & 2, 5 & 6 closed
50MHz	pins 3 & 4 closed
66iMHz	pins 1 & 2, 3 & 4 closed

VESA WAIT STATE CONFIGURATION	
Wait states	JP18
0 wait states	pins 1 & 2 closed
1 wait state	pins 2 & 3 closed

BUS SPEED CONFIGURATION	
CPU speed	JP19
<= 33MHz	pins 1 & 2 closed
> 33MHz	pins 2 & 3 closed