AUVA COMPUTER, INC.
B A B Y   2 8 6   B A M / 1 2 - G 1

Processor	80286 (exact location unknown)
Processor Speed	12MHz
Chip Set	G-2
Max. Onboard DRAM	4MB
Cache	None
BIOS	AMI
Dimensions	220mm x 220mm
I/O Options	None
NPU Options	80287
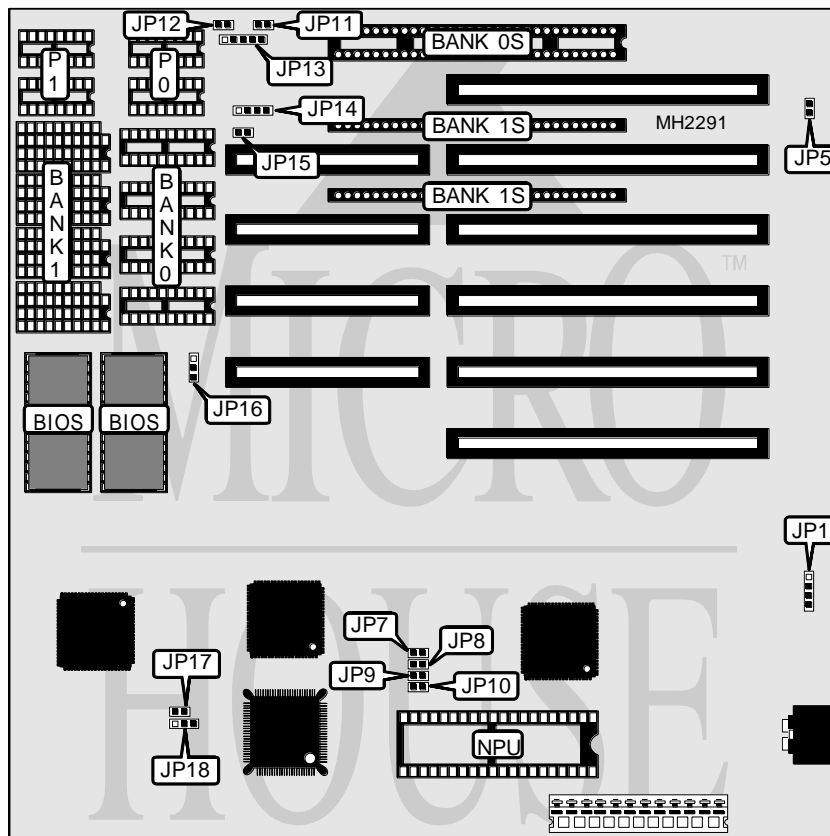
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP1	Power LED & keylock	JP13
Reset switch	JP11	Speaker	JP14
Turbo switch	JP12	Turbo LED	JP15

Continued on next page. . .

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BABY 286 BAM/12-G1

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Battery type select internal	JP5	Open
Battery type select external	JP5	Closed
í Monitor type select monochrome	JP10	Open
Monitor type select color	JP10	Closed
í BIOS type select 27128	JP16	pins 1 & 2 closed
BIOS type select 27256	JP16	pins 2 & 3 closed
í Wait state select 0 wait state	JP17	Closed
Wait state select 1 wait state	JP17	Open
í Parity check disabled	JP18	pins 2 & 3 closed
Parity check enabled	JP18	pins 1 & 2 closed

DRAM CONFIGURATION (DIP)		
Size	Bank 0	Bank 1
512KB	(4) 44256	NONE
640KB	(4) 44256	(4) 44256
1MB	(4) 44256	(4) 44256

PARITY CONFIGURATION (DIP)		
Size	P 0	P1
512KB	(2) 41256	NONE
640KB	(2) 41256	(2) 4164
1MB	(2) 41256	(2) 41256

DRAM SWITCH CONFIGURATION (DIP)			
Size	JP7	JP8	JP9
512KB	Open	Closed	Closed
640KB	Closed	Open	Closed
1MB	Open	Open	Closed

DRAM CONFIGURATION (SIP)		
Size	Bank 0S	Bank 1S
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9

DRAM SWITCH CONFIGURATION (SIP)			
Size	JP7	JP8	JP9
2MB	Closed	Open	Open
4MB	Open	Open	Open