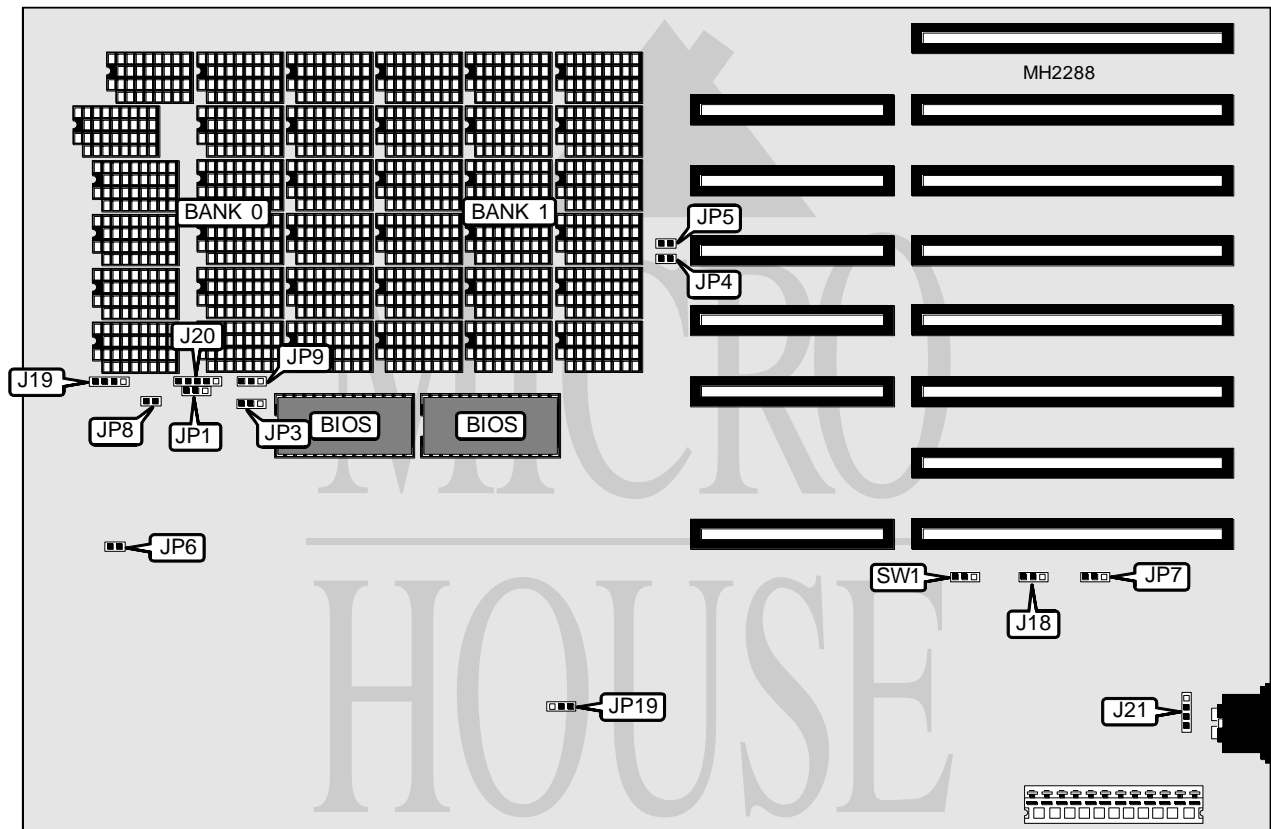


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DIGIS - 286 C

Processor	80286 (exact location unknown)
Processor Speed	12MHz
Chip Set	Suntac
Max. Onboard DRAM	4MB
Cache	None
BIOS	Award/Phoenix
Dimensions	330mm x 218mm
I/O Options	None
NPU Options	None



CONNECTIONS			
Purpose	Location	Purpose	Location
Speaker	J19	Turbo LED	JP4
Power LED & keylock	J20	Turbo switch	JP5
External battery	J21	Reset switch	JP6

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USER CONFIGURABLE SETTINGS		
Function	Jumper/Switch	Position
í CPU clock speed select high	DSP1/1	On
CPU clock speed select low	DSP1/1	Off
í Factory configured - do not alter	DSP1/2	On
í I/O wait state select 4 wait states	DSP1/3	On
I/O wait state select 6 wait states	DSP1/3	Off
í EMS port address select 0EBH	DSP1/4	On
EMS port address select 098H	DSP1/4	Off
í RAM size select 256KB	J18	pins 1 & 2 closed
RAM size select 512KB	J18	pins 2 & 3 closed
í CPU clock speed select high	JP1	Open
CPU clock speed select low	JP1	pins 2 & 3 closed
í I/O wait control select keyboard	JP3	pins 1 & 2 closed
I/O wait control select DSP1/3	JP3	pins 2 & 3 closed
í Turbo enabled	JP4	Closed
Turbo disabled	JP4	Open
í BIOS type select Phoenix	JP7	pins 1 & 2 closed
BIOS type select Award	JP7	pins 2 & 3 closed
í DRAM mode select expanded	JP8	Open
DRAM mode select extended	JP8	Closed
í DRAM wait state select 1 wait state	JP19	Open
DRAM wait state select 0 wait states	JP19	Closed
Note: The location of DSP1 is unknown.		

DRAM CONFIGURATION						
Mode	Size	Base	Extended	Expanded	Bank 0	Bank 1
0	512KB	512KB	0KB	0KB	(18) 41256	NONE
1	640KB	640KB	0KB	0KB	(18) 41256	(18) 4164
2	1MB	640KB	384KB	0KB	(18) 41256	(18) 41256
3	1MB	640KB	0KB	384KB	(18) 41256	(18) 41256
4	2MB	640KB	1408KB	0KB	(18) 411000	NONE
5	2MB	640KB	0KB	1408KB	(18) 411000	NONE
6	4MB	640KB	3456KB	0KB	(18) 411000	(18) 411000
7	4MB	640KB	0KB	3456KB	(18) 411000	(18) 411000

DRAM SWITCH CONFIGURATION			
Mode	DSP1/6	DSP1/7	DSP1/8
0	On	On	On
1	On	On	Off
2	On	Off	On
3	On	Off	Off
4	Off	On	On
5	Off	On	Off
6	Off	Off	On
7	Off	Off	Off
Note: The location of DSP1 is unknown.			

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BIOS CONFIGURATION		
Size	DSP1/5	JP9
27128	On	pins 1 & 2 closed
27256	Off	pins 2 & 3 closed