AUVA COMPUTER, INC. BABY 286 BAM/12S

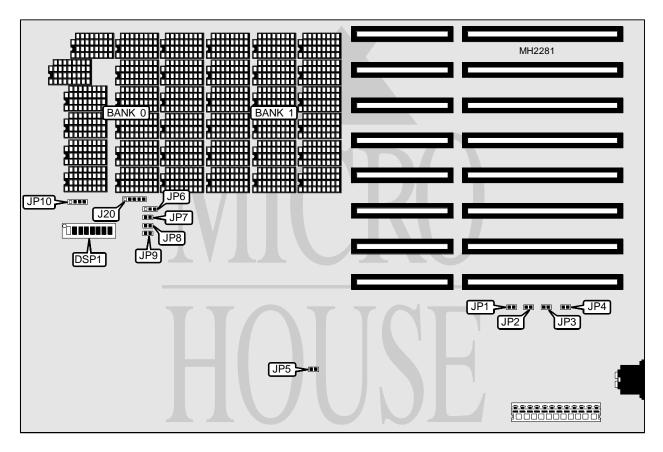
80286 (exact location unknown) **Processor**

Processor Speed 12MHz **Chip Set** Suntac Max. Onboard DRAM 4MB Cache None

BIOS AMI/Award/Phoenix **Dimensions** 330mm x 218mm

I/O Options None

NPU Options 80287 (exact location unknown)



CONNECTIONS				
Purpose	Location	Purpose	Location	
Power LED & keylock	J20	Turbo LED	JP8	
Reset switch	JP6	Speaker	JP10	
Turbo switch	JP7			

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USER CONFIGURABLE SETTINGS			
Function	Jumper/Switch	Position	
í CPU speed select low	DSP1/1	On	
CPU speed select high	DSP1/1	Off	
í Monitor type select color	DSP1/2	On	
Monitor type select monochrome	DSP1/2	Off	
í Factory configured - do not alter	DSP1/3	Off	
í Port address select 0E8H	DSP1/4	On	
Port address select 098H	DSP1/4	Off	
í BIOS type select 27128	DSP1/5	On	
BIOS type select 27256	DSP1/5	Off	
í Wait select 0 wait states	JP5	Closed	
Wait select 1 wait state	JP5	Open	
í Turbo enabled	JP7	Closed	
Turbo disabled	JP7	Open	
í Clock speed control DSP1/1	JP9	pins 2 & 3 closed	
Clock speed control keyboard	JP9	pins 1 & 2 closed	

DRAM CONFIGURATION				
Size	EMS	Bank 0	Bank 1	
512KB	N/A	(18) 41256	NONE	
640KB	N/A	(18) 41256	(18) 4164	
1MB	OKB	(18) 41256	(18) 41256	
1MB	348KB	(18) 41256	(18) 41256	
2MB	ОКВ	(18) 411000	NONE	
2MB	1408KB	(18) 411000	NONE	
4MB	ОКВ	(18) 411000	(18) 411000	
4MB	3456KB	(18) 411000	(18) 411000	

DRAM CONFIGURATION				
Size	DSP1/6	DSP1/7	DSP1/8	
512KB	On	On	On	
640KB	On	On	Off	
1MB	On	Off	On	
1MB	On	Off	Off	
2MB	Off	On	On	
2MB	Off	On	Off	
4MB	Off	Off	On	
4MB	Off	Off	Off	

KEYBOARD BIOS CONFIGURATION				
Туре	JP1	JP2	JP3	JP4
Award/Phoenix	Open	Open	Open	Closed
Other	N/A	N/A	N/A	Open
Note: If other BIOS is installed, see table below for JP1 - JP3 settings.				

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KEYBOARD BIOS CONFIGURATION				
Output pins JP1 JP2 JP3				
23	Open	Open	Closed	
27	Open	Closed	Open	
32	Closed	Open	Open	