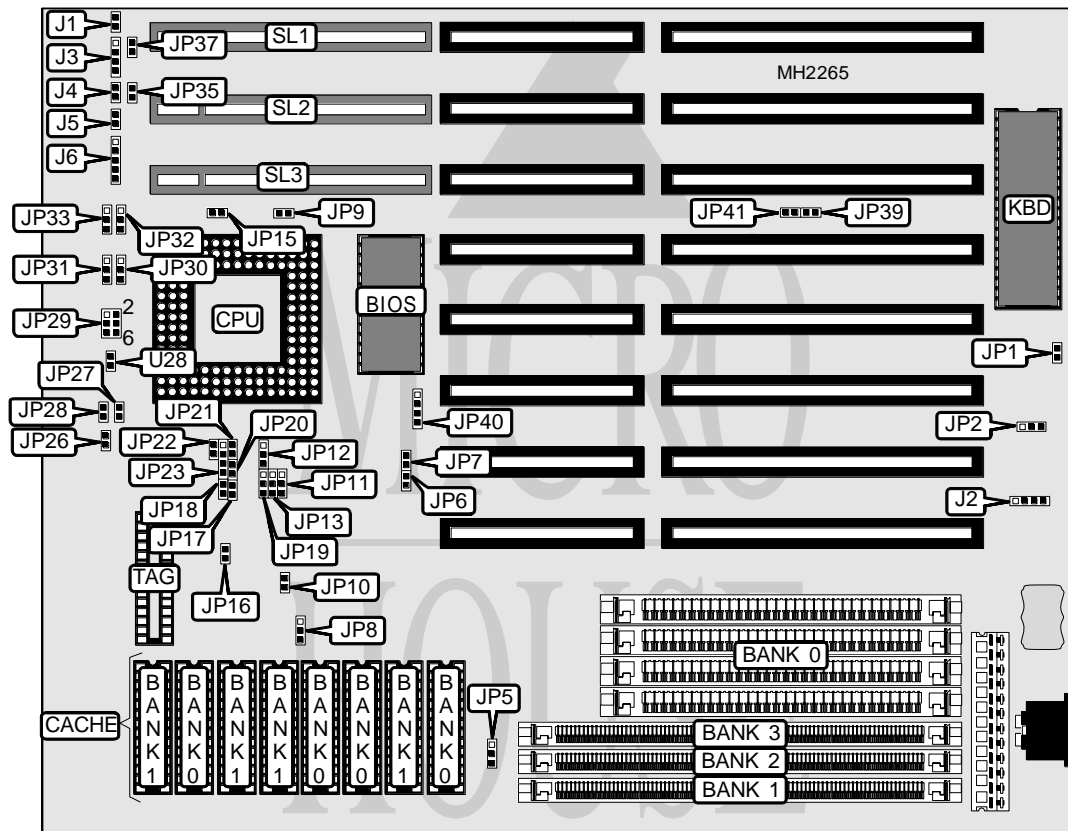


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J-403TG 486 GREEN VLB

Processor	80486SX/CX486DX/80486DX/80486DX2/80486DX4
Processor Speed	25/33/40/50(internal)/50/66(internal)MHz
Chip Set	OPTI
Max. Onboard DRAM	128MB
Cache	64/128/256KB
BIOS	AMI/Award
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (3), green PC connector
NPU Options	None



CONNECTIONS

Purpose	Location	Purpose	Location
Reset switch	J1	Power LED & keylock	J6
External battery	J2	Green PC connector	JP6
Speaker	J3	Green PC connector (monitor)	JP39
Turbo switch	J4	Green PC LED	JP41
Turbo LED	J5	32-bit VESA local bus slots	SL1 - SL3

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP1	Open
í CMOS memory normal operation	JP2	pins 2 & 3 closed
CMOS memory clear	JP2	pins 1 & 2 closed
í Factory configured - do not alter	JP7	Open
í Factory configured - do not alter	JP12	Open
í Factory configured - do not alter	JP16	Open
í Factory configured - do not alter	JP19	Open
í Factory configured - do not alter	JP22	Open
í Factory configured - do not alter	JP26	Open
í Factory configured - do not alter	JP30	Open
í Factory configured - do not alter	U28	Open

DRAM CONFIGURATION					
Size	Bank 0	Bank 1	Bank 2	Bank 3	JP5
2MB	(4) 256K x 9	NONE	NONE	(1) 256K x 36	2 & 3
2MB	NONE	NONE	NONE	(1) 512K x 36	1 & 2
4MB	(4) 1M x 9	NONE	NONE	NONE	2 & 3
4MB	NONE	NONE	NONE	(1) 1M x 36	1 & 2
4MB	(4) 256K x 9	(1) 256K x 36	(1) 256K x 36	(1) 256K x 36	2 & 3
4MB	NONE	NONE	(1) 512K x 36	(1) 512K x 36	1 & 2
5MB	(4) 256K x 9	NONE	NONE	(1) 1M x 36	2 & 3
6MB	(4) 256K x 9	NONE	(1) 1M x 36	(1) 256K x 36	2 & 3
6MB	NONE	NONE	(1) 1M x 36	(1) 512K x 36	1 & 2
8MB	(4) 1M x 9	NONE	NONE	(1) 1M x 36	2 & 3
8MB	NONE	NONE	NONE	(1) 2M x 36	1 & 2
8MB	NONE	NONE	(1) 1M x 36	(1) 1M x 36	1 & 2
10MB	(4) 256K x 9	NONE	(1) 2M x 36	(1) 256K x 36	2 & 3
10MB	(4) 256K x 9	(1) 1M x 36	(1) 1M x 36	(1) 256K x 36	2 & 3
10MB	NONE	NONE	(1) 2M x 36	(1) 512K x 36	1 & 2
10MB	NONE	(1) 1M x 36	(1) 1M x 36	(1) 512K x 36	1 & 2
12MB	(4) 1M x 9	NONE	(1) 2M x 36	NONE	2 & 3
12MB	NONE	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	1 & 2
12MB	(4) 1M x 9	(1) 1M x 36	(1) 1M x 36	NONE	2 & 3
16MB	(4) 4M x 9	NONE	NONE	NONE	2 & 3
16MB	NONE	NONE	NONE	(1) 4M x 36	1 & 2
16MB	(4) 1M x 9	(1) 1M x 36	(1) 1M x 36	(1) 1M x 36	2 & 3
16MB	NONE	NONE	(1) 2M x 36	(1) 2M x 36	1 & 2
17MB	(4) 256K x 9	NONE	NONE	(1) 4M x 36	2 & 3
20MB	(4) 1M x 9	NONE	NONE	(1) 4M x 36	2 & 3
20MB	(4) 1M x 9	NONE	(1) 4M x 36	NONE	2 & 3
20MB	NONE	NONE	(1) 4M x 36	(1) 1M x 36	1 & 2
32MB	(4) 4M x 9	NONE	NONE	(1) 4M x 36	2 & 3
32MB	NONE	NONE	NONE	(1) 8M x 36	1 & 2
32MB	(4) 4M x 9	NONE	(1) 4M x 36	NONE	2 & 3

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DRAM CONFIGURATION (CON'T)					
Size	Bank 0	Bank 1	Bank 2	Bank 3	JP5
32MB	NONE	NONE	(1) 4M x 36	(1) 4M x 36	1 & 2
64MB	(4) 16M x 9	NONE	NONE	NONE	2 & 3
64MB	NONE	NONE	NONE	(1) 16M x 36	1 & 2
64MB	(4) 4M x 9	(1) 4M x 36	(1) 4M x 36	(1) 4M x 36	2 & 3
64MB	NONE	NONE	(1) 8M x 36	(1) 8M x 36	1 & 2
128MB	(4) 16M x 9	NONE	NONE	(1) 16M x 36	2 & 3
128MB	NONE	NONE	NONE	(1) 32M x 36	1 & 2
128MB	(4) 16M x 9	NONE	(1) 16M x 36	NONE	2 & 3
128MB	NONE	NONE	(1) 16M x 36	(1) 16M x 36	1 & 2

Note: Pins designated should be in the closed position.

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION			
Size	JP8	JP27	JP28
64KB	pins 2 & 3 closed	Open	Open
128KB	pins 1 & 2 closed	Closed	Open
256KB	pins 2 & 3 closed	Closed	Closed

CPU TYPE CONFIGURATION					
Type	JP9	JP10	JP11	JP13	JP15
80486SX	Open	Open	pins 1 & 2 closed	Open	Open
SL80486SX	Open	Closed	pins 1 & 2 closed	Open	Open
CX486DX	Closed	Open	pins 2 & 3 closed	pins 1 & 2 closed	Closed
80486DX	Open	Open	pins 1 & 2 closed	pins 1 & 2 closed	Open
SL80486DX	Open	Closed	pins 1 & 2 closed	pins 1 & 2 closed	Open
80486DX2	Open	Open	pins 1 & 2 closed	pins 1 & 2 closed	Open
SL80486DX2	Open	Closed	pins 1 & 2 closed	pins 1 & 2 closed	Open
80486DX4	Open	Closed	pins 1 & 2 closed	pins 1 & 2 closed	Open

CPU TYPE CONFIGURATION					
Type	JP20	JP21	JP23	JP32	JP40
80486SX	Open	Open	2 & 3	Open	1 & 2, 3 & 4
SL80486SX	Open	Open	2 & 3	Open	1 & 2, 3 & 4
CX486DX	Open	Open	1 & 2, 3 & 4	1 & 2	1 & 2, 3 & 4
80486DX	Open	Open	1 & 2, 3 & 4	Open	1 & 2, 3 & 4
SL80486DX	Closed	Closed	1 & 2, 3 & 4	Open	1 & 2, 3 & 4
80486DX2	Open	Open	1 & 2, 3 & 4	Open	1 & 2, 3 & 4
SL80486DX2	Closed	Closed	1 & 2, 3 & 4	Open	1 & 2, 3 & 4
80486DX4	Closed	Closed	1 & 2, 3 & 4	2 & 3	1 & 2

Note: Pins designated should be in the closed position.

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CPU SPEED CONFIGURATION		
Speed	JP17	JP18
25MHz	Open	Open
33MHz	Closed	Closed
40MHz	Closed	Open
50iMHz	Open	Open
50MHz	Open	Closed
66iMHz	Closed	Closed

CPU VOLTAGE CONFIGURATION	
Voltage	JP29
3.3v	pins 3 & 5, 4 & 6 closed
5.0v	pins 1 & 3, 2 & 4 closed

VESA WAIT STATE CONFIGURATION	
Wait states	JP35
0 wait states	Open
1 wait state	Closed

BUS SPEED CONFIGURATION			
CPU speed	JP31	JP33	JP37
<= 33MHz	pins 1 & 2 closed	pins 2 & 3 closed	Open
> 33MHz	pins 2 & 3 closed	pins 1 & 2 closed	Closed