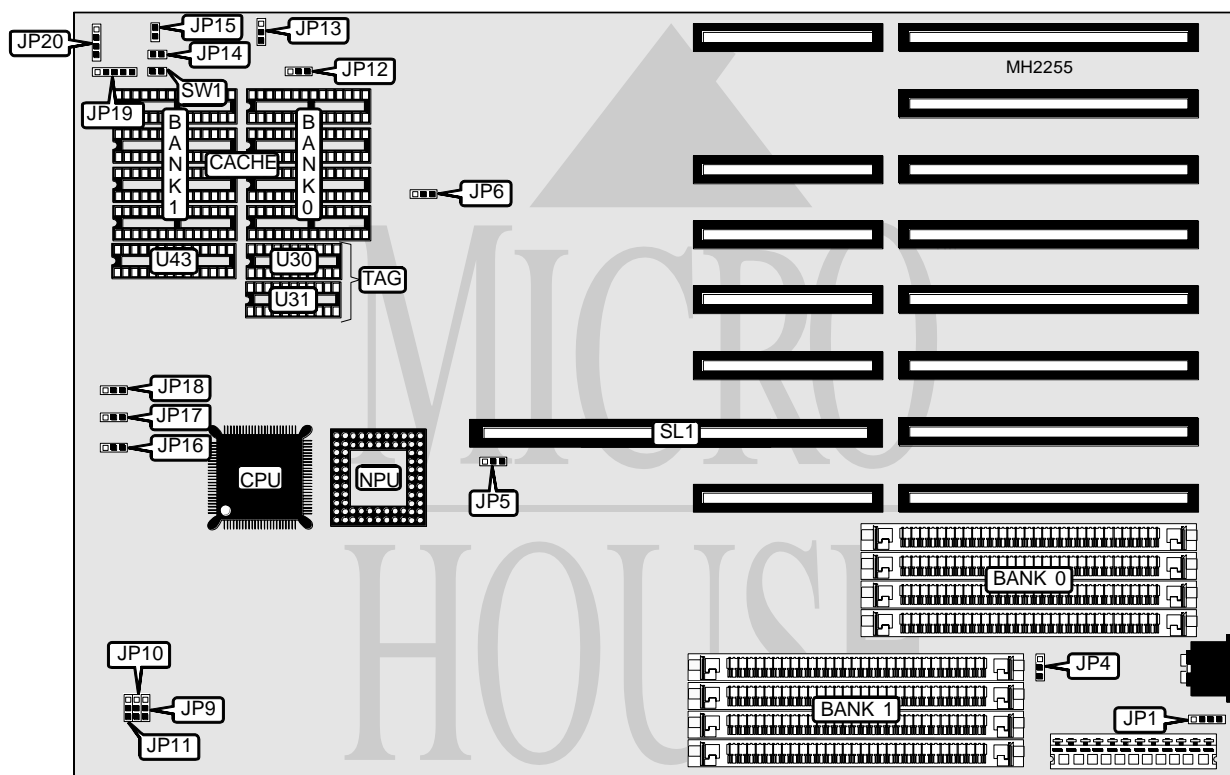


UNIDENTIFIED

386 CACHE SERIES VER. 2.0

Processor	80386DX
Processor Speed	16/20/25/33/40MHz
Chip Set	SIS
Max. Onboard DRAM	16MB (8MB on external memory card)
Cache	32/64/128KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit external memory card
NPU Options	80387DX



CONNECTIONS

Purpose	Location	Purpose	Location
External battery	JP1	Speaker	JP20
Turbo LED	JP14	32-bit external memory card	SL1
Turbo switch	JP15	Reset switch	SW1
Power LED & keylock	JP19		

Continued next page. . .

UNIDENTIFIED

386 CACHE SERIES VER. 2.0

... continued from previous page

USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	JP4	pins 1 & 2 closed
í Factory configured - do not alter	JP5	Open
í Factory configured - do not alter	JP6	pins 1 & 2 closed
Cache type select external	JP7	pins 2 & 3 closed
Cache type select internal	JP7	pins 1 & 2 closed
í Monitor type select monochrome	JP13	Open
Monitor type select CGA/EGA/VGA	JP13	pins 1 & 2 closed
í Factory configured - do not alter	JP16	pins 2 & 3 closed
í Factory configured - do not alter	JP17	pins 2 & 3 closed

Note: The location of JP7 is unknown.

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
8MB	(4) 1M x 9	(4) 1M x 9

CACHE CONFIGURATION		
Size	Bank 0	Bank 1
32KB	(4) 8K x 8	NONE
64KB	(4) 8K x 8	(4) 8K x 8
128KB	NONE	(4) 32K x 8

CACHE CONFIGURATION (TAG)			
Size	U30	U31	U43
32KB	(1) 16K x 4	(1) 16K x 4	NONE
64KB	(1) 16K x 4	(1) 16K x 4	NONE
128KB	NONE	NONE	(1) 32K x 8

CACHE JUMPER CONFIGURATION						
Size	JP8	JP9	JP10	JP11	JP12	JP18
32KB	2 & 3	2 & 3	2 & 3	2 & 3	1 & 2	Open
64KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	Open
128KB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	1 & 2
128KB (internal)	1 & 2	1 & 2	1 & 2	1 & 2	Open	Open

Note: Pins designated should be in the closed position. The location of JP8 is unknown.