INFORMTECH INTERNATIONAL, INC.

## IT386SX

| Processor | 80386 SX |
| :--- | :--- |
| Processor Speed | $8 / 16 / 20 \mathrm{MHz}$ |
| Chip Set | Intel |
| Max. Onboard DRAM | 2 MB |
| Cache | None |
| BIOS | Phoenix |
| Dimensions | $330 \mathrm{~mm} \times 218 \mathrm{~mm}$ |
| I/O Options | 32 -bit external memory card |
| NPU Options | 80387 SX |



| CONNECTIONS |  |  |  |
| :--- | :---: | :--- | :---: |
| Purpose | Location | Purpose | Location |
| Turbo LED | P2 | External battery | P8 |
| Speaker | P3 | Keyboard connector | J9 |
| Reset switch | P4 | 32-bit external memory card | SL1 |
| Power LED \& keylock | P5 |  |  |

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## USER CONFIGURABLE SETTINGS

| Function | Jumper | Position |
| :---: | :---: | :---: |
| í Keyboard controller clock speed select 6MHz | J5 | pins 1 \& 2 closed |
| Keyboard controller clock speed select 12MHz | J5 | pins 2 \& 3 closed |
| í Power good signal detect from board | J8 | pins 1 \& 2 closed |
| Power good signal detect from power supply | J8 | pins 2 \& 3 closed |
| í Monitor type select color | J19 | Closed |
| Monitor type select monochrome | J19 | Open |


| DRAM CONFIGURATION |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Size | Bank 0 | Parity 0 | Bank 1 | Parity 1 |  |
| 1 MB | (8) 44256 | (4) 41256 | NONE | NONE |  |
| 2 MB | (8) 44256 | (4) 41256 | (8) 44256 | (4) 41256 |  |


| CPU SPEED CONFIGURATION |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| CPU Speed | Clock speed | J2 | J10 | J13 | J14 |
| 8 MHz | 8 MHz | $1 \& 2$ | Open | Open | Closed |
| 16 MHz | 8 MHz | $1 \& 2$ | $1 \& 2$ | Open | Open |
| 16 MHz | 12 MHz | $2 \& 3$ | $1 \& 2$ | Open | Open |
| 20 MHz | 8 MHz | $1 \& 2$ | $2 \& 3$ | Open | Open |
| 20 MHz | 10 MHz | Open | $2 \& 3$ | Closed | Open |
| 20 MHz | 12 MHz | $2 \& 3$ | $2 \& 3$ | Open | Open |
| Note: Pins designated should be in the closed position. |  |  |  |  |  |

## BATTERY TYPE CONFIGURATION

| Type | J7 |
| :---: | :---: |
| 3.6 v (without limiting resistor) | pins $1 \& 2$ closed |
| 4.5 v | pins $1 \& 3$ closed |
| 6 v (with limiting resistor) | pins 3 \& 4 closed |
| 6 v | Open |

