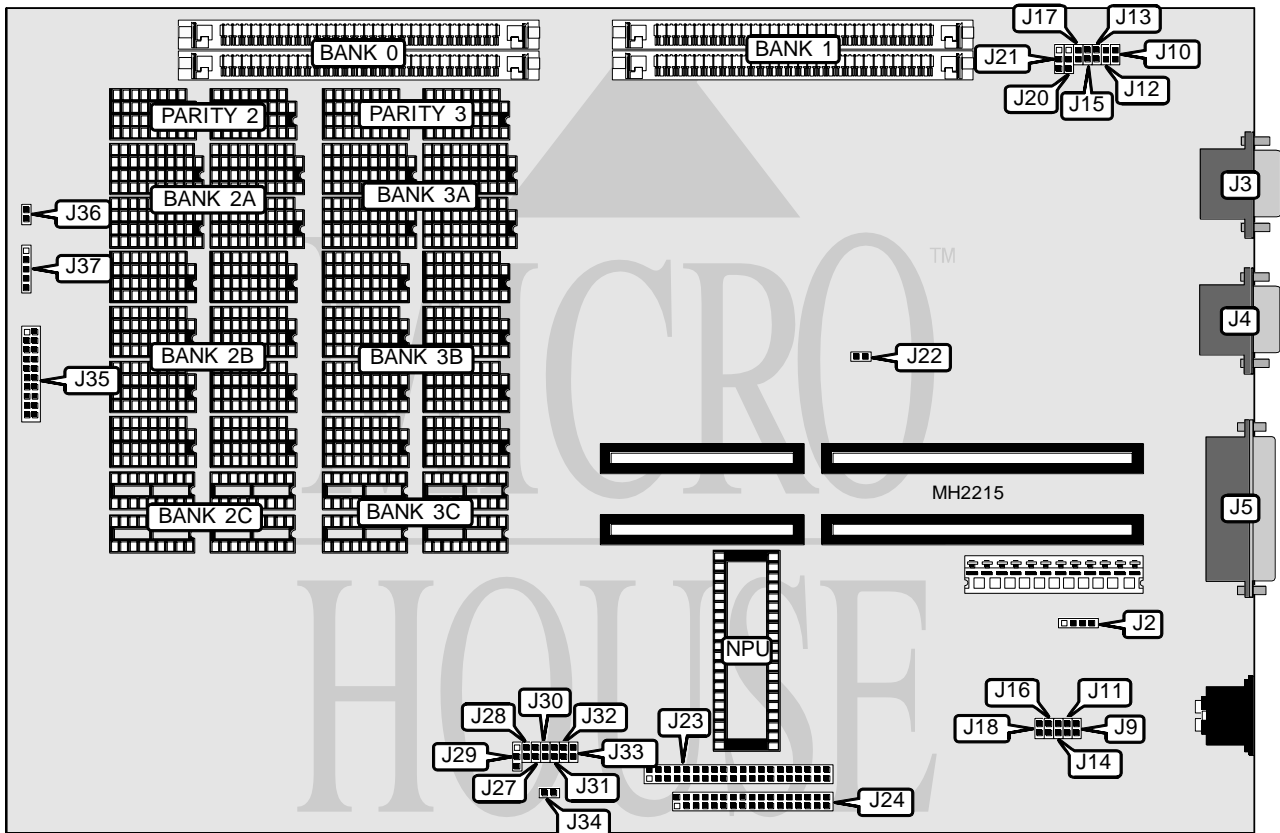


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2 1 3 S P

Processor	80286 (exact location unknown)
Processor Speed	12MHz
Chip Set	VLSI
Max. Onboard DRAM	4MB
Cache	None
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	Floppy drive interface, IDE interface, parallel port, serial ports (2)
NPU Options	80287



CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 2	J3	Floppy drive interface	J24
Serial port 1	J4	Front panel connector	J35
Parallel port	J5	External keyboard connector	J36
IDE interface	J23	IDE interface LED	J37

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í Factory configured - do not alter	J2	Open
í IDE interface enabled	J9	Closed
IDE interface disabled	J9	Open
í Serial port 1 enabled	J10	Closed
Serial port 1 disabled	J10	Open
í Floppy drive select dual speed	J11	Closed
Floppy drive select single speed	J11	Open
í COM port select COM1	J12	Closed
COM port select COM3	J12	Open
í Serial port 2 enabled	J13	Closed
Serial port 2 disabled	J13	Open
í IRQ6 enabled	J14	Closed
IRQ6 disabled	J14	Open
í COM port select COM2	J15	Closed
COM port select COM4	J15	Open
í DRQ2 enabled	J16	Closed
DRQ2 disabled	J16	Open
í Parallel port enabled	J17	Closed
Parallel port disabled	J17	Open
í Floppy drive interface enabled	J18	Closed
Floppy drive interface disabled	J18	Open
í Parallel port select LPT A	J20	pins 1 & 2 closed
Parallel port select LPT B	J20	pins 2 & 3 closed
í Parallel port IRQ select IRQ7	J21	pins 1 & 2 closed
Parallel port IRQ select IRQ5	J21	pins 2 & 3 closed
í BIOS type select 27512	J22	Closed
BIOS type select 27256	J22	Open
í Monitor type select color	J27	Closed
Monitor type select monochrome	J27	Open
í DRAM speed select 80ns	J29	pins 1 & 2 closed
DRAM speed select 70ns	J29	pins 2 & 3 closed
í RAM wait state 0 wait states	J32	Closed
RAM wait state 1 wait state	J32	Open
í ROM wait state 2 wait states	J33	Closed
ROM wait state 1 wait state	J33	Open
í XMEMR wait state 0 wait states	J34	Closed
XMEMR wait state 1 wait state	J34	Open

DRAM CONFIGURATION (SIMM)		
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9
4MB	(4) 1M x 9	NONE

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DRAM CONFIGURATION (DIP)						
Size	Bank 2A	Bank 2B	Bank 2C	Bank 3A	Bank 3B	Bank 3C
512KB	(4) 44256	NONE	NONE	NONE	NONE	NONE
640KB	(4) 44256	(8) 4164	NONE	NONE	(8) 4164	NONE
1MB	(4) 44256	NONE	NONE	(4) 44256	NONE	NONE
2MB	(4) 411000	(8) 411000	(4) 411000	NONE	NONE	NONE
4MB	(4) 411000	(8) 411000	(4) 411000	(4) 411000	(6) 411000	(4) 411000

DRAM CONFIGURATION (PARITY CHIP)		
Size	Parity 2	Parity 3
512KB	(2) 41256	NONE
640KB	(2) 41256	(2) 4164
1MB	(2) 41256	(2) 41256
2MB	(2) 411000	NONE
4MB	(2) 411000	(2) 411000

DRAM JUMPER CONFIGURATION			
Size	JP28	JP30	JP31
512KB	Closed	Closed	Closed
640KB	Open	Closed	Closed
1MB	Closed	Open	Closed
2MB	Open	Open	Closed
4MB	Closed	Closed	Open

MISCELLANEOUS TECHNICAL NOTE
Note: The location of the jumpers are for general reference. Actual jumper location may vary slightly.