#### SA-486 VL-BUS M.B. REV. V2C

CX486SX/80486SX/80487SX/AMD486DXLV/CX486DX/80486DX/80486DX2/ **Processor** 

80486DX4/Pentium Overdrive

20/25/33/40/50(internal)/50/66(internal)MHz **Processor Speed** 

Chip Set ALI Max. Onboard DRAM 128MB

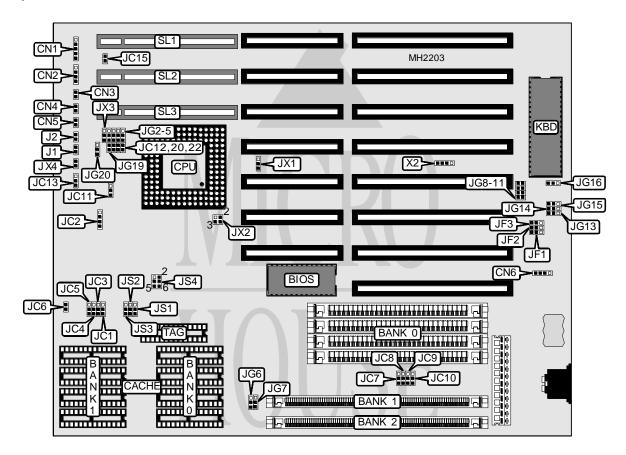
Cache 128/256/512/1024KB

**BIOS** Phoenix

**Dimensions** 254mm x 220mm

I/O Options 32-bit VESA local bus slots (3), green PC connector

**NPU Options** 



CONNECTIONS				
Purpose	Location	Purpose	Location	
Power LED & keylock	CN1	Green PC connector	JG7	
Speaker	CN2	Green PC connector	JG8	
Reset switch	CN3	Green PC connector	JG9	
Turbo switch	CN4	Green PC connector	JG10	
Turbo LED	CN5	Green PC connector	JG11	
External battery	CN6	32-bit VESA local bus slots	SL1 - SL3	
SMI connector	JG6			

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USER CONFIGURABLE SETTINGS			
Function	Jumper	Position	
í Turbo switch enabled	CN4	Closed	
Turbo switch disabled	CN4	Open	
í Factory configured - do not alter	JC12	Open	
í Factory configured - do not alter	JC15	Open	
í Factory configured - do not alter	JG16	Open	
í Factory configured - do not alter	X2	Open	

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	
1MB	(4) 256K x 9	NONE	NONE	
2MB	NONE	NONE	(1) 512K x 36	
2MB	NONE	(1) 512K x 36	NONE	
4MB	NONE	(1) 1M x 36	NONE	
4MB	(4) 1M x 9	NONE	NONE	
4MB	NONE	NONE	(1) 1M x 36	
5MB	(4) 256K x 9	(1) 1M x 36	NONE	
8MB	(4) 1M x 9	(1) 1M x 36	NONE	
8MB	NONE	(1) 2M x 36	NONE	
8MB	NONE	NONE	(1) 2M x 36	
16MB	(4) 4M x 9	NONE	NONE	
16MB	NONE	(1) 4M x 36	NONE	
16MB	NONE	NONE	(1) 4M x 36	
17MB	(4) 256K x 9	(1) 4M x 36	NONE	
20MB	(4) 4M x 9	(1) 1M x 36	NONE	
20MB	(4) 1M x 9	(1) 4M x 36	NONE	
20MB	NONE	(1) 1M x 36	(1) 4M x 36	
20MB	NONE	(1) 4M x 36	(1) 1M x 36	
32MB	NONE	(1) 8M x 36	NONE	
32MB	NONE	NONE	(1) 8M x 36	
32MB	(4) 4M x 9	(1) 4M x 36	NONE	
64MB	NONE	(1) 16M x 36	NONE	
64MB	NONE	NONE	(1) 16M x 36	
64MB	(4) 16M x 9	NONE	NONE	
128MB	NONE	(1) 16M x 36	(1) 16M x 36	

CACHE CONFIGURATION				
Size	Bank 0	Bank 1	TAG	
128KB	(4) 32K x 8	NONE	(1) 8K x 8	
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8	
256KB	(4) 64K x 8	NONE	(1) 32K x 8	
512KB	(4) 64K x 8	(4) 64K x 8	(1) 32K x 8	
512KB	(4) 128K x 8	NONE	(1) 64K x 8	
1MB	(4) 128K x 8	(4) 128K x 8	(1) 64K or 128K x 8	

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CACHE JUMPER CONFIGURATION				
Size	JS1	JS2	JS3	JS4
128KB	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 3 closed
256KB	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 4 closed
256KB	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 3 closed
512KB	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed	pins 3 & 5 closed
512KB	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed	pins 1 & 3 closed
1MB	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed	pins 4 & 6 closed

		CPU .	TYPE CONFIG	URATION			
Туре	JC1	JC2	JC3	JC4	JC5	JC6	JC20
CX486SX	Open	2 & 3	1 & 2	1 & 2	2 & 3	Open	Open
80486SX	Open	2 & 3	1 & 2	1 & 2	Open	Closed	Closed
80487SX	1 & 2	1 & 2, 3 & 4	1 & 2	1 & 2	Open	Closed	Open
CX486DX	2 & 3	1 & 2, 3 & 4	1 & 2	1 & 2	2 & 3	Closed	Open
AMD486DX	2 & 3	1 & 2, 3 & 4	1 & 2	1 & 2	Open	Closed	Open
80486DX/DX2	2 & 3	1 & 2, 3 & 4	1 & 2	1 & 2	Open	Closed	Closed
80486DX4	2 & 3	1 & 2, 3 & 4	1 & 2	1 & 2	Open	Closed	Closed
P4S/P24S	2 & 3	1 & 2, 3 & 4	1 & 2	1 & 2	Open	Closed	Closed
P23S	Open	2 & 3	1 & 2	1 & 2	Open	Closed	Open
P24T	1 & 2	1 & 2, 3 & 4	2 & 3	2 & 3	1 & 2	Closed	Closed
Note: Pins desig	nated should	be in the closed p	osition.				

		CPU T	YPE CONFIGUI	RATION (CON'T	Γ)		
Туре	JC22	JG2	JG3	JG4	JG5	JG19	JG20
CX486SX	Closed	1 & 2	1 & 2	1 & 2	1 & 2	Closed	1 & 2
80486SX	Open	1 & 2	1 & 2	1 & 2	1 & 2	Open	Open
80487SX	Open	1 & 2	1 & 2	1 & 2	1 & 2	Open	2 & 3
CX486DX	Closed	1 & 2	1 & 2	1 & 2	1 & 2	Closed	1 & 2
AMD486DX	Open	2 & 3	2 & 3	2 & 3	2 & 3	Open	Open
80486DX/DX2	Open	1 & 2	1 & 2	1 & 2	1 & 2	Open	Open
80486DX4	Open	2 & 3	2 & 3	2 & 3	Open	Open	2 & 3
P4S/P24S	Open	2 & 3	2 & 3	2 & 3	2 & 3	Open	2 & 3
P23S	Open	2 & 3	2 & 3	2 & 3	2 & 3	Open	2 & 3
P24T	Open	2 & 3	2 & 3	2 & 3	2 & 3	Open	Open
Note: Pins desig	nated should	be in the close	d position.				

CPU TYPE CONFIGURATION (CON'T)				
Туре	JC7	JC8	JC9	
CX486SX	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed	
CX486DX	pins 1 & 2 closed	pins 1 & 2 closed	pins 2 & 3 closed	
80486DX/DX2/DX4	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	
Pentium Overdrive	pins 1 & 2 closed	pins 2 & 3 closed	pins 1 & 2 closed	

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	СР	U SPEED CONFIGURATION	ON	
Speed	JF1	JF2	JF3	JG13
20MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed
25MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed	Open
33MHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed	Open
40MHz	pins 1 & 2 closed	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed
50iMHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed	Open
50MHz	pins 2 & 3 closed	pins 1 & 2 closed	pins 2 & 3 closed	Open
66iMHz	pins 2 & 3 closed	pins 2 & 3 closed	pins 1 & 2 closed	Open

CPU SPEED CONFIGURATION (CON'T)					
Speed	JG14	JG15	JC10	JC11	JC13
20MHz	Open	Open	1 & 2	1 & 2	1 & 2
25MHz	1 & 2	Open	1 & 2	1 & 2	1 & 2
33MHz	Open	1 & 2	1 & 2	1 & 2	1 & 2
40MHz	Open	Open	2 & 3	2 & 3	2 & 3
50iMHz	1 & 2	Open	1 & 2	1 & 2	1 & 2
50MHz	1 & 2	Open	2 & 3	2 & 3	2 & 3
66iMHz	Open	1 & 2	1 & 2	1 & 2	1 & 2
Note: Pins designated should be in the closed position.					

CPU SPEED CONFIGURATION (DX4)				
Speed	JX3	JX4		
2x	pins 2 & 3 closed	Open		
2.5x	Open	Closed		
3x	pins 1 & 2 closed	Open		

Voltage	JX1	JX2
3.3v	pins 2 & 3 closed	Open
3.45v	pins 1 & 2 closed	Open
5v	Open	pins 1 & 2, 3 & 4 closed

BUS SPEED CONFIGURATION	
CPU speed	J1
<=33MHz	Open
>33MHz	Closed

VESA WAIT STATE CONFIGURATION	
Wait states	J2
0 wait states	Open
1 wait state	Closed