ILON USA, INC.
M - 3 9 6 B

Processor　　　　　　　80386SX
Processor Speed　　　　16/20/25/33MHz
Chip Set　　　　　　　　PC Chip
Max. Onboard DRAM　　16MB
Cache　　　　　　　　　None
BIOS　　　　　　　　　　AMI
Dimensions　　　　　　254mm x 220mm
I/O Options　　　　　　None
NPU Options　　　　　　80387SX
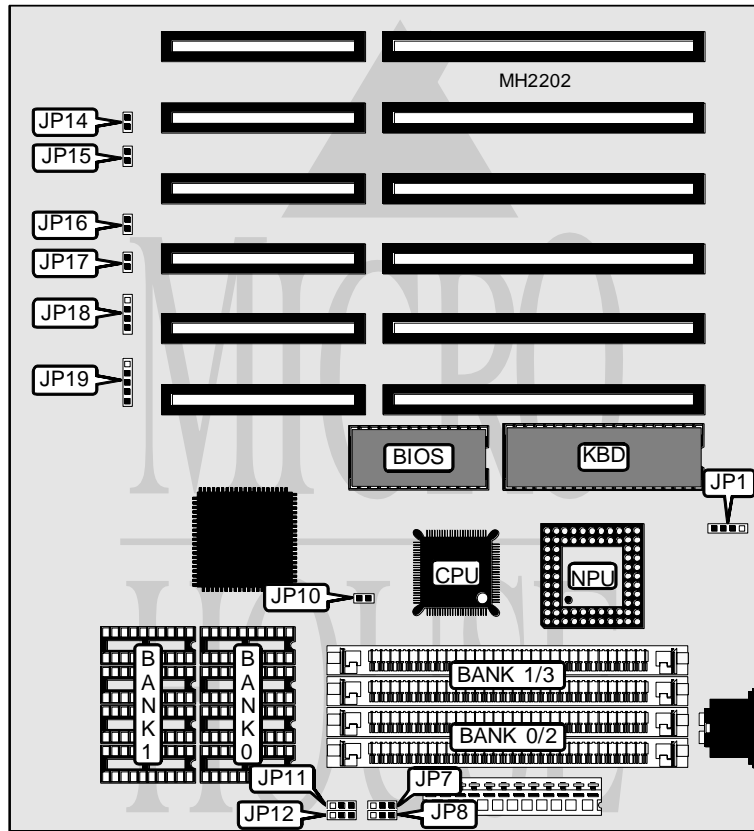
CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	JP1	Turbo switch	JP17
Reset switch	JP15	Speaker	JP18
Turbo LED	JP16	Power LED & keylock	JP19

Continued on next page. . .

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
í CMOS memory normal operation	JP1	pins 2 & 3 closed
CMOS memory clear	JP1	pins 3 & 4 closed
External battery installed	JP1	Closed
í Parity check disabled	JP10	Closed
Parity check enabled	JP10	Open
í Monitor type select monochrome	JP14	Closed
Monitor type select color	JP14	Open

DRAM CONFIGURATION (DIP ONLY INSTALLED)		
Size	Bank 0	Bank 1
512KB	(4) 44256	NONE
1MB	(4) 44256	(4) 44256
2MB	(4) 441000	NONE
4MB	(4) 441000	(4) 441000

DRAM CONFIGURATION (SIMM ONLY INSTALLED)		
Size	Bank 0	Bank 1
512KB	(2) 256K x 9	NONE
1MB	(2) 256K x 9	(2) 256K x 9
2MB	(2) 1M x 9	NONE
4MB	(2) 1M x 9	(2) 1M x 9
8MB	(2) 4M x 9	NONE
16MB	(2) 4M x 9	(2) 4M x 9

DRAM CONFIGURATION (DIP & SIMM INSTALLED)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
2MB	(4) 44256	(4) 44256	(2) 256K x 9	(2) 256K x 9
8MB	(4) 441000	(4) 441000	(2) 1M x 9	(2) 1M x 9

DRAM JUMPER CONFIGURATION				
Type	JP7	JP8	JP11	JP12
DIP only	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed
SIMM only	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed	pins 1 & 2 closed
DIP & SIMM	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed	pins 2 & 3 closed