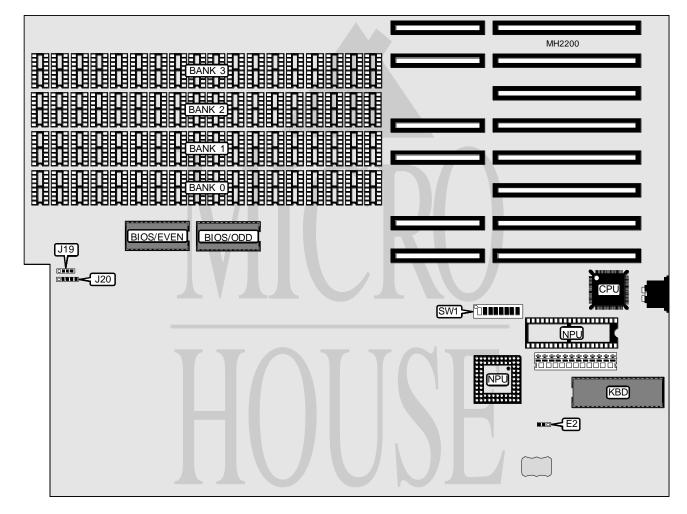
ADVANCED LOGIC RESEARCH, INC. 386/220 REV. C

- Processor Processor Speed Chip Set Max. Onboard DRAM Cache BIOS Dimensions I/O Options NPU Options
- 80386SX 20MHz ALR 12MB (8MB on external memory card) None Phoenix 330mm x 218mm External memory card 80287/80387SX



CONNECTIONS					
Purpose	Location	Purpose	Location		
Speaker	J19	Power LED & keylock	J20		

Continued on next page. . .

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USER CONFIGURABLE SETTINGS				
Function	Jumper/Switch	Position		
í Factory configured - do not alter	E2	Open		
í Monitor type select CGA/EGA/VGA	SW1/1	On		
Monitor type select monochrome	SW1/1	Off		
í Factory configured - do not alter	SW1/2	Off		
í Factory configured - do not alter	SW1/3	Off		
í Memory size select 1MB	SW1/4	Off		
Memory size select 2MB	SW1/4	On		
í ROM to RAM enabled	SW1/5	On		
ROM to RAM disabled	SW1/5	Off		
í Speed select low	SW1/8	On		
Speed select high	SW1/8	Off		

DRAM CONFIGURATION					
Size	Bank 0	Bank 1	Bank 2	Bank 3	
1MB	(18) 44256	NONE	NONE	NONE	
2MB	(18) 44256	(18) 44256	NONE	NONE	
3MB	(18) 44256	(18) 44256	(18) 44256	NONE	
4MB	(18) 44256	(18) 44256	(18) 44256	(18) 44256	

NUMERIC COPROCESSOR CONFIGURATION					
Туре	SW1/6	SW1/7			
Not installed	Off	On			
80287	On	Off			
80387	Off	Off			