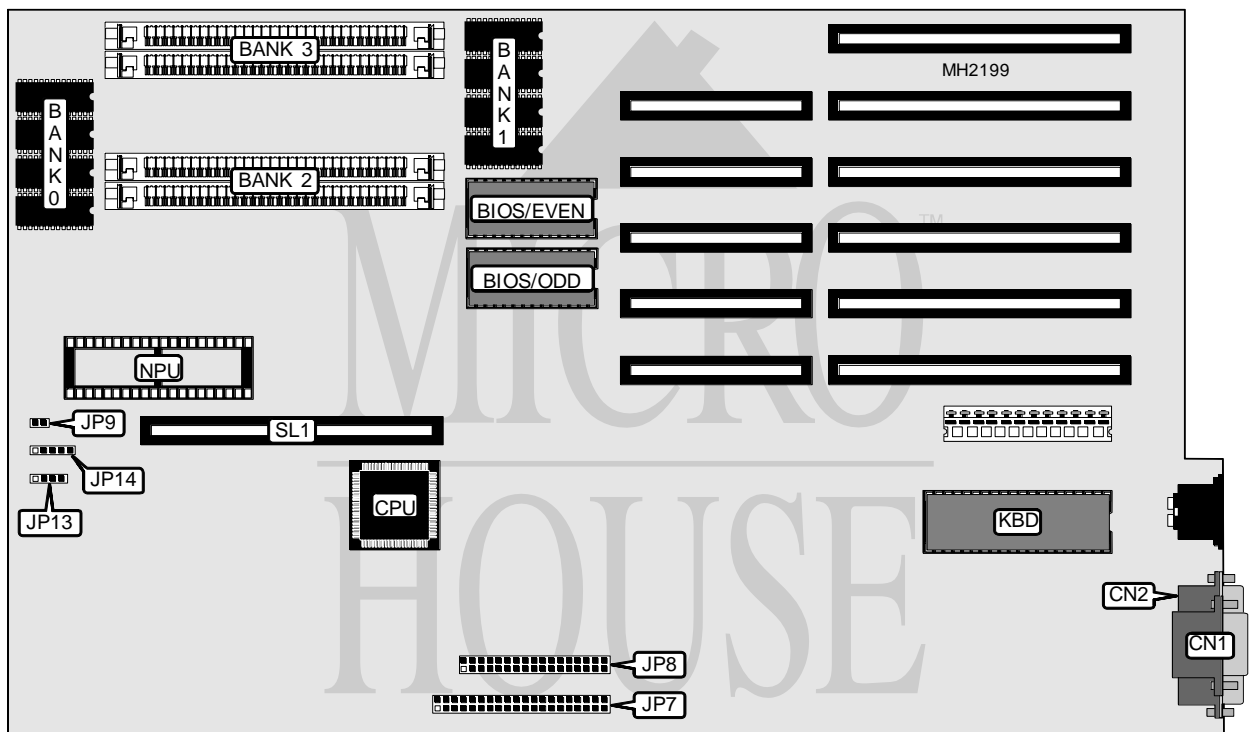


ADVANCED LOGIC RESEARCH, INC. POWERFLEX 386SX

Processor	80286/80386SX
Processor Speed	16MHz
Chip Set	ALR
Max. Onboard DRAM	5MB
Cache	None
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	CPU expansion slot, IDE interface, parallel port, serial port
NPU Options	80287/80387SX



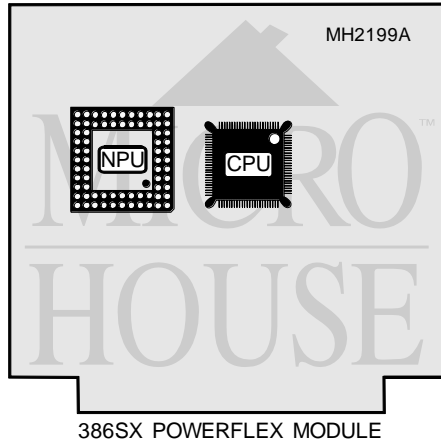
CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port	CN1	IDE interface LED	JP9
Parallel port	CN2	Power LED & keylock	JP13
IDE interface	JP7	Speaker	JP14
Floppy drive interface	JP8	CPU expansion slot	SL1

Continued on next page. . .

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POWERFLEX 386SX

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DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 128K x 8	(4) 128K x 8	NONE	NONE
3MB	(4) 128K x 8	(4) 128K x 8	(2) 1M x 9	NONE
5MB	(4) 128K x 8	(4) 128K x 8	(2) 1M x 9	(2) 1M x 9
Note: Banks 0 & 1 are factory installed and are not configurable				



386SX POWERFLEX MODULE

MISCELLANEOUS TECHNICAL NOTE
 Note: If the 386SX PowerFlex module is installed, remove the 80287 coprocessor.